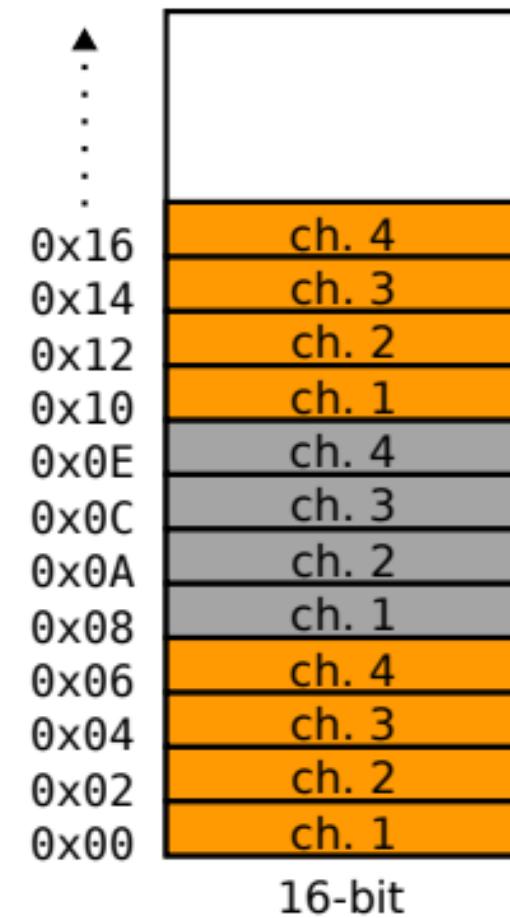


Samples write (from ADC core)



A diagram illustrating a 16-bit register structure. On the right, there is a vertical stack of three orange-colored boxes. The top box is labeled "ch. 3", the middle box is labeled "ch. 2", and the bottom box is labeled "ch. 1". To the left of these boxes, the binary values "0x04", "0x02", and "0x00" are aligned vertically above them. A thick black arrow points from the left towards the boxes, indicating the direction of data flow or address assignment.

DDR memory



Samples read (from host)

