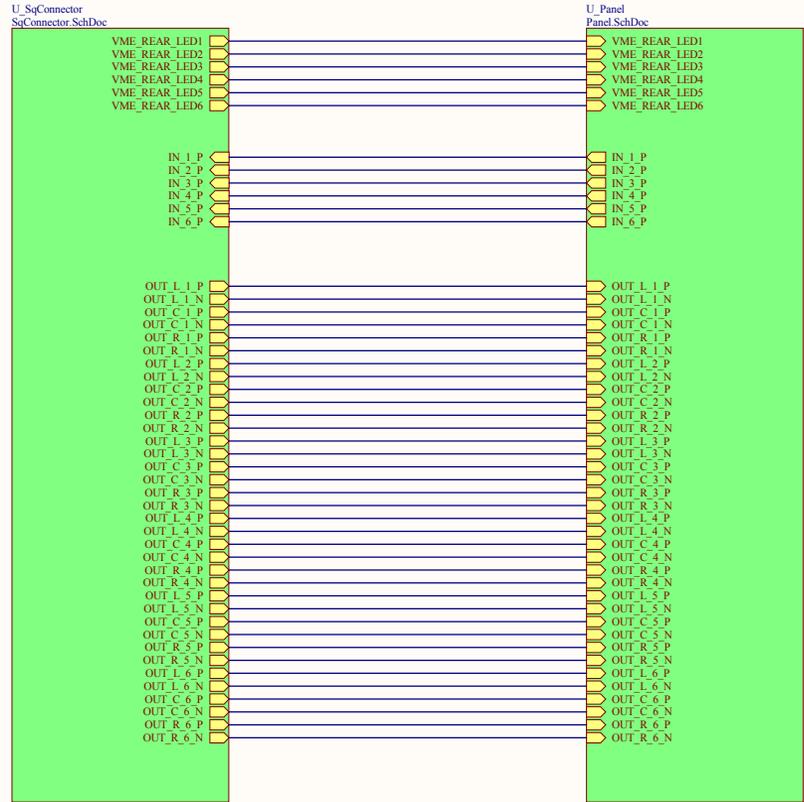


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Silkscreen on top  
 "Licensed under CERN OHL v1.1  
<http://ohwr.org/CERNOHL>"  
 More silkscreen on top:  
 "http://www.ohwr.org/projects/conv-ttl-blo"

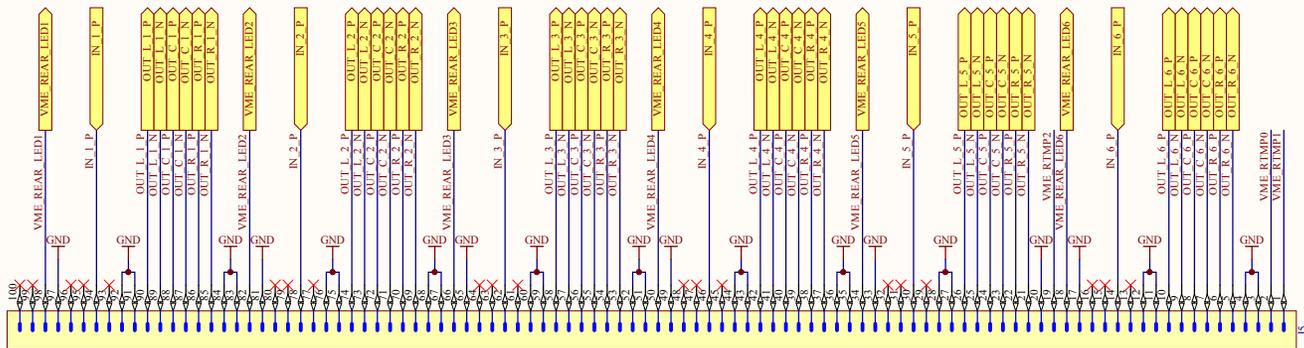
No CERN logo on silkscreen



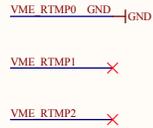
Project/Equipment		Standard Blocking Pulse Repeater	
Document	<b>RTM Piggyback TOP</b>		
	Designer	Carlos Gil Soriano	09/01/2012
	Drawn by	Carlos Gil Soriano	CheckedDate
	Check by	*	10/02/2012
	Last Mod.	Carlos Gil Soriano	
	File	RTMP_top.SchDoc	
Print Date	10/02/2012 11:46:07	Sheet	1 of 4
EDA Number			A3

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Port and nets to allow nets to be properly seen in PCB editor



Rear Transition Module  
 Piggyback Blocking v1  
 RTMP0 GND  
 RTMP1 Open  
 RTMP2 Open

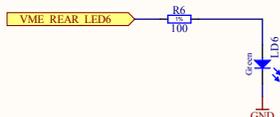
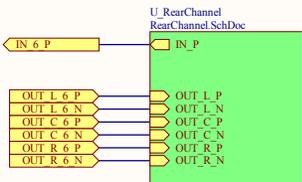
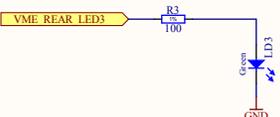
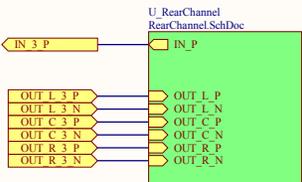
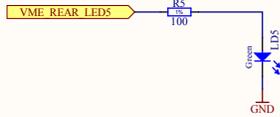
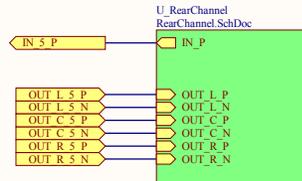
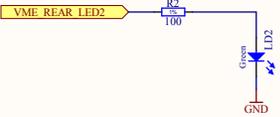
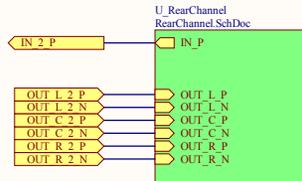
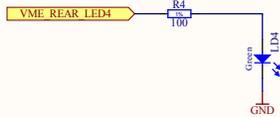
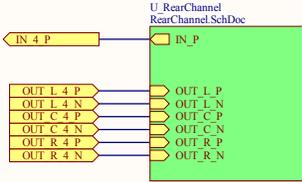
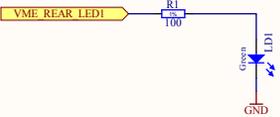
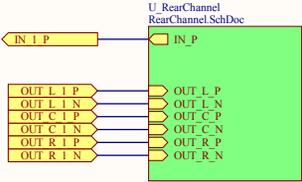


Project/Equipment		Standard Blocking Pulse Repeater	
Document		Designer Carlos Gil Soriano	
BE-CO		Drawn by Carlos Gil Soriano 09/01/2012	
CERN		Check by * CheckedDate	
		Last Mod. Carlos Gil Soriano 10/02/2012	
		File SqConnector_SchDoc	
		Print Date 10/02/2012 11:46:07	
		Sheet 2 of 4	
		EDA Number	
		A3	

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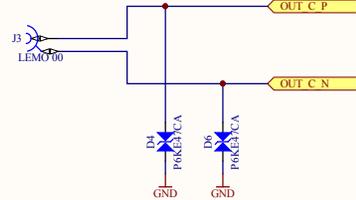
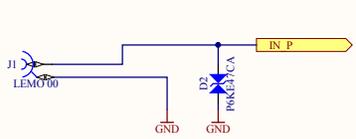
Changer tout le LEDES par DIALIGHT  
 561-2201-060F  
 CONV-TTL-BLO et CONV-TTL-RS485

Silkscreen on opposite  
 layer to 100 pin  
 connector:  
 Channel 1  
 Channel 2  
 Channel 3  
 Channel 4  
 Channel 5  
 Channel 6

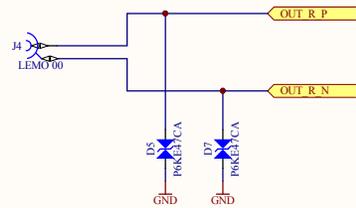
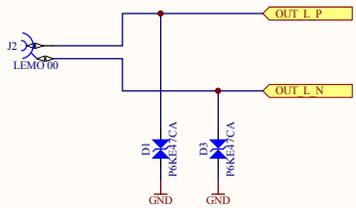


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The TVS provides protection against lightning and discharge.  
 If the voltage rises above 75V in only one rail, the optoisolator is able to  
 be driven at the current transformed by this 75V clamped voltage.



LEMOs on opposite layer  
 to 100 pin connector:  
 Channel 1  
 Channel 2  
 Channel 3  
 Channel 4  
 Channel 5  
 Channel 6



Project/Equipment		Standard Blocking Pulse Repeater	
Document	 <b>RTM Piggyback</b> <b>LEMO Connectors</b>	Designer	Carlos Gil Soriano
		Drawn by	Carlos Gil Soriano
		Check by	*
		Last Mod.	Carlos Gil Soriano
		File	RearChannel_SchDoc
	Print Date	10/02/2012	11:46:07
		Sheet	4 of 4
		EDA Number	A3