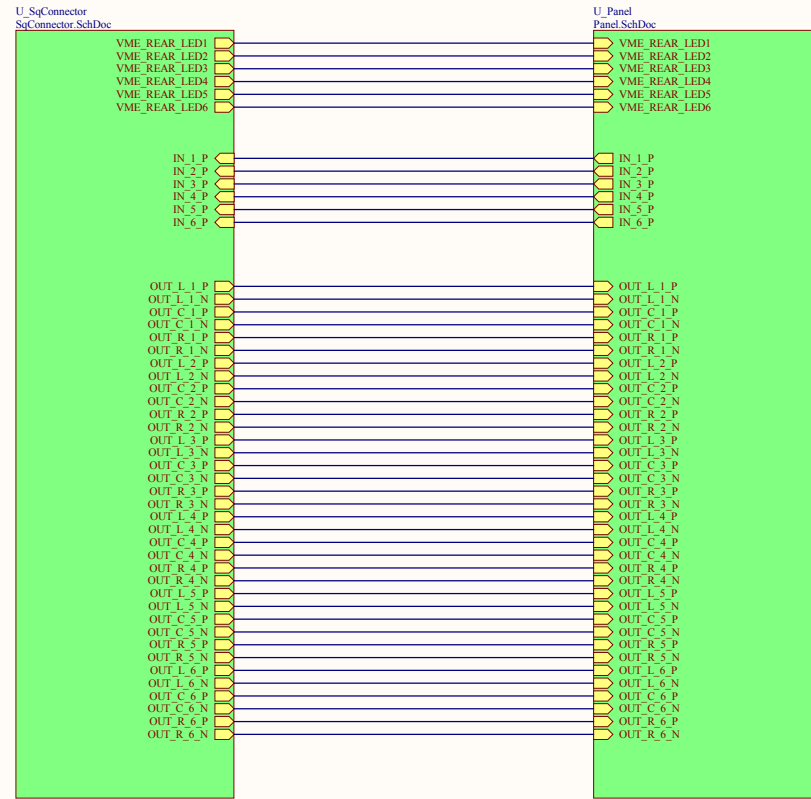
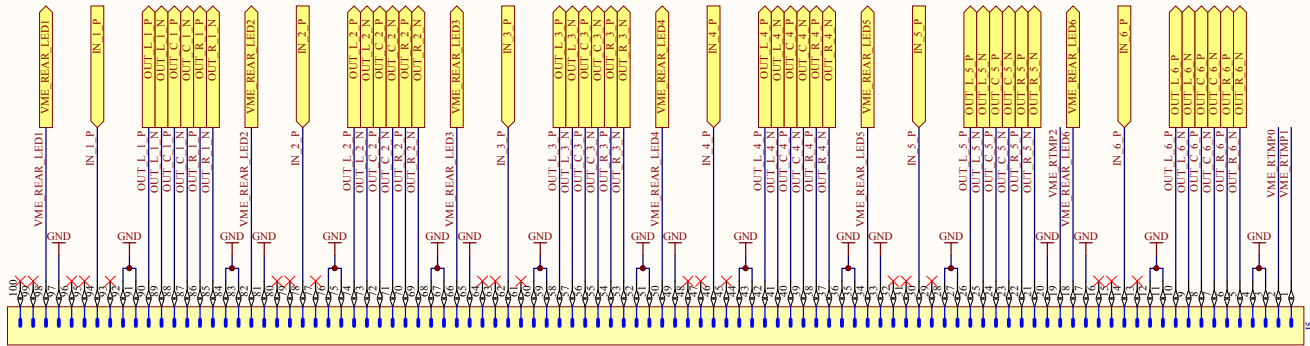


Silkscreen on top
 "Licensed under CERN OHL v1.1
<http://ohwr.org/CERNOHL>"
 More silkscreen on top:
["http://www.ohwr.org/projects/conv-ttl-blo"](http://www.ohwr.org/projects/conv-ttl-blo)

▲ No CERN logo on silkscreen



Port and nets to allow nets to be properly seen in PCB editor



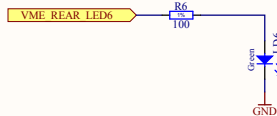
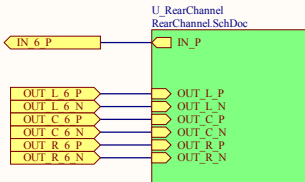
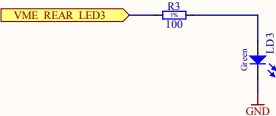
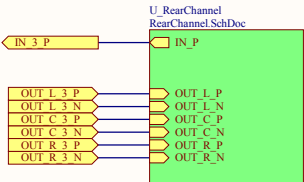
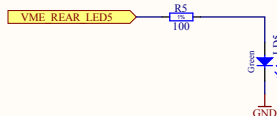
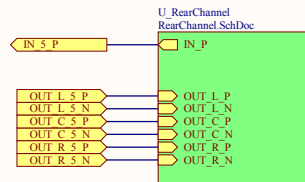
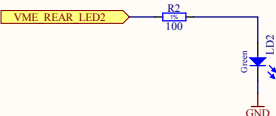
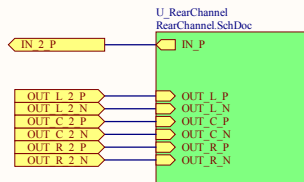
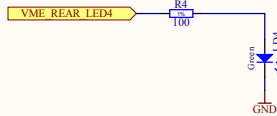
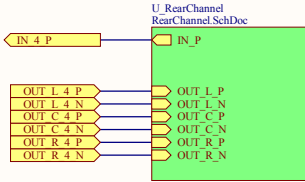
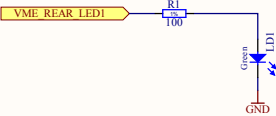
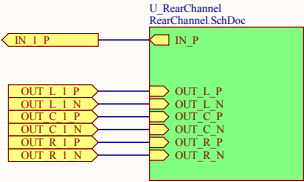
Rear Transition Module
Piggyback Blocking v1
RTMP0 GND
RTMP1 Open
RTMP2 Open



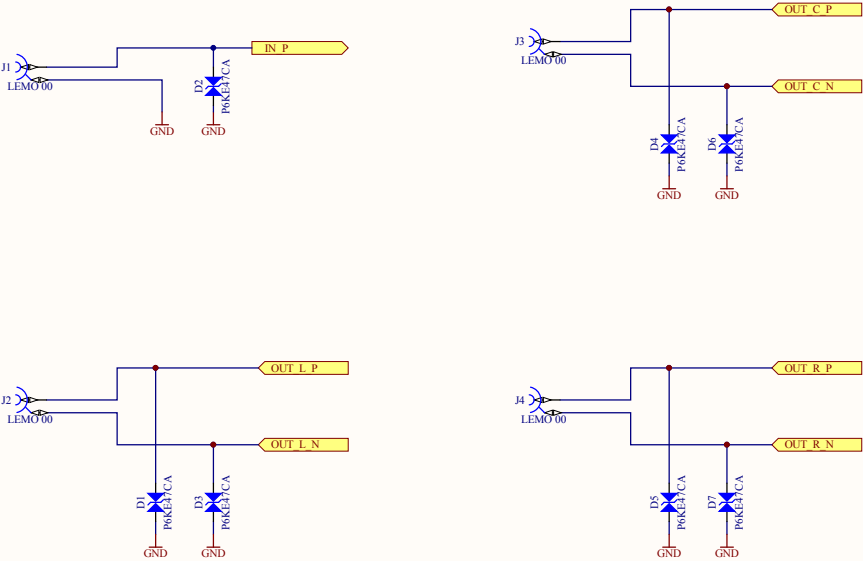
Silkscreen on opposite
layer to 100 pin
connector:

Channel 1
Channel 2
Channel 3
Channel 4
Channel 5
Channel 6

Changer tout le LEDS par DIALIGHT
561-2201-060F
CONV-TTL-BLO et CONV-TTL-RS485



The TVS provides protection against lightning and discharge.
If the voltage rises above 75V in only one rail, the optoisolator is able to
be driven at the current transformed by this 75V clamped voltage.



LEMOs on opposite layer
to 100 pin connector:
Channel 1
Channel 2
Channel 3
Channel 4
Channel 5
Channel 6