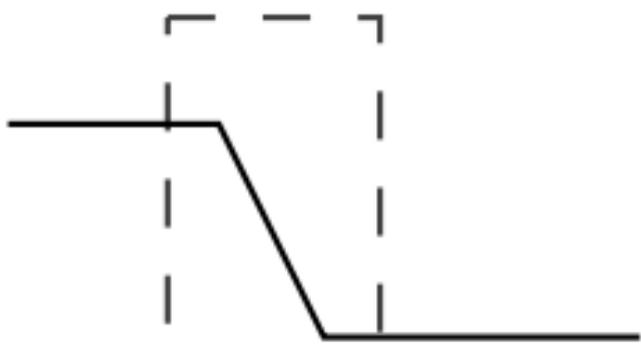


Start (S) or repeated start (Sr)
condition

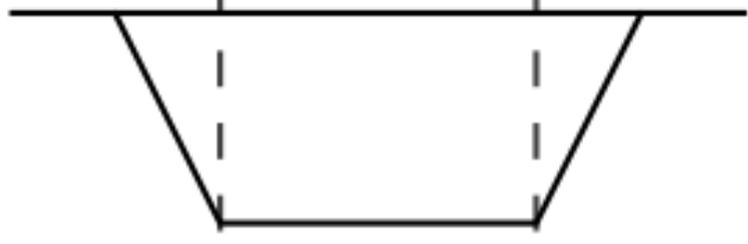
SDA



SCL



Valid bit transfer



Stop condition (P)

