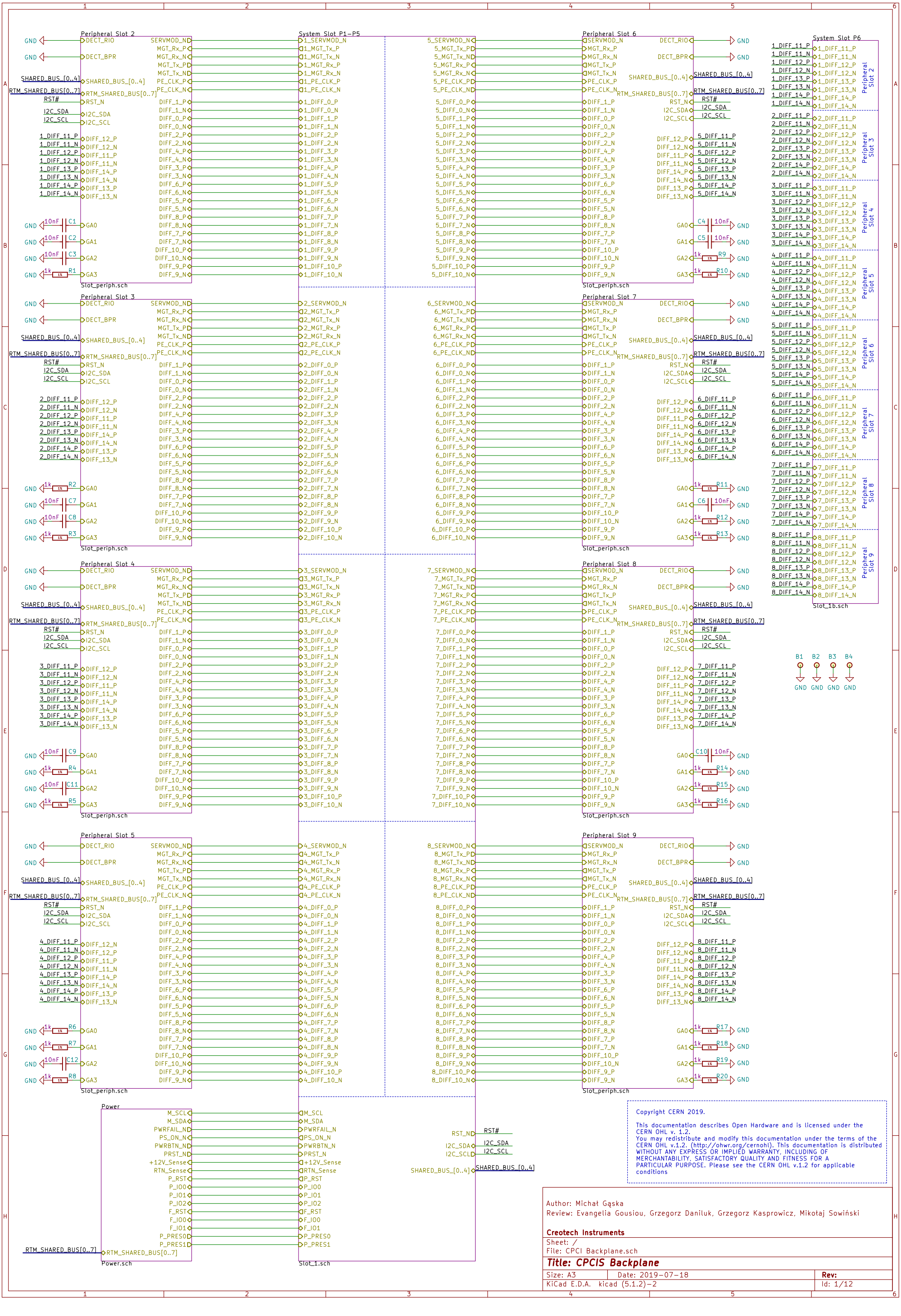
Peripheral Slot 2
Slot_periph.sch
System Slot P1–P5
Slot_1.sch
Peripheral Slot 6
Slot_periph.sch
System Slot P6
Slot_1b.sch
Peripheral Slot 3
Slot_periph.sch
Peripheral Slot 7
Slot_periph.sch
Peripheral Slot 4
Slot_periph.sch
Peripheral Slot 8
Slot_periph.sch
Peripheral Slot 5
Slot_periph.sch
Peripheral Slot 9
Slot_periph.sch
Power
Power.sch
Peripheral Slot 2
Peripheral Slot 3
Peripheral Slot 4
Peripheral Slot 5
Peripheral Slot 6
Peripheral Slot 7
Peripheral Slot 8
Peripheral Slot 9
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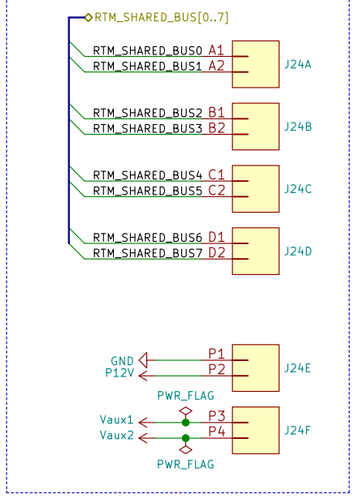
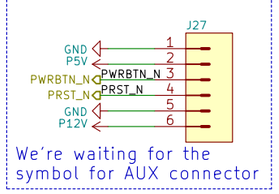
Author: Michał Gąska
Review: Evangelia Gousiou, Grzegorz Daniluk, Grzegorz Kasprowicz, Mikołaj Sowiński
Creotech Instruments
Sheet: /
File: CPCI Backplane.sch
Title: CPCIS Backplane
Size: A3
Date: 2019-07-18
Rev:
KiCad E.D.A. kicad (5.1.2)-2
Id: 1/12

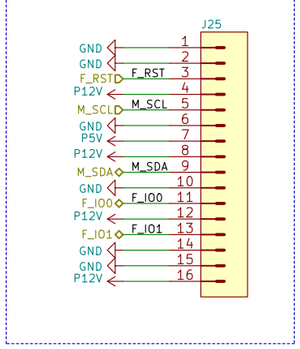
Power AUX slot



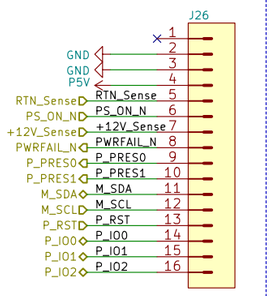
AUX



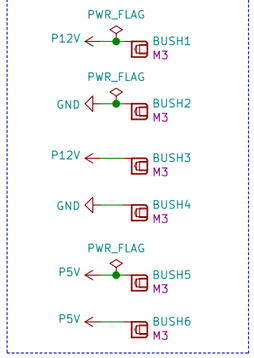
FANTRAY



Utility(PSU)



Power bushing connectors



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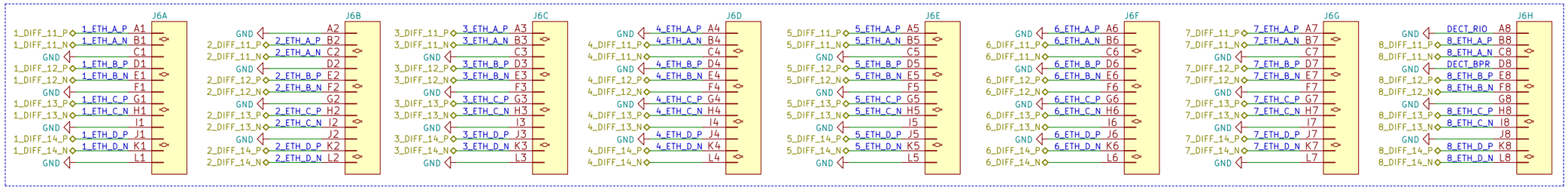
Author: Michał Gąska
Review: Evangelia Gousiou, Grzegorz Daniluk, Grzegorz Kasprowicz, Mikołaj Sowiński

Creotech Instruments

Sheet: /Power/		
File: Power.sch		
Title: CPCIS Backplane		
Size: A3	Date: 2019-07-18	**Rev:**
KiCad E.D.A. kicad (5.1.2)-2		Id: 2/12

P6



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Creotech Instruments

Sheet: /System Slot P6/
File: Slot_1b.sch

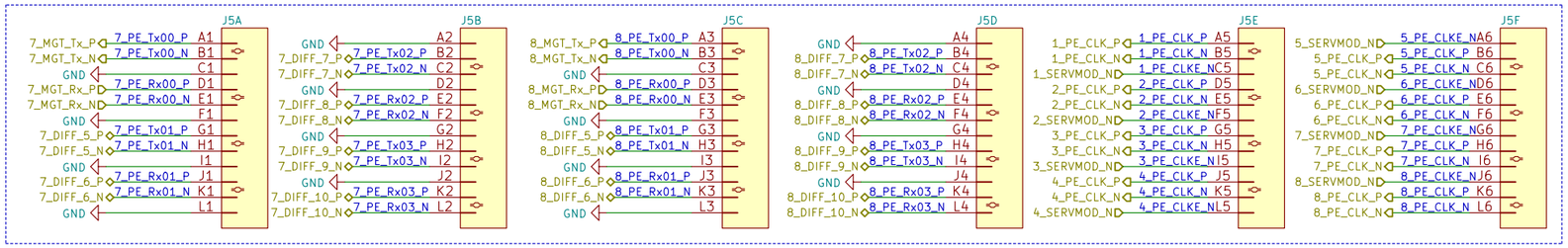
Title: CPCIS Backplane

Size: A3	Date: 2019-07-18	**Rev:**
KiCad E.D.A. kicad (5.1.2)-2		Id: 3/12

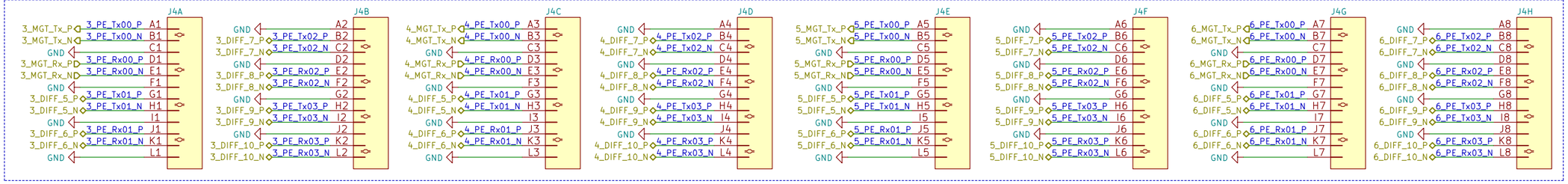
Guide element needed. We're waiting for the symbol.

All n_MGT_Tx/n_MGT_Rx signals mast have controlled differential impedance of 100 Ohm

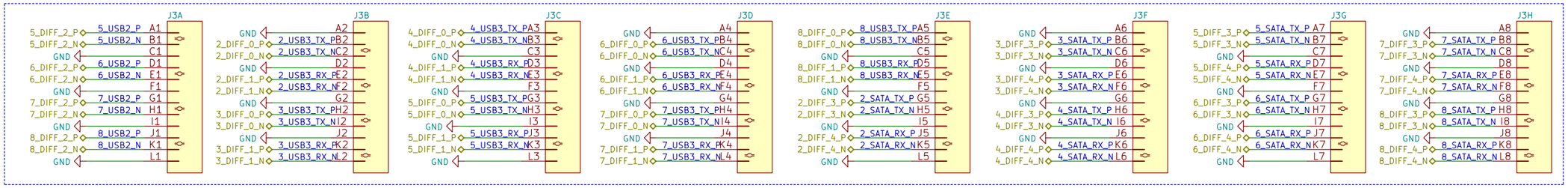
P5



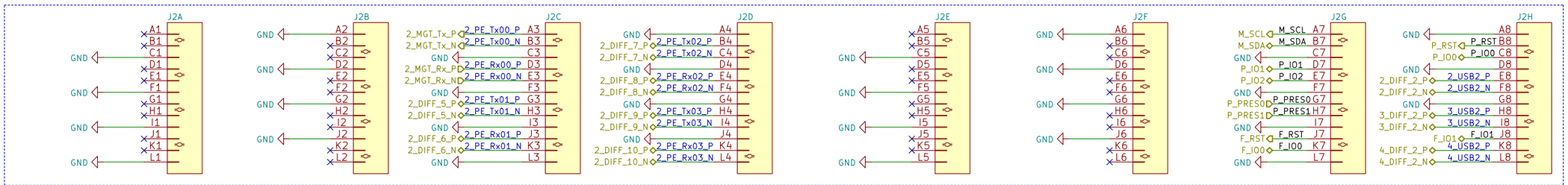
P4



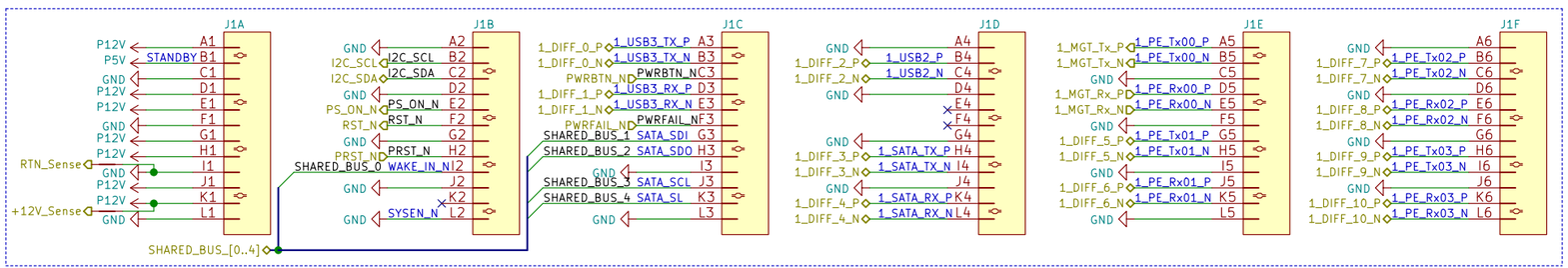
P3



P2



P1



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Review: Evangelia Gousiou, Grzegorz Daniluk, Grzegorz Kasprowicz, Mikołaj Sowiński

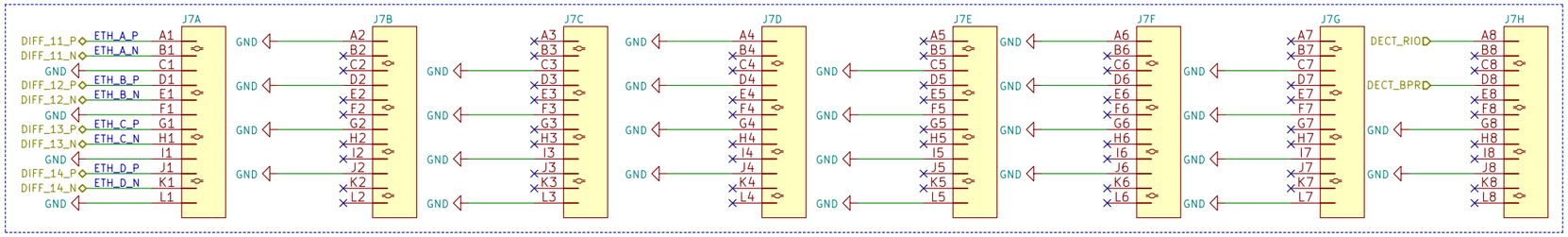
Creotech Instruments

Sheet: /System Slot P1-P5/
File: Slot_1.sch

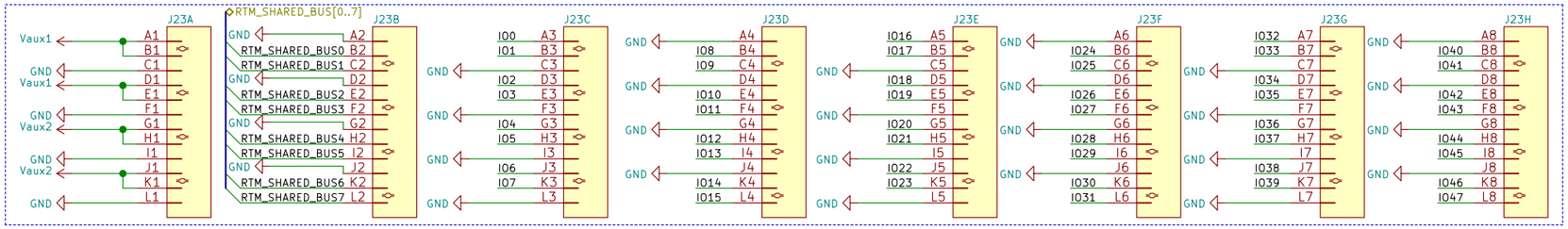
Title: CPCIS Backplane

Size: A3	Date: 2019-07-18	**Rev:**
KiCad E.D.A. kicad (5.1.2)-2		Id: 4/12

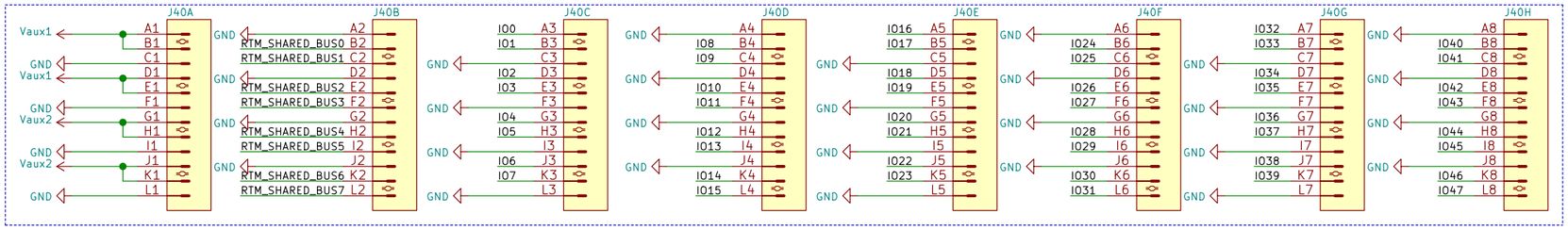
P6



P4

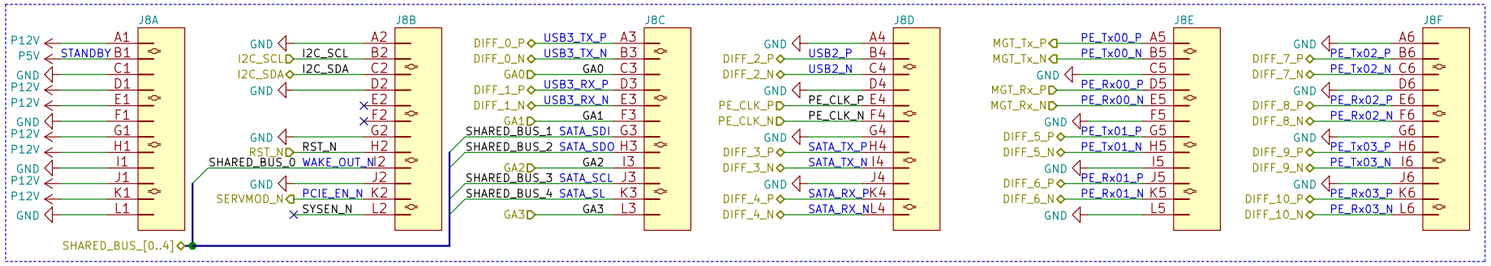


RTM connector



P1

All MGT_Tx/MGT_Rx signals mast have controlled differential impedance of 100 Ohm



CPCI-S.0 REQ 4.73 Backplane System Slot Identification 2
The SYSEN_N pin shall be connected to GND at the system slot and left open on peripheral slots.

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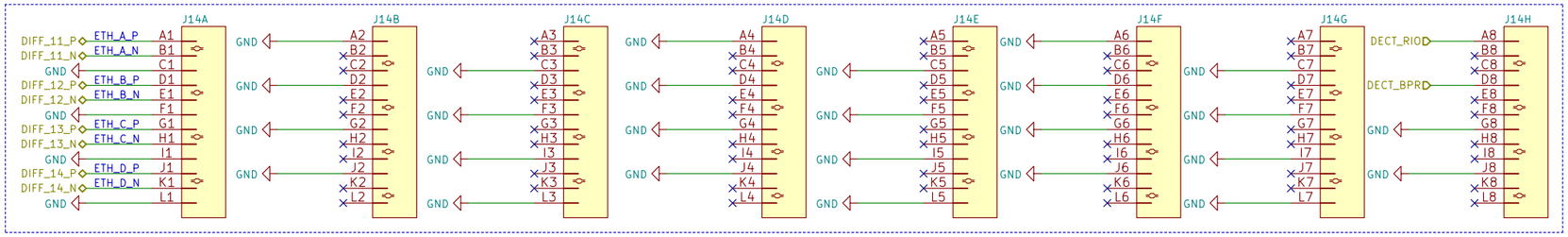
Creotech Instruments

Sheet: /Peripheral Slot 2/
File: Slot_periph.sch

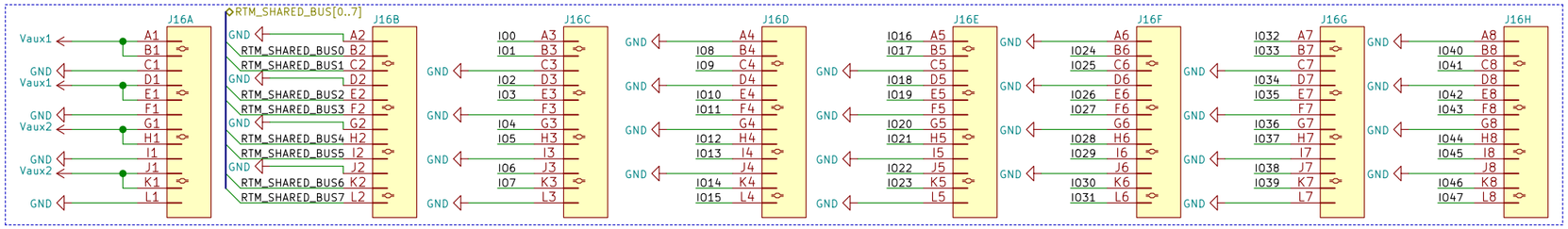
Title: CPCIS Backplane

Size: A3	Date: 2019-07-18	**Rev:**
KiCad E.D.A. kicad (5.1.2)-2		Id: 5/12

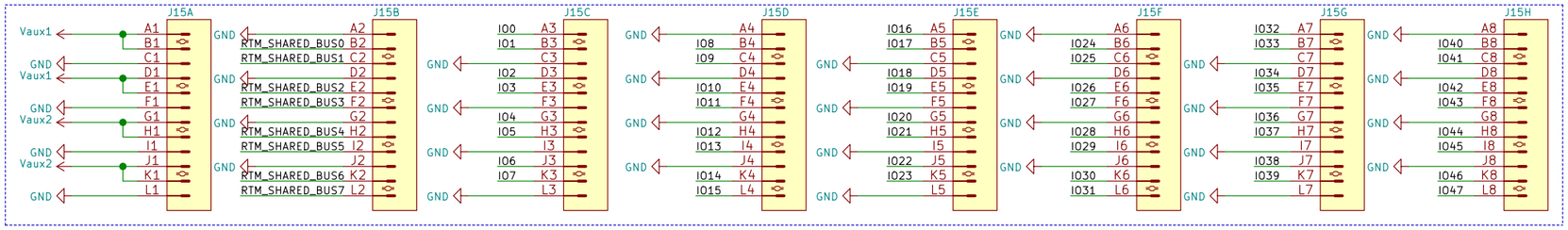
P6



P4

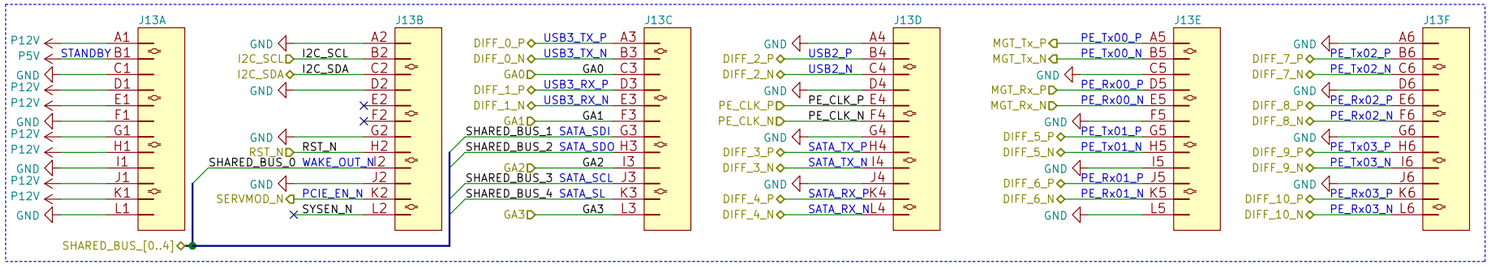


RTM connector



P1

All MGT_Tx/MGT_Rx signals mast have controlled differential impedance of 100 Ohm



CPCI-S.0 REQ 4.73 Backplane System Slot Identification 2
The SYSEN_N pin shall be connected to GND at the system slot and left open on peripheral slots.

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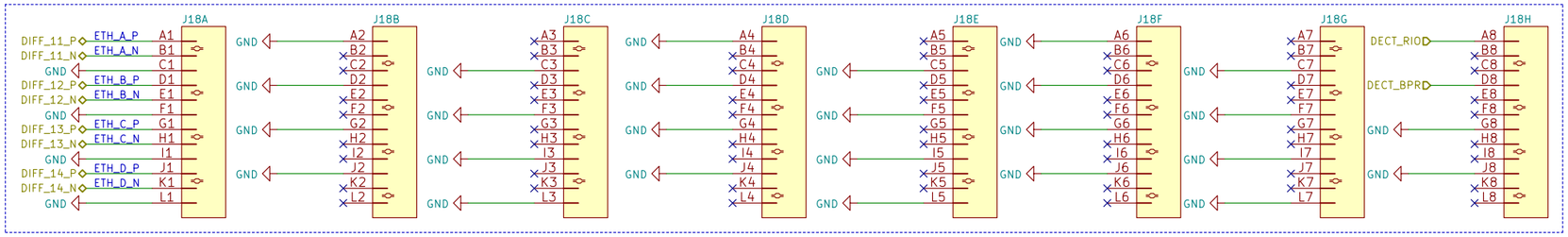
Creotech Instruments

Sheet: /Peripheral Slot 4/
File: Slot_periph.sch

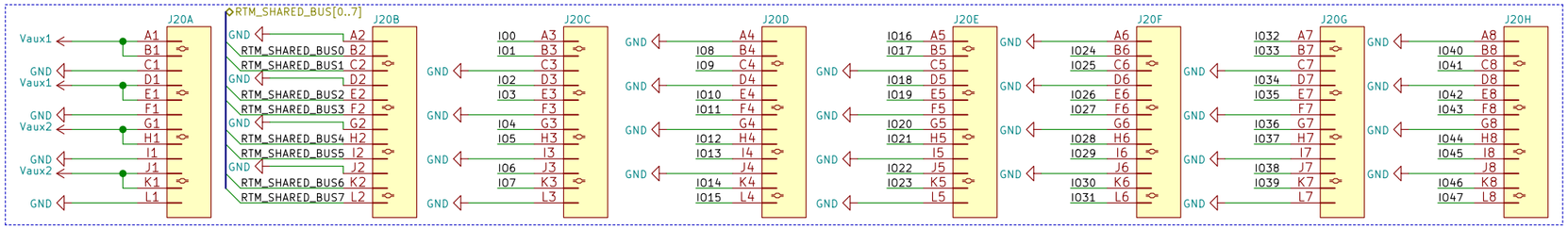
Title: CPCIS Backplane

Size: A3	Date: 2019-07-18	**Rev:**
KiCad E.D.A. kicad (5.1.2)-2		Id: 6/12

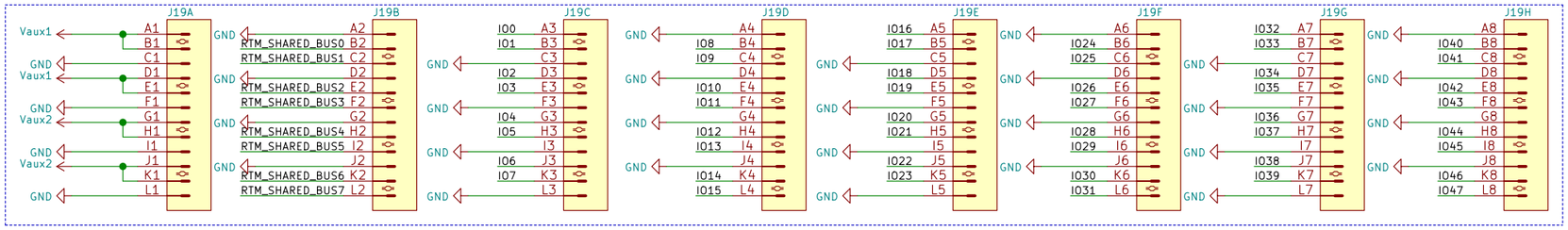
P6



P4

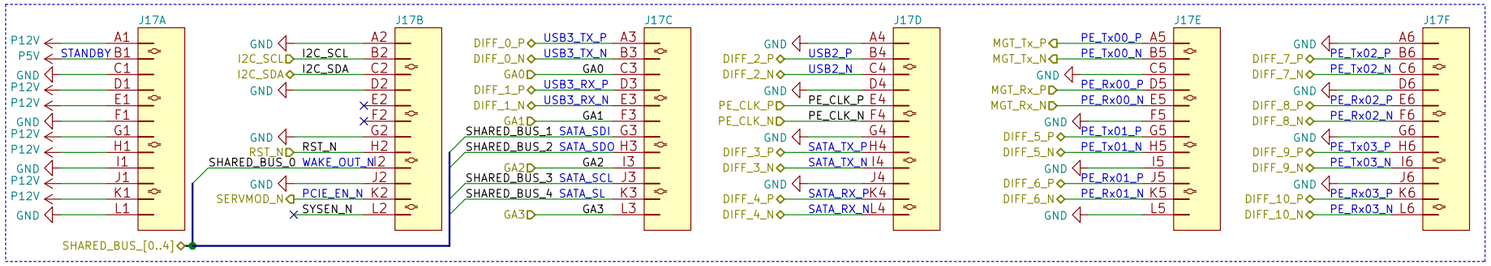


RTM connector



P1

All MGT_Tx/MGT_Rx signals mast have controlled differential impedance of 100 Ohm



CPCI-S.0 REQ 4.73 Backplane System Slot Identification 2
The SYSEN_N pin shall be connected to GND at the system slot and left open on peripheral slots.

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Author: Michał Gąska
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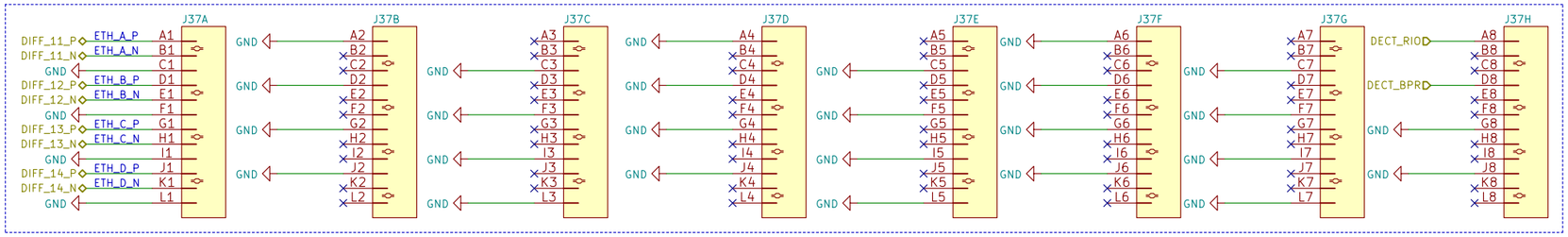
Creotech Instruments

Sheet: /Peripheral Slot 5/
File: Slot_periph.sch

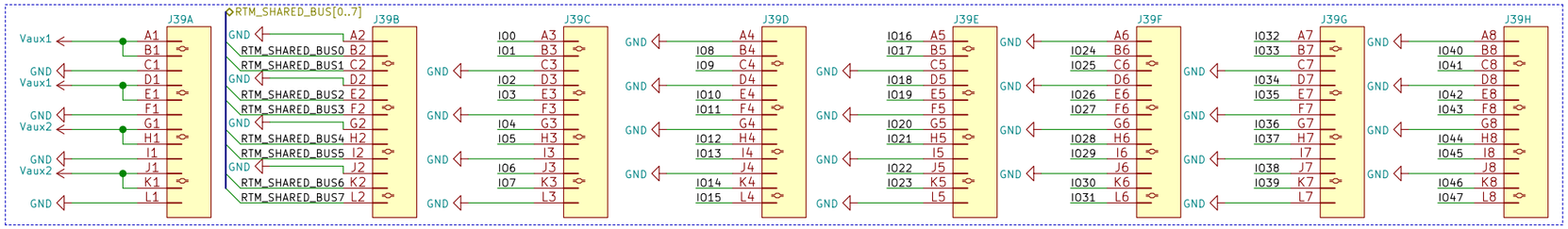
Title: CPCIS Backplane

Size: A3	Date: 2019-07-18	**Rev:**
KiCad E.D.A. kicad (5.1.2)-2		Id: 7/12

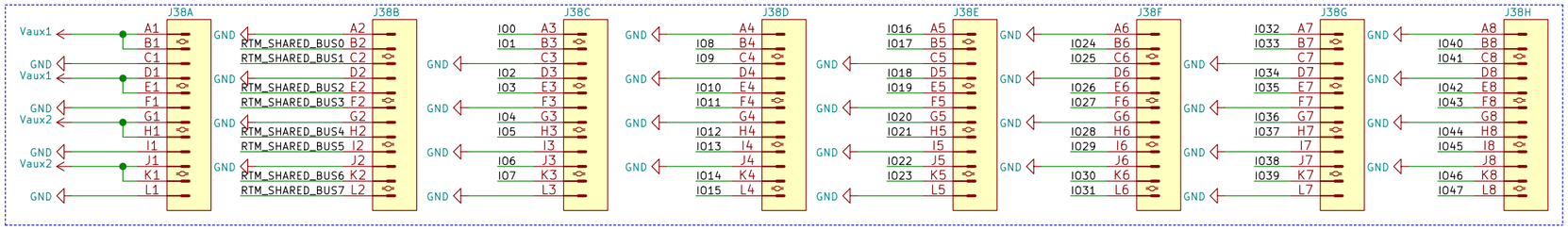
P6



P4

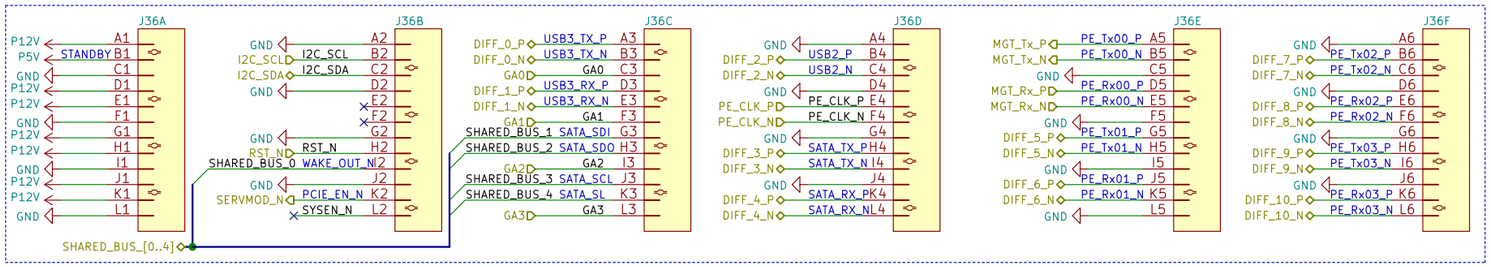


RTM connector



P1

All MGT_Tx/MGT_Rx signals mast have controlled differential impedance of 100 Ohm



CPCI-S.0 REQ 4.73 Backplane System Slot Identification 2
The SYSEN_N pin shall be connected to GND at the system slot and left open on peripheral slots.

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Author: Michał Gąska
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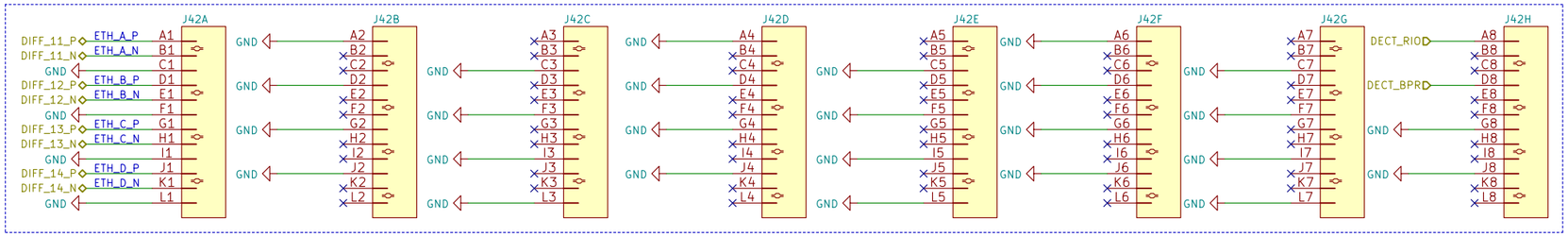
Creotech Instruments

Sheet: /Peripheral Slot 7/
File: Slot_periph.sch

Title: CPCIS Backplane

Size: A3	Date: 2019-07-18	**Rev:**
KiCad E.D.A. kicad (5.1.2)-2		Id: 8/12

P6



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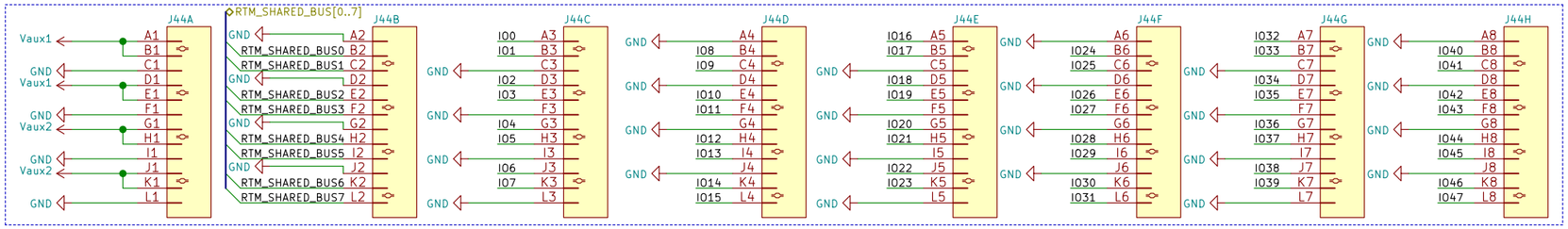
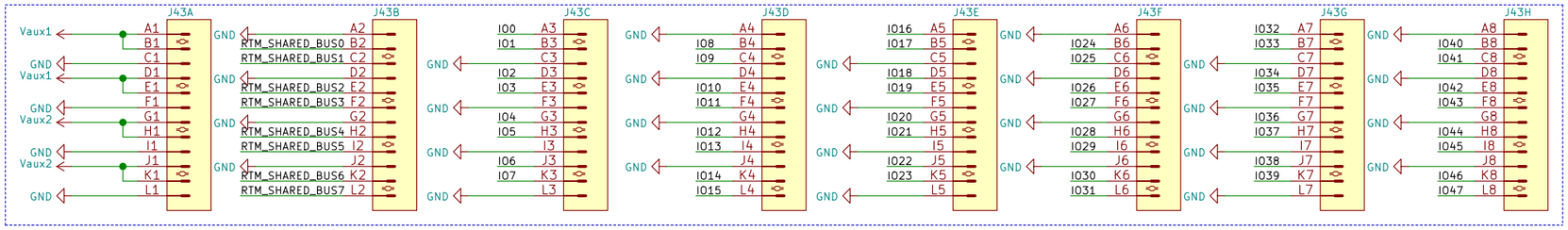
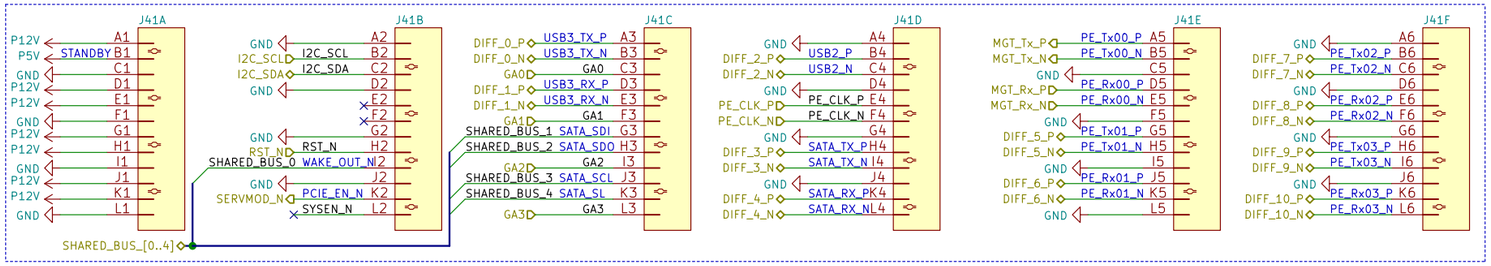


RTM connector



P1

All MGT_Tx/MGT_Rx signals mast have controlled differential impedance of 100 Ohm



CPCI-S.0 REQ 4.73 Backplane System Slot Identification 2
The SYSEN_N pin shall be connected to GND at the system slot and left open on peripheral slots.

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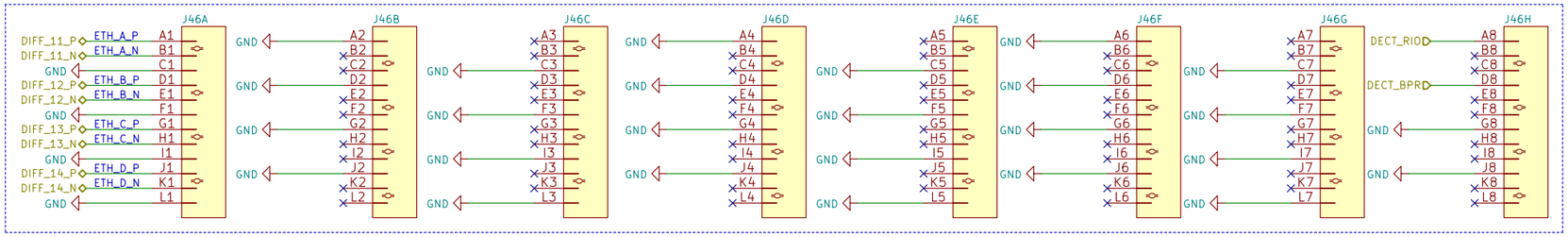
Creotech Instruments

Sheet: /Peripheral Slot 8/
File: Slot_periph.sch

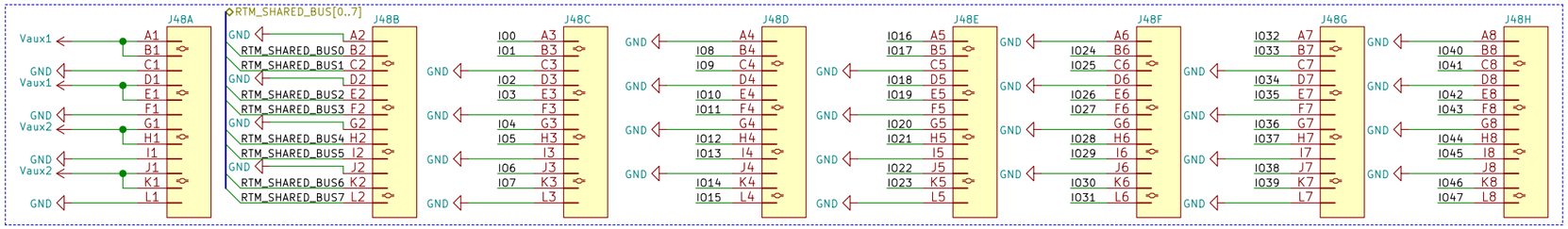
Title: CPCIS Backplane

Size: A3	Date: 2019-07-18	**Rev:**
KiCad E.D.A. kicad (5.1.2)-2		Id: 9/12

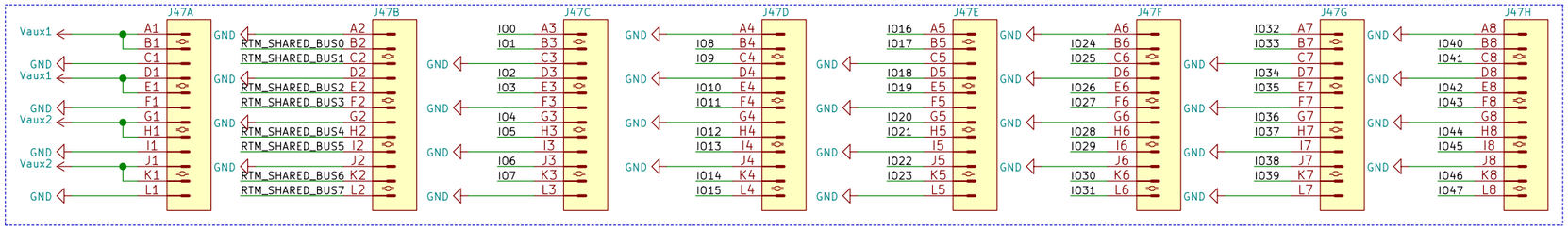
P6



P4

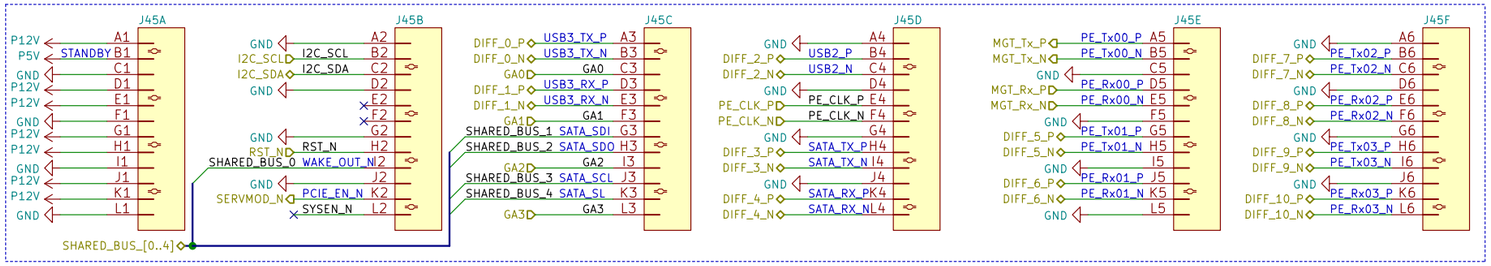


RTM connector



P1

All MGT_Tx/MGT_Rx signals mast have controlled differential impedance of 100 Ohm



CPCI-S.0 REQ 4.73 Backplane System Slot Identification 2
The SYSEN_N pin shall be connected to GND at the system slot and left open on peripheral slots.

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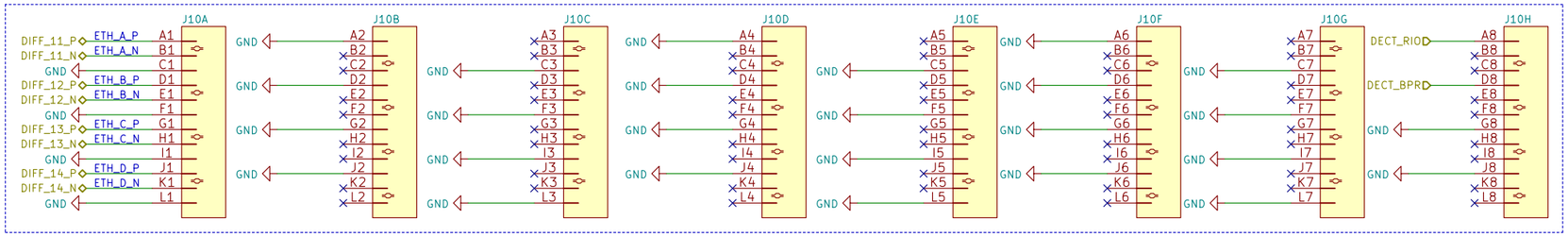
Creotech Instruments

Sheet: /Peripheral Slot 9/
File: Slot_periph.sch

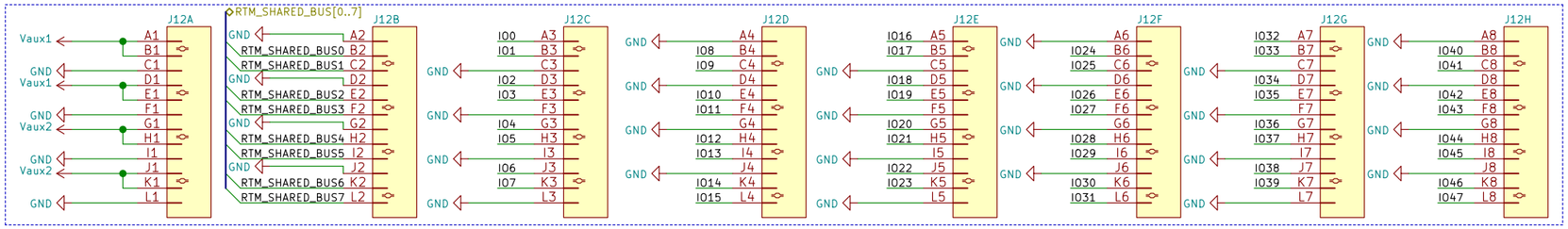
Title: CPCIS Backplane

Size: A3	Date: 2019-07-18	**Rev:**
KiCad E.D.A. kicad (5.1.2)-2		Id: 10/12

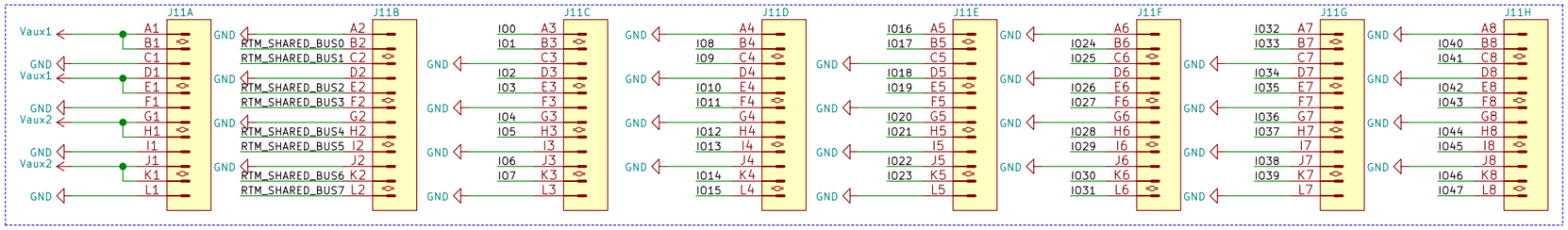
P6



P4

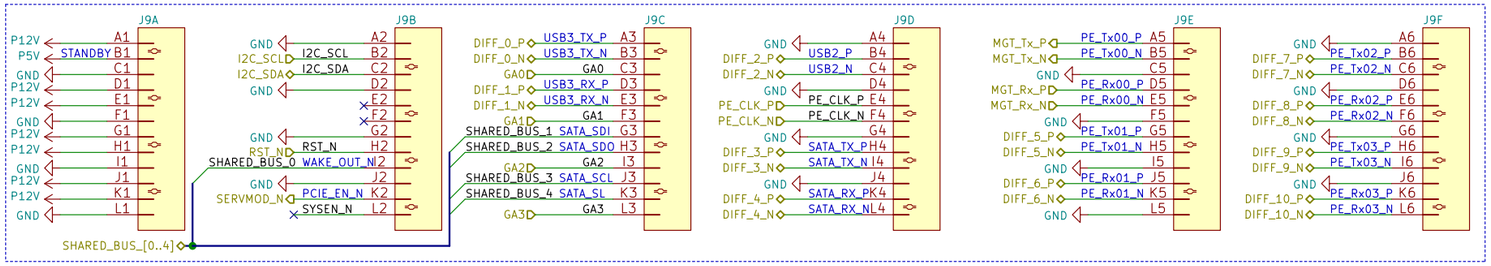


RTM connector



P1

All MGT_Tx/MGT_Rx signals mast have controlled differential impedance of 100 Ohm



CPCI-S.0 REQ 4.73 Backplane System Slot Identification 2
The SYSEN_N pin shall be connected to GND at the system slot and left open on peripheral slots.

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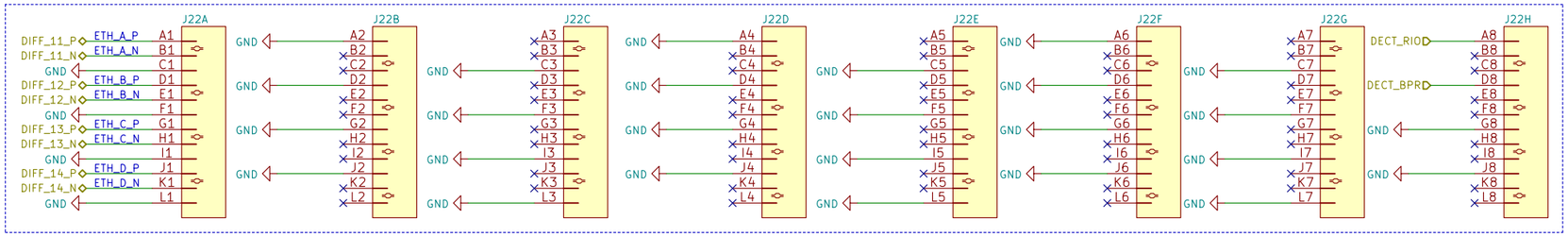
Sheet: /Peripheral Slot 3/
File: Slot_periph.sch

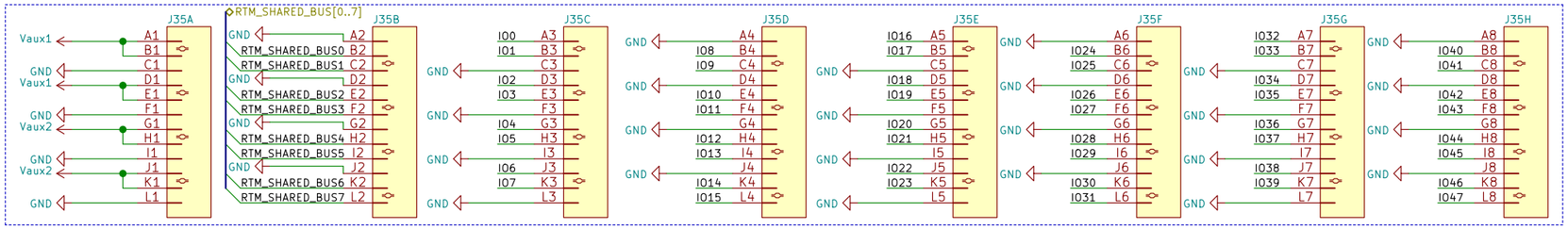
Title: CPCIS Backplane

Size: A3	Date: 2019-07-18	**Rev:**
KiCad E.D.A. kicad (5.1.2)-2		Id: 11/12

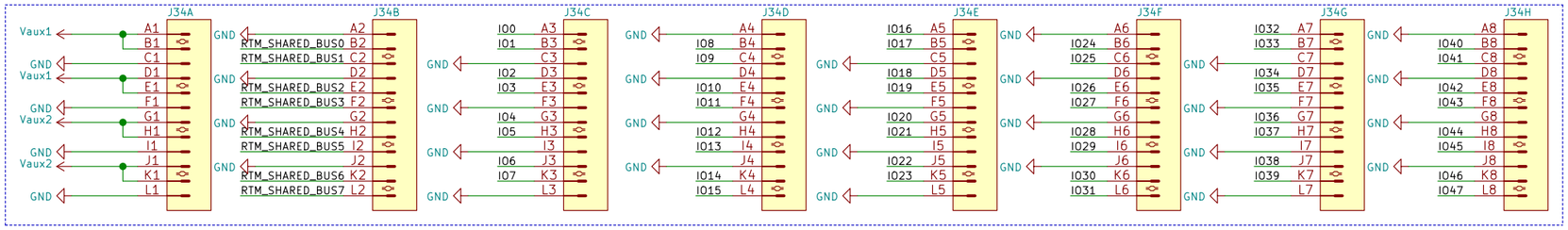
P6



P4

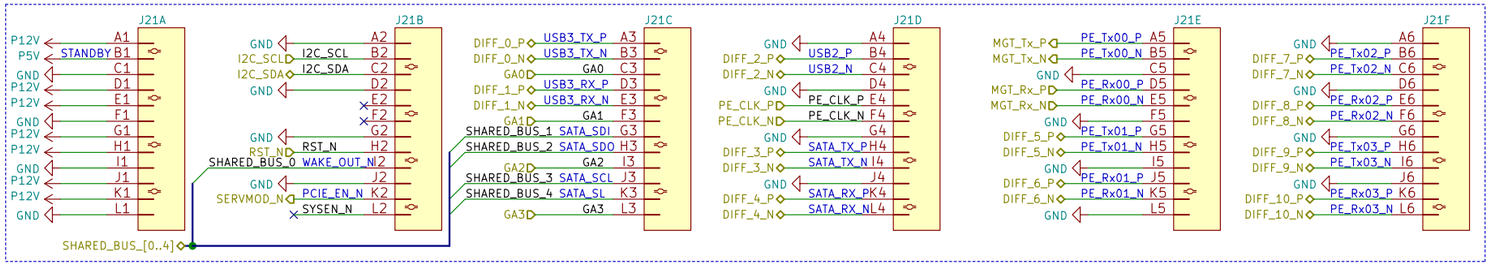


RTM connector



P1

All MGT_Tx/MGT_Rx signals mast have controlled differential impedance of 100 Ohm



CPCI-S.0 REQ 4.73 Backplane System Slot Identification 2
The SYSEN_N pin shall be connected to GND at the system slot and left open on peripheral slots.

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Creotech Instruments

Sheet: /Peripheral Slot 6/
File: Slot_periph.sch

Title: CPCIS Backplane

Size: A3	Date: 2019-07-18	**Rev:**
KiCad E.D.A. kicad (5.1.2)-2		Id: 12/12