

FANTRAY

GND

GND

F_RST

P12V

M_SCL

GND

P5V

P12V

M_SDA

GND

F_I00

P12V

F_I01

GND

GND

P12V

1

2

3

4

M_SCL

5

6

7

8

M_SDA

9

10

11

12

F_I01

13

14

15

16

J25

Utility(PSU)

GND

GND

P5V

RTN_Sense

PS_ON_N

+12V_Sense

PWRFAIL_N

P_PRESO

P_PRESI

M_SDA

M_SCL

P_RST

P_I00

P_I01

P_I02

1

2

3

4

RTN_Sense

PS_ON_N

+12V_Sense

PWRFAIL_N

P_PRESO

P_PRESI

M_SDA

M_SCL

P_RST

P_I00

P_I01

P_I02

J26

Power bushing connectors

PWR_FLAG

P12V

GND

PWR_FLAG

P12V

GND

PWR_FLAG

P5V

P5V

GND

BUSH1

M3

BUSH2

M3

BUSH3

M3

BUSH4

M3

BUSH5

M3

BUSH6

M3

BUSH7

M3

Copyright CERN 2019.

This documentation describes Open Hardware and is licensed under the CERN OHL v. 1.2.

You may redistribute and modify this documentation under the terms of the CERN OHL v.1.2. (<http://ohwr.org/cernohl>). This documentation is distributed WITHOUT ANY EXPRESS OR IMPLIED WARRANTY, INCLUDING OF MERCHANTABILITY, SATISFACTORY QUALITY AND FITNESS FOR A PARTICULAR PURPOSE.

Please see the CERN OHL v.1.2 for applicable conditions.

Project/Equipment: DI/OT CPCI Serial Backplane

KiCad E.D.A. kicad (5.1.10)–1

Document

DI/OT CPCI–serial 9 Slot Backplane

Designer Michal Gaska

Drawn by

Checked by

Last Mod.

2019–07–18

File: Power.sch

Sheet: 2 of 12

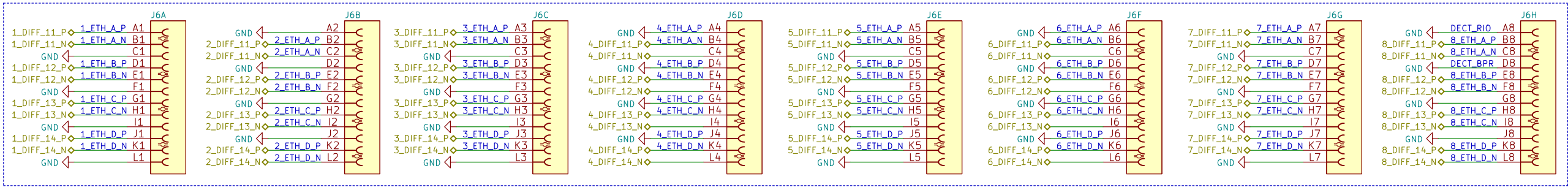
BE–CO

European Organization for Nuclear Research
CH–1211 Genève 23 – Switzerland

EDA–04211–V1–1

A3

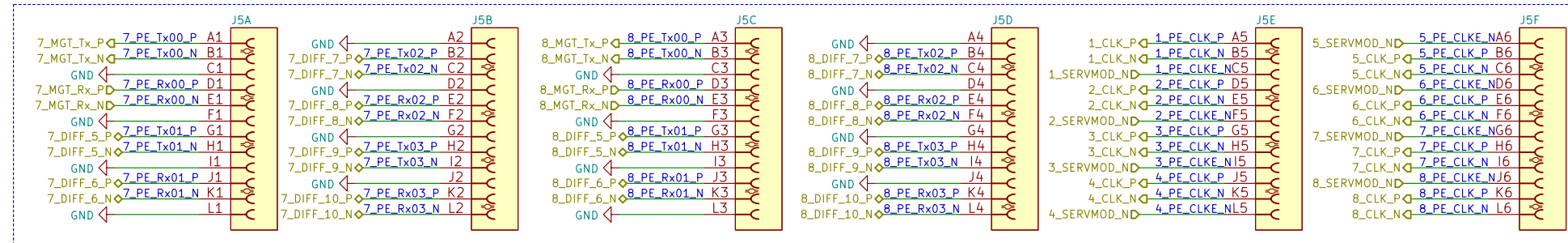
P6



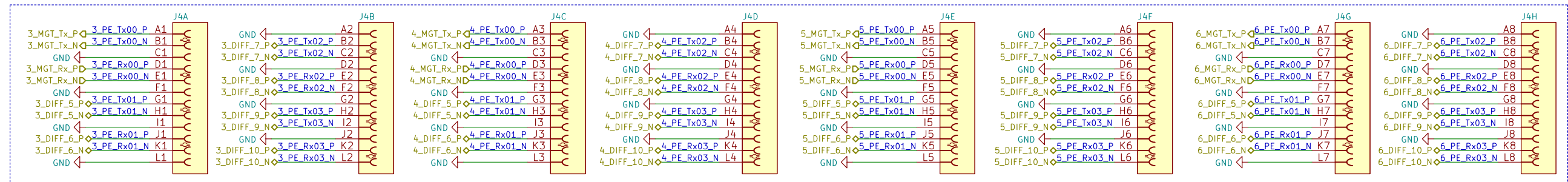
Copyright CERN 2019.

This documentation describes Open Hardware and is licensed under the CERN OHL v. 1.2.
You may redistribute and modify this documentation under the terms of the CERN OHL v.1.2. (<http://ohwr.org/cernohl>). This documentation is distributed WITHOUT ANY EXPRESS OR IMPLIED WARRANTY, INCLUDING OF MERCHANTABILITY, SATISFACTORY QUALITY AND FITNESS FOR A PARTICULAR PURPOSE.
Please see the CERN OHL v.1.2 for applicable conditions.

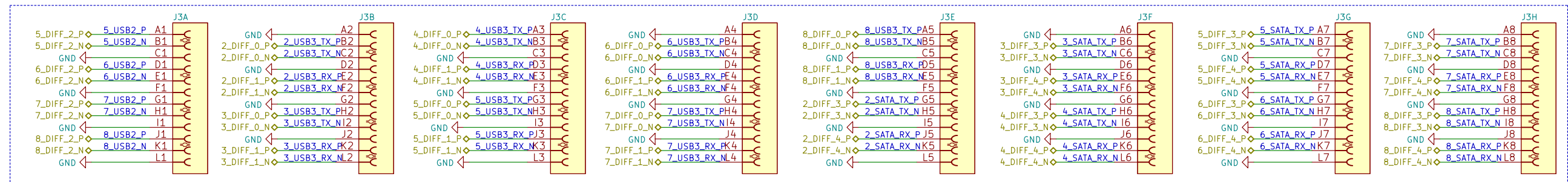
P5



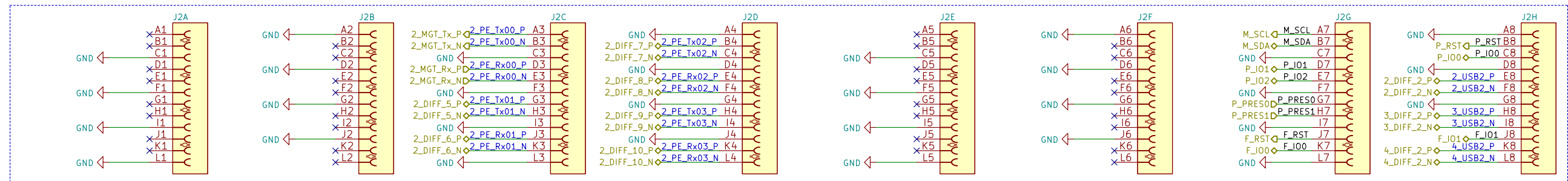
P4



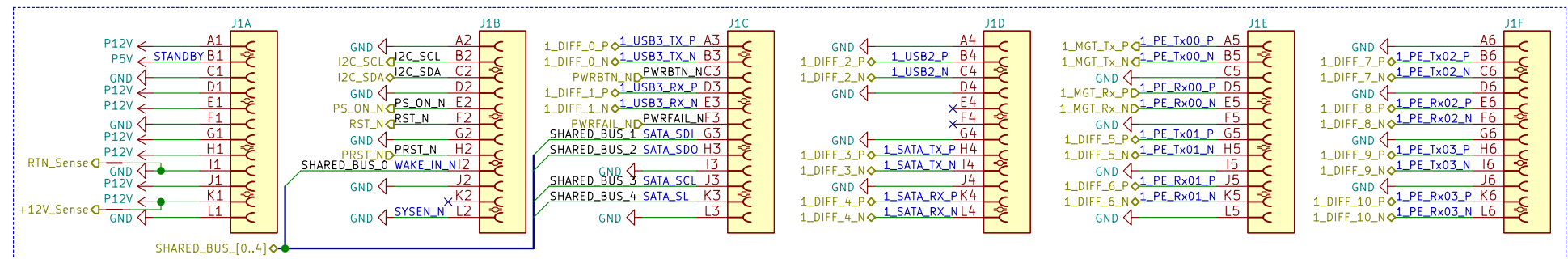
P3



P2



P1



Copyright CERN 2019.

This documentation describes Open Hardware and is licensed under the CERN OHL v.1.2. You may redistribute and modify this documentation under the terms of the CERN OHL v.1.2. (<http://ohwr.org/cernohl>). This documentation is distributed WITHOUT ANY EXPRESS OR IMPLIED WARRANTY, INCLUDING OF MERCHANTABILITY, SATISFACTORY QUALITY AND FITNESS FOR A PARTICULAR PURPOSE. Please see the CERN OHL v.1.2 for applicable conditions.

Length matching:

- between the two MGT lines per slot [MGT_Rx, MGT_Tx]; these 2 diff-pairs are matched for the same slot
- among the eight CLK [1_CLK..8_CLK]; these 8 diff-pairs are length-matched.

All n_MGT_Tx/n_MGT_Rx signals must have controlled differential impedance of 100 Ohm

Project/Equipment: DI/OT CPCI Serial Backplane

KiCad E.D.A. kicad (5.1.10)–1

Document

DI/OT CPCI–serial 9 Slot Backplane

Designer Michał Gaska

Drawn by

Checked by

Last Mod.

2019–07–18

File: Slot_1.sch

Sheet: 4 of 12

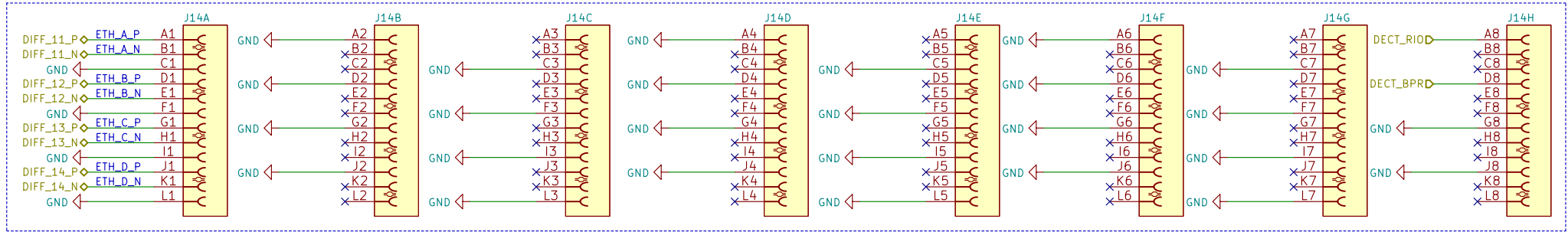
BE–CO

European Organization for Nuclear Research
CH–1211 Genève 23 – Switzerland

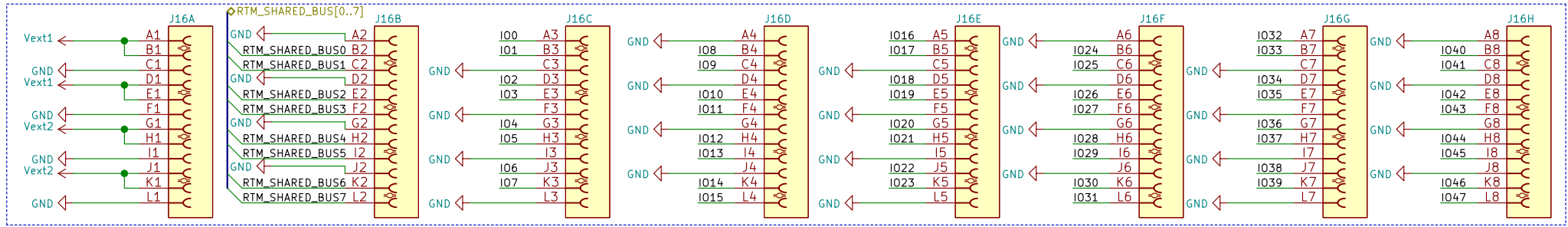
EDA–04211–V1–1

A3

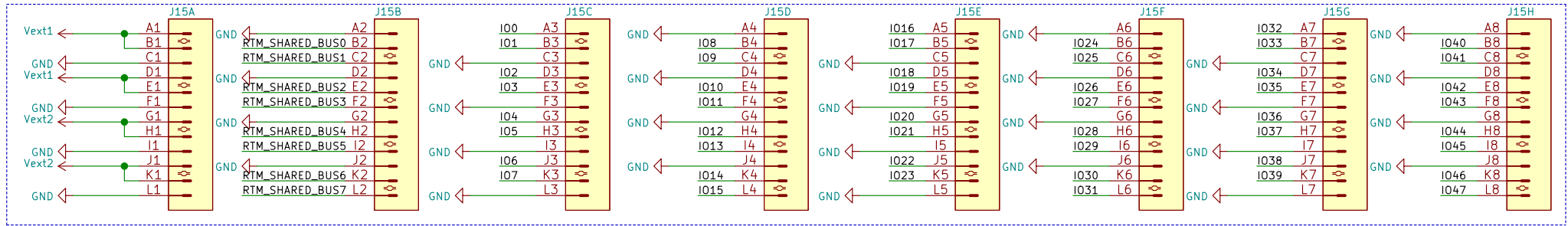
P6



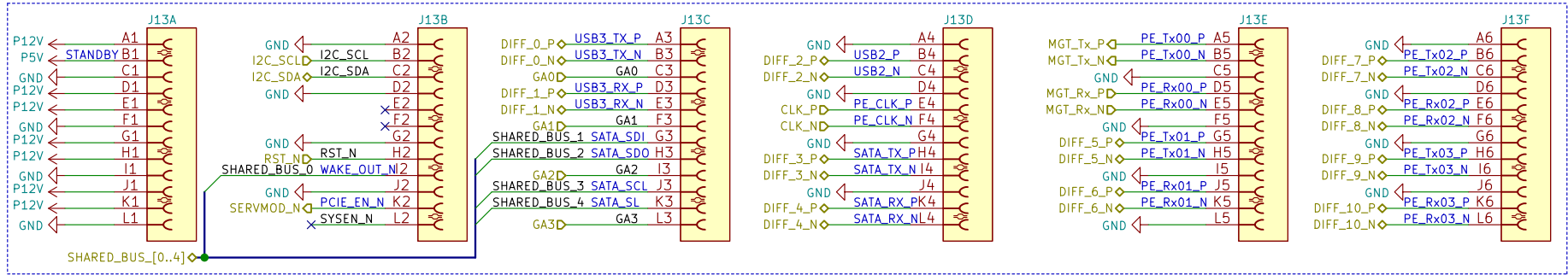
P4



RTM connector



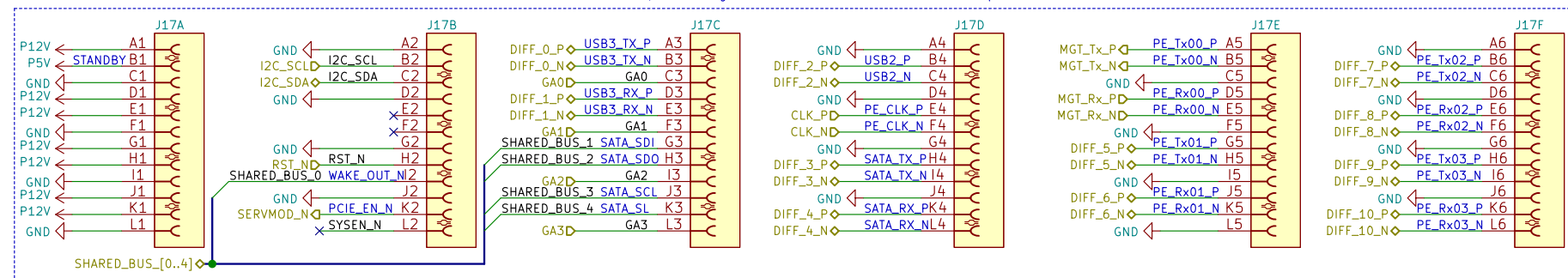
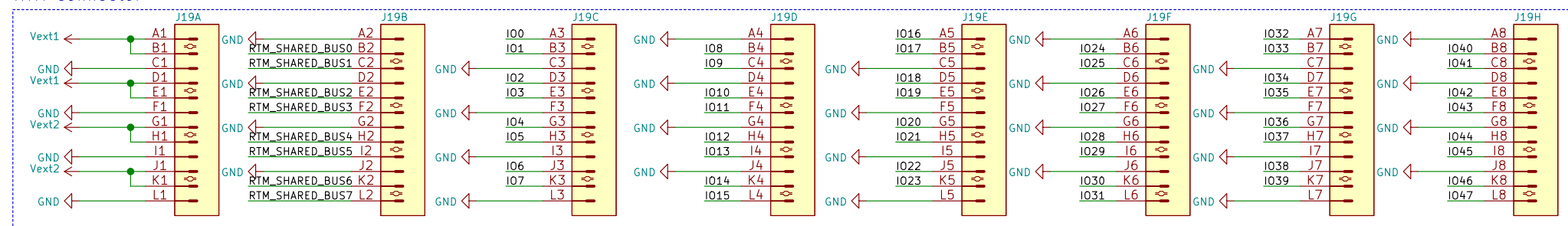
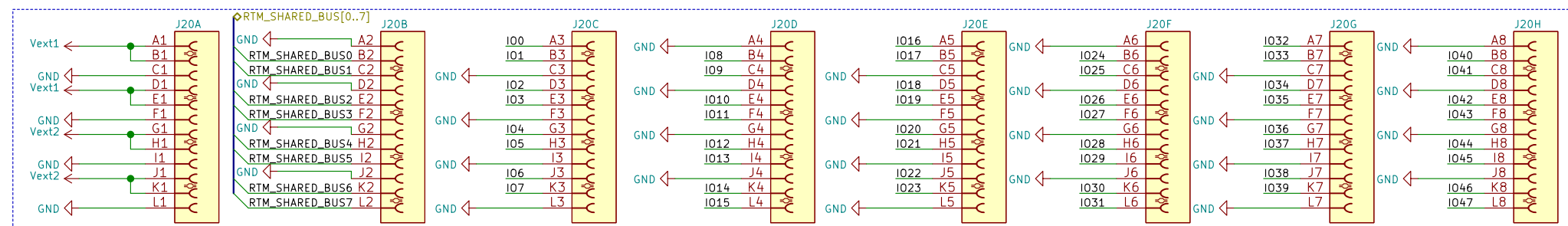
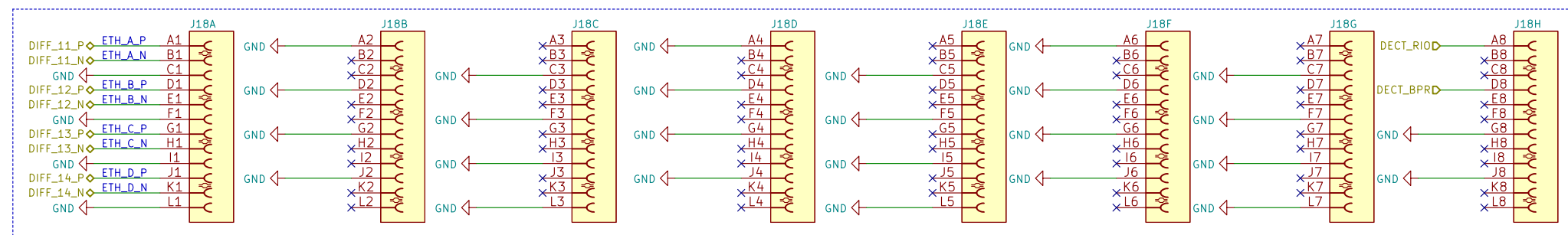
P1



CPCI-S.0 REQ 4.73 Backplane System Slot Identification 2
The SYSN_N pin shall be connected to GND at the system slot and left open on peripheral slots.

Copyright CERN 2019.

This documentation describes Open Hardware and is licensed under the CERN OHL v. 1.2.
You may redistribute and modify this documentation under the terms of the CERN OHL v.1.2. (<http://ohwr.org/cernohl>). This documentation is distributed WITHOUT ANY EXPRESS OR IMPLIED WARRANTY, INCLUDING OF MERCHANTABILITY, SATISFACTORY QUALITY AND FITNESS FOR A PARTICULAR PURPOSE.
Please see the CERN OHL v.1.2 for applicable conditions.



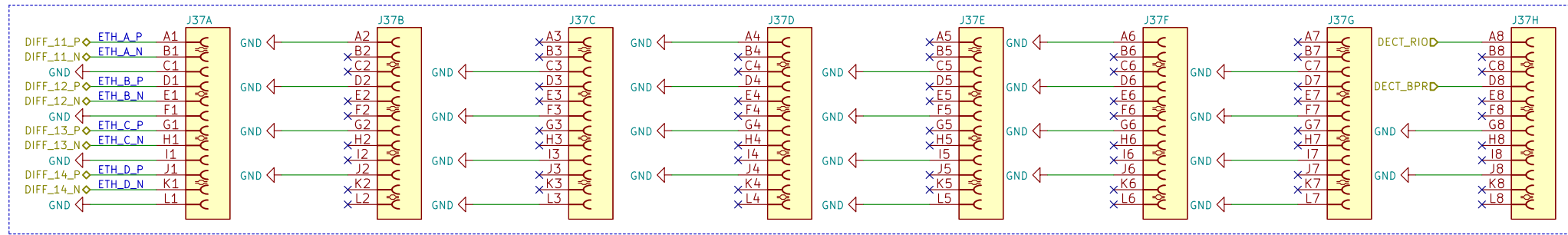
Copyright CERN 2019.

This documentation describes Open Hardware and is licensed under the CERN OHL v. 1.2.

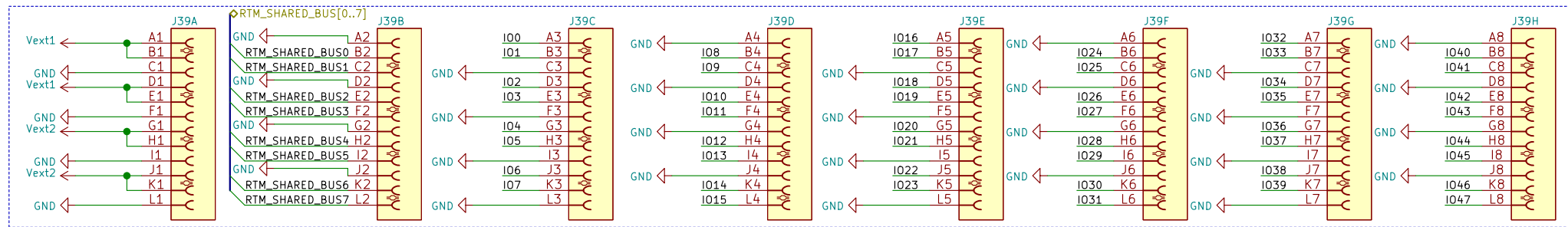
You may redistribute and modify this documentation under the terms of the CERN OHL v.1.2. (<http://ohwr.org/cernohl>). This documentation is distributed WITHOUT ANY EXPRESS OR IMPLIED WARRANTY, INCLUDING OF MERCHANTABILITY, SATISFACTORY QUALITY AND FITNESS FOR A PARTICULAR PURPOSE.

Please see the CERN OHL v.1.2 for applicable conditions.

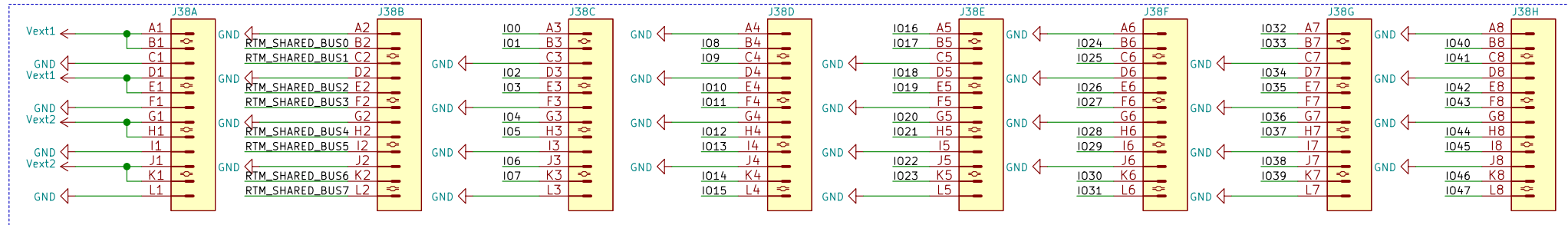
P6



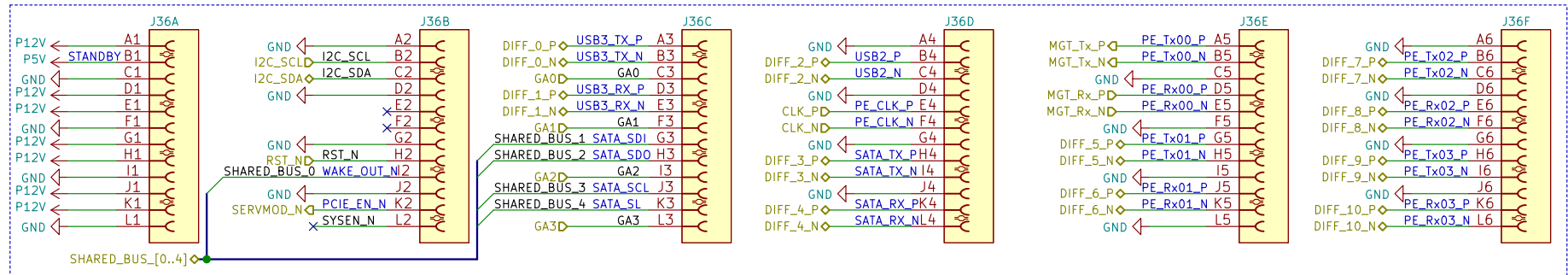
P4



RTM connector



P1

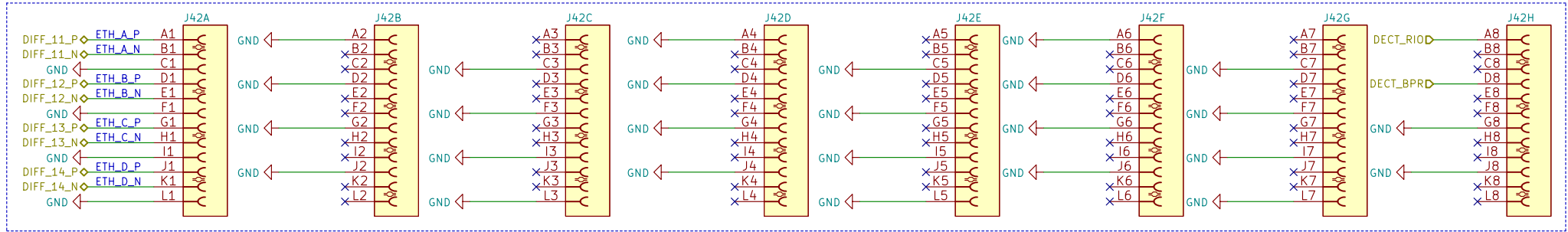


CPCI-S.0 REQ 4.73 Backplane System Slot Identification 2
The SYSN_N pin shall be connected to GND at the system slot and left open on peripheral slots.

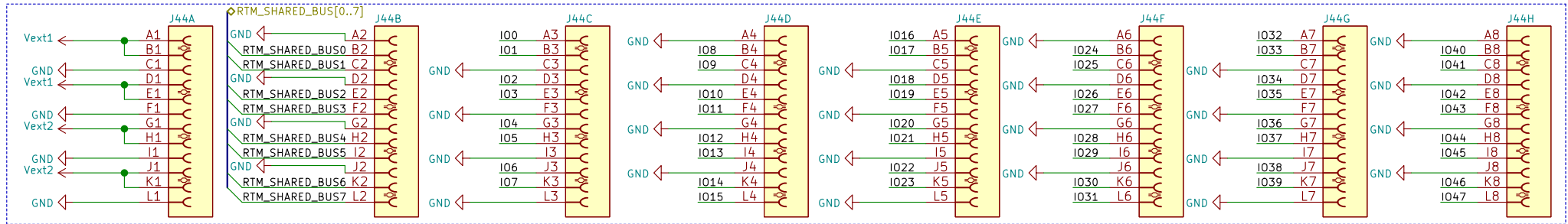
Copyright CERN 2019.

This documentation describes Open Hardware and is licensed under the CERN OHL v. 1.2.
You may redistribute and modify this documentation under the terms of the CERN OHL v.1.2. (<http://ohwr.org/cernohl>). This documentation is distributed WITHOUT ANY EXPRESS OR IMPLIED WARRANTY, INCLUDING OF MERCHANTABILITY, SATISFACTORY QUALITY AND FITNESS FOR A PARTICULAR PURPOSE.
Please see the CERN OHL v.1.2 for applicable conditions.

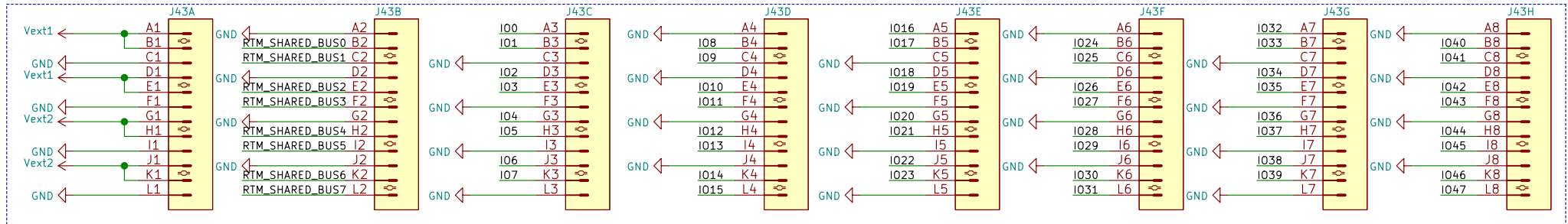
P6



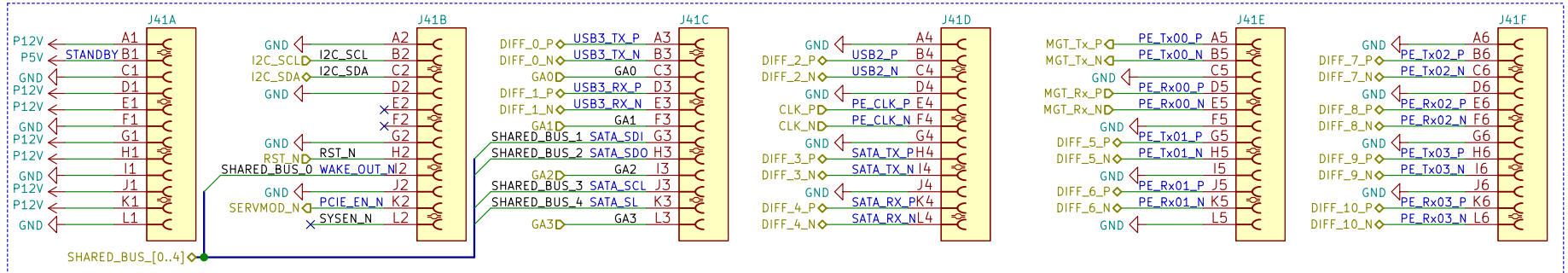
P4



RTM connector



P1

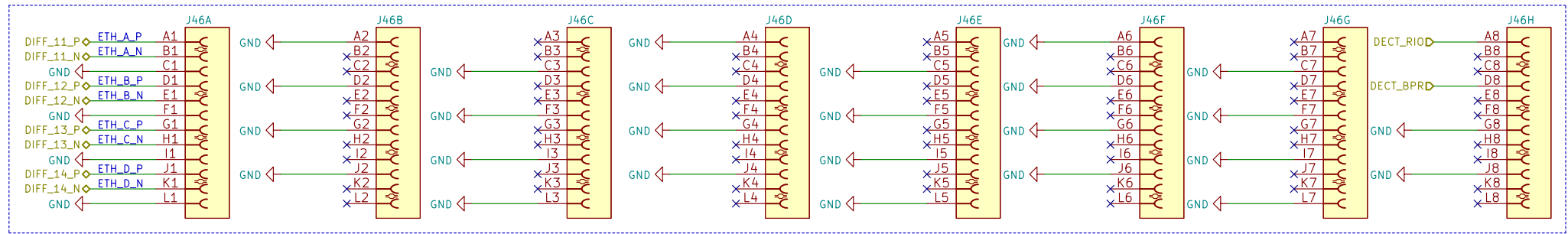


CPCI-S.0 REQ 4.73 Backplane System Slot Identification 2
The SYSN_N pin shall be connected to GND at the system slot and left open on peripheral slots.

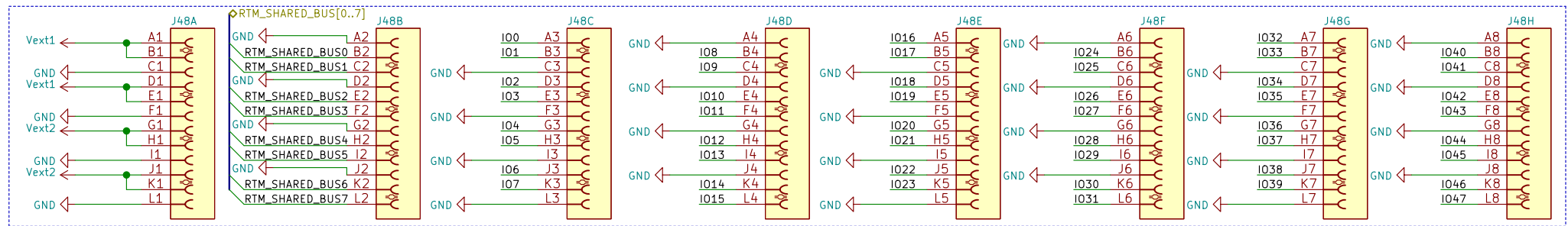
Copyright CERN 2019.

This documentation describes Open Hardware and is licensed under the CERN OHL v. 1.2.
You may redistribute and modify this documentation under the terms of the CERN OHL v.1.2. (<http://ohwr.org/cernohl>). This documentation is distributed WITHOUT ANY EXPRESS OR IMPLIED WARRANTY, INCLUDING OF MERCHANTABILITY, SATISFACTORY QUALITY AND FITNESS FOR A PARTICULAR PURPOSE.
Please see the CERN OHL v.1.2 for applicable conditions.

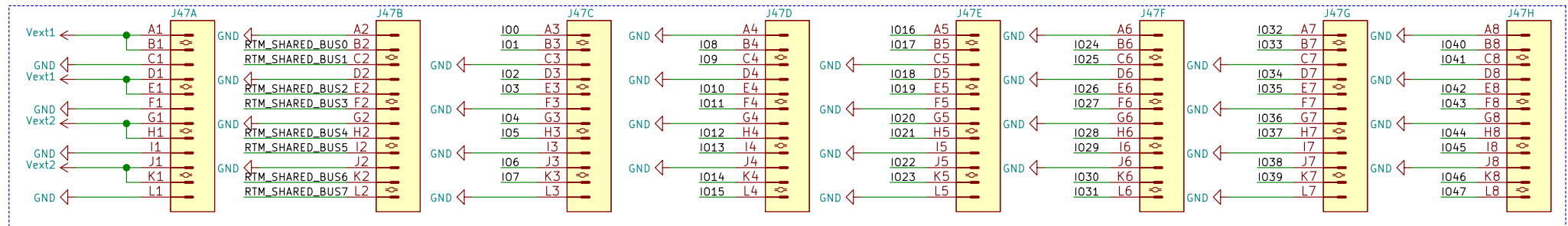
P6



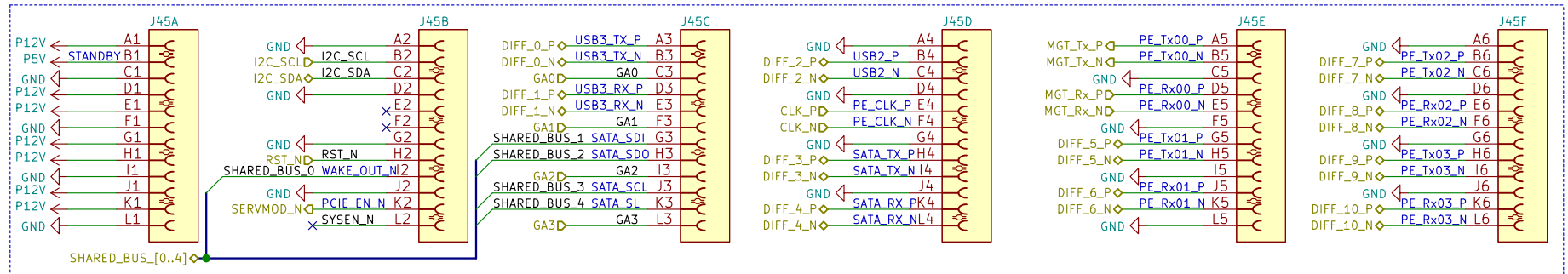
P4



RTM connector



P1

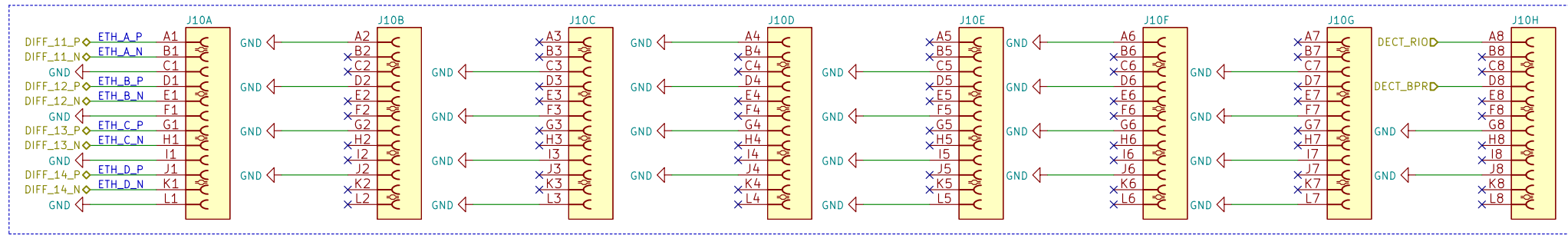


CPCI-S.0 REQ 4.73 Backplane System Slot Identification 2
The SYSN_N pin shall be connected to GND at the system slot and left open on peripheral slots.

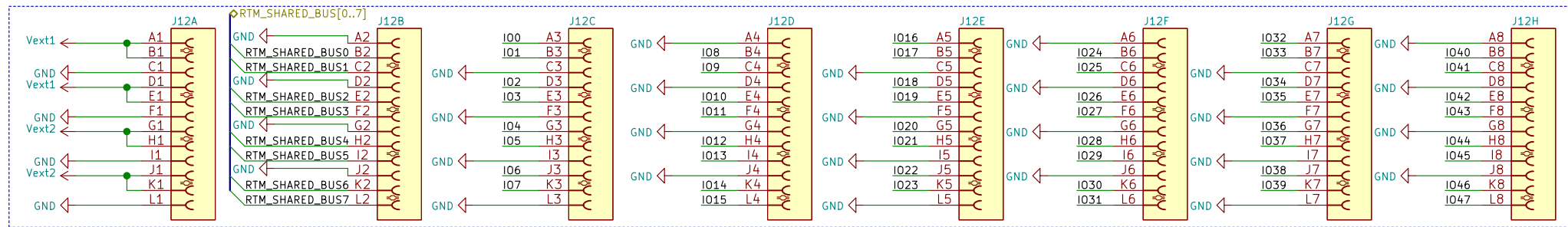
Copyright CERN 2019.

This documentation describes Open Hardware and is licensed under the CERN OHL v. 1.2.
You may redistribute and modify this documentation under the terms of the CERN OHL v.1.2. (<http://ohwr.org/cernohl>). This documentation is distributed WITHOUT ANY EXPRESS OR IMPLIED WARRANTY, INCLUDING OF MERCHANTABILITY, SATISFACTORY QUALITY AND FITNESS FOR A PARTICULAR PURPOSE.
Please see the CERN OHL v.1.2 for applicable conditions.

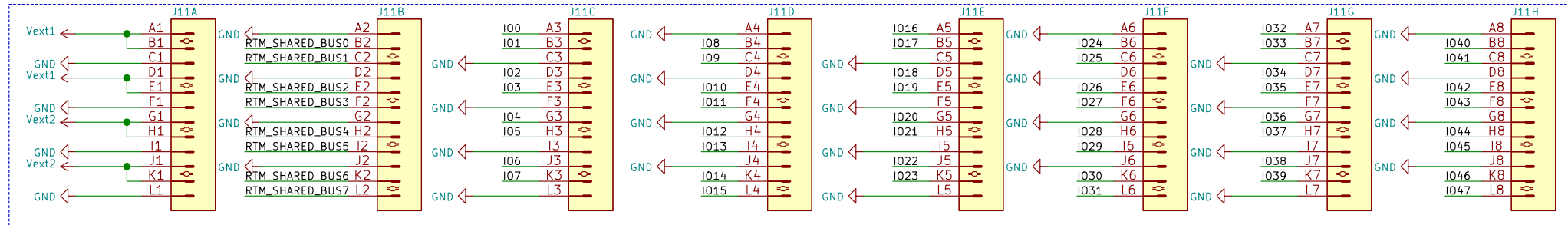
P6



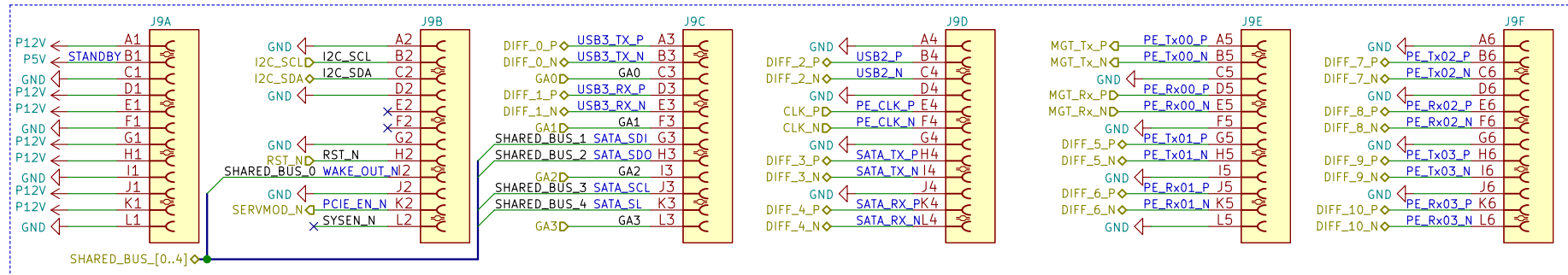
P4



RTM connector



P1

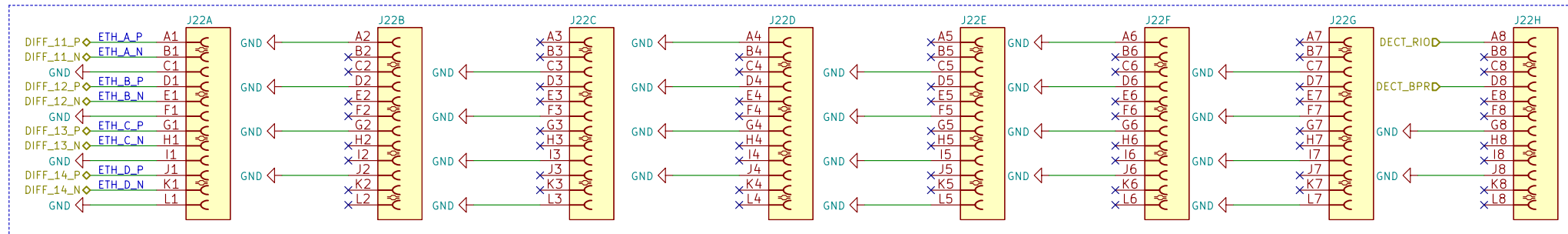


CPCI-S.0 REQ 4.73 Backplane System Slot Identification 2
The SYSN_N pin shall be connected to GND at the system slot and left open on peripheral slots.

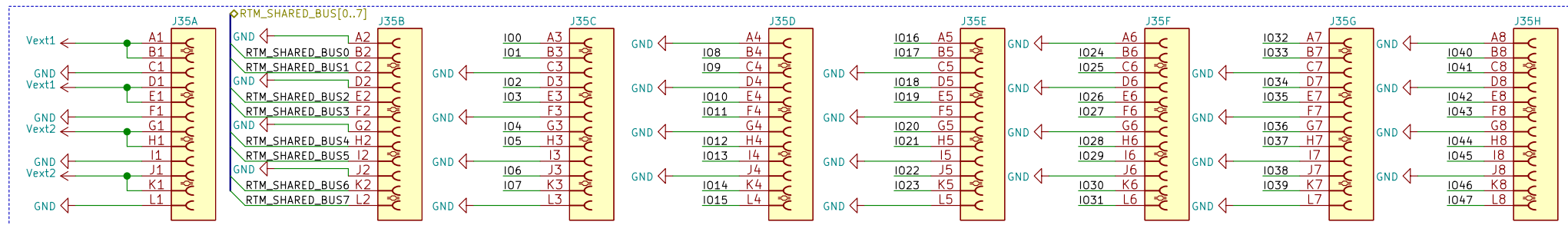
Copyright CERN 2019.

This documentation describes Open Hardware and is licensed under the CERN OHL v. 1.2.
You may redistribute and modify this documentation under the terms of the CERN OHL v.1.2. (<http://ohwr.org/cernohl>). This documentation is distributed WITHOUT ANY EXPRESS OR IMPLIED WARRANTY, INCLUDING OF MERCHANTABILITY, SATISFACTORY QUALITY AND FITNESS FOR A PARTICULAR PURPOSE.
Please see the CERN OHL v.1.2 for applicable conditions.

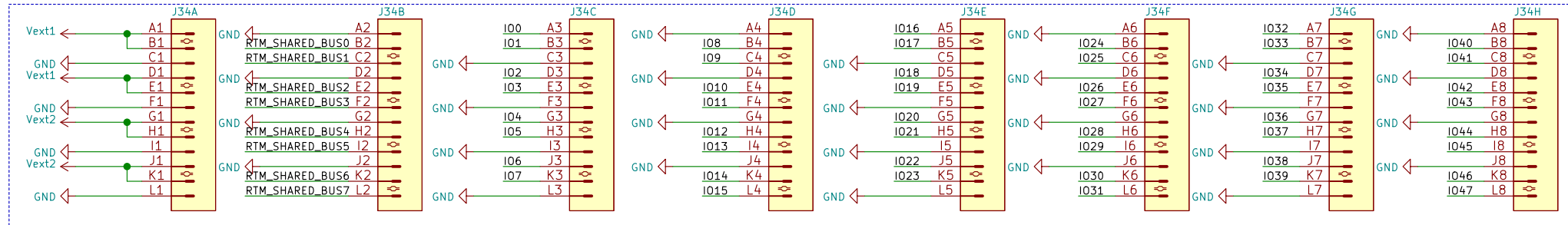
P6



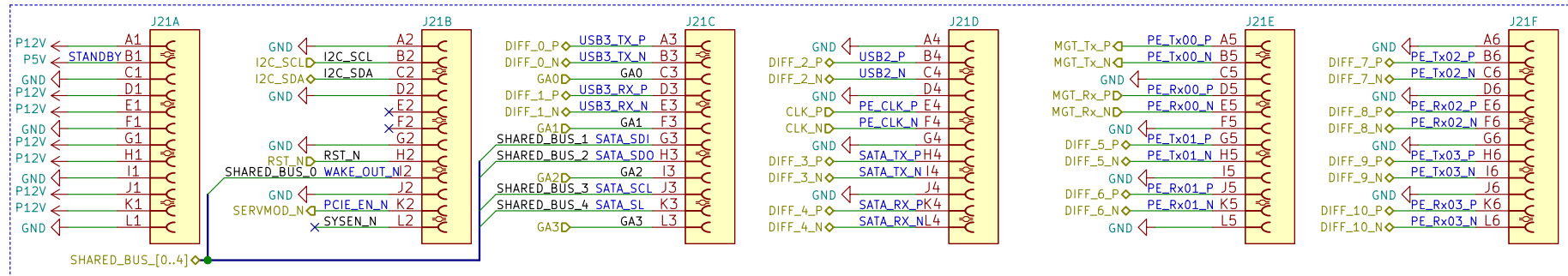
P4



RTM connector



P1

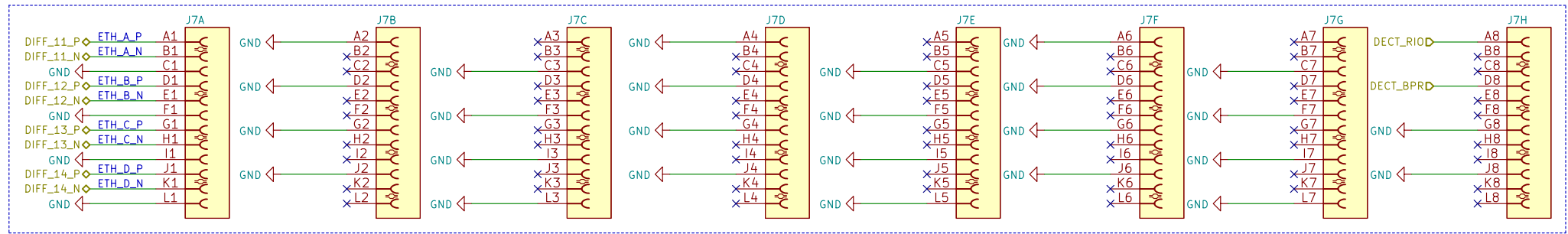


CPCI-S.0 REQ 4.73 Backplane System Slot Identification 2
The SYSN_N pin shall be connected to GND at the system slot and left open on peripheral slots.

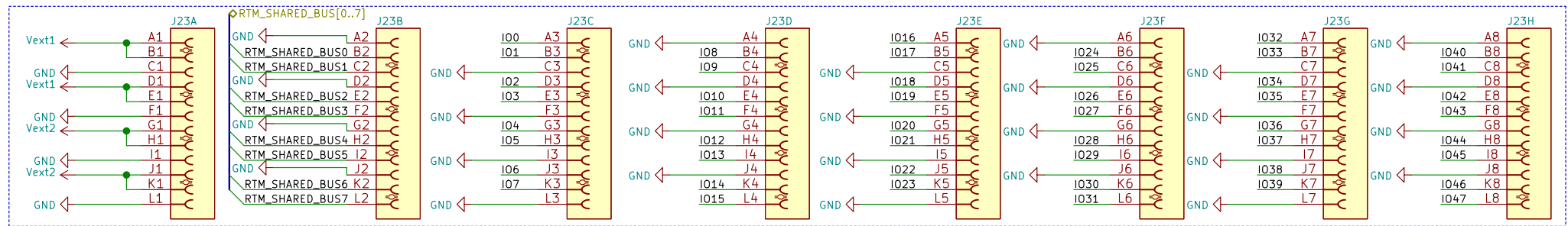
Copyright CERN 2019.

This documentation describes Open Hardware and is licensed under the CERN OHL v. 1.2.
You may redistribute and modify this documentation under the terms of the CERN OHL v.1.2. (<http://ohwr.org/cernohl>). This documentation is distributed WITHOUT ANY EXPRESS OR IMPLIED WARRANTY, INCLUDING OF MERCHANTABILITY, SATISFACTORY QUALITY AND FITNESS FOR A PARTICULAR PURPOSE.
Please see the CERN OHL v.1.2 for applicable conditions.

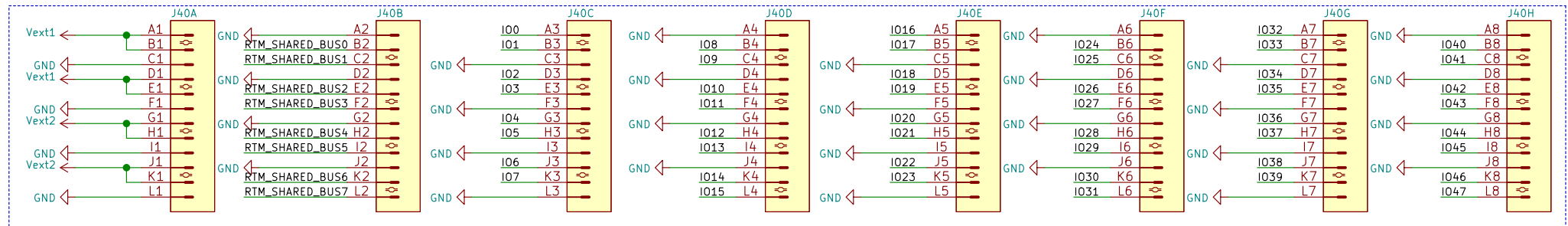
P6



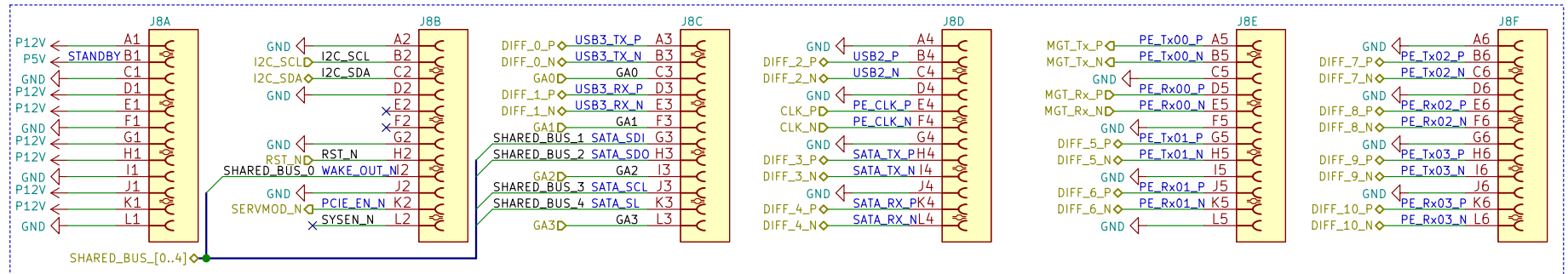
P4



RTM connector



P1



CPCI-S.0 REQ 4.73 Backplane System Slot Identification 2
The SYSEN_N pin shall be connected to GND at the system slot and left open on peripheral slots.

Copyright CERN 2019.

This documentation describes Open Hardware and is licensed under the CERN OHL v. 1.2.
You may redistribute and modify this documentation under the terms of the CERN OHL v.1.2. (<http://ohwr.org/cernohl>). This documentation is distributed WITHOUT ANY EXPRESS OR IMPLIED WARRANTY, INCLUDING OF MERCHANTABILITY, SATISFACTORY QUALITY AND FITNESS FOR A PARTICULAR PURPOSE.
Please see the CERN OHL v.1.2 for applicable conditions.