

256MB DDR3 memory

ADC mezzanine

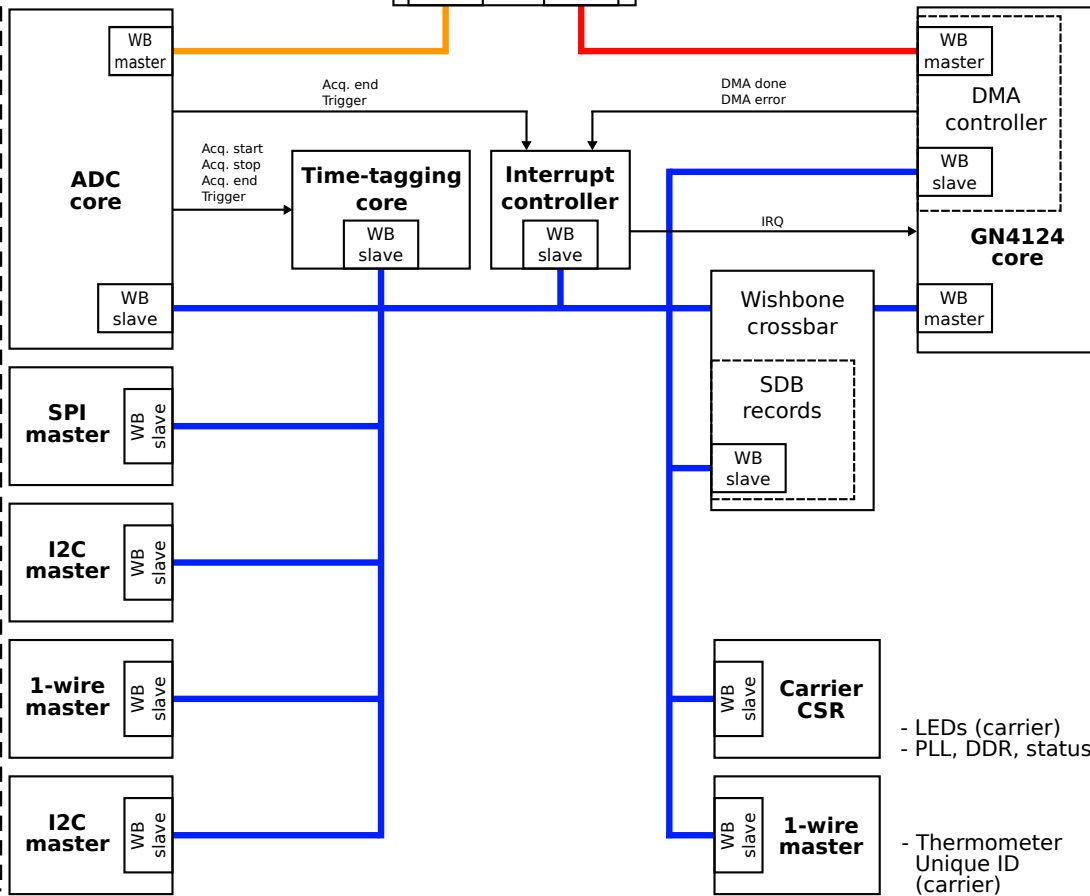
- ADC datapath
- Analogue front-end

- DC offset DAC
- ADC control

- Clock control (Si570)

- Thermometer Unique ID (mezzanine)

- EEPROM



GN4124  
PCIe bridge

- LEDs (carrier)
- PLL, DDR, status

- Thermometer Unique ID (carrier)