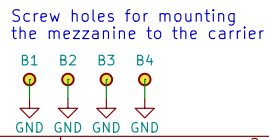
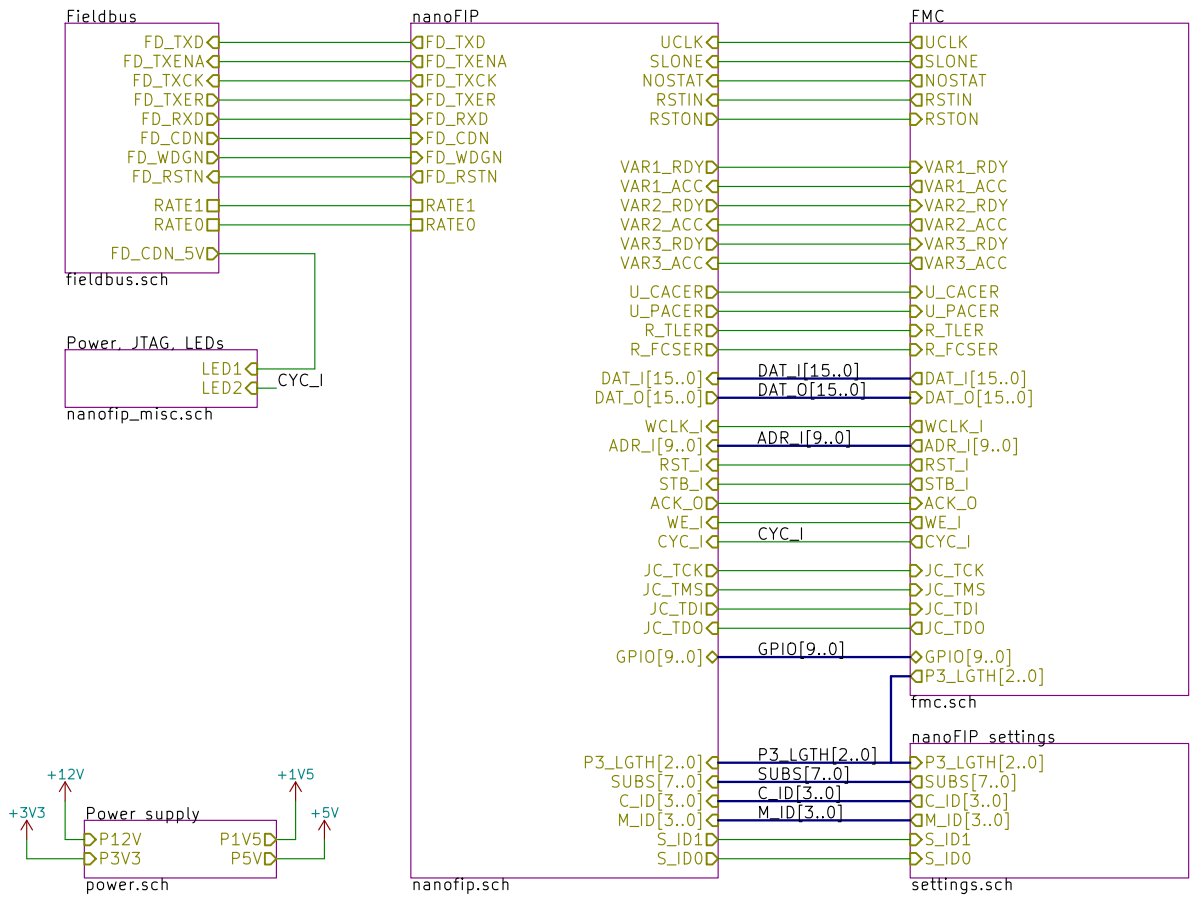


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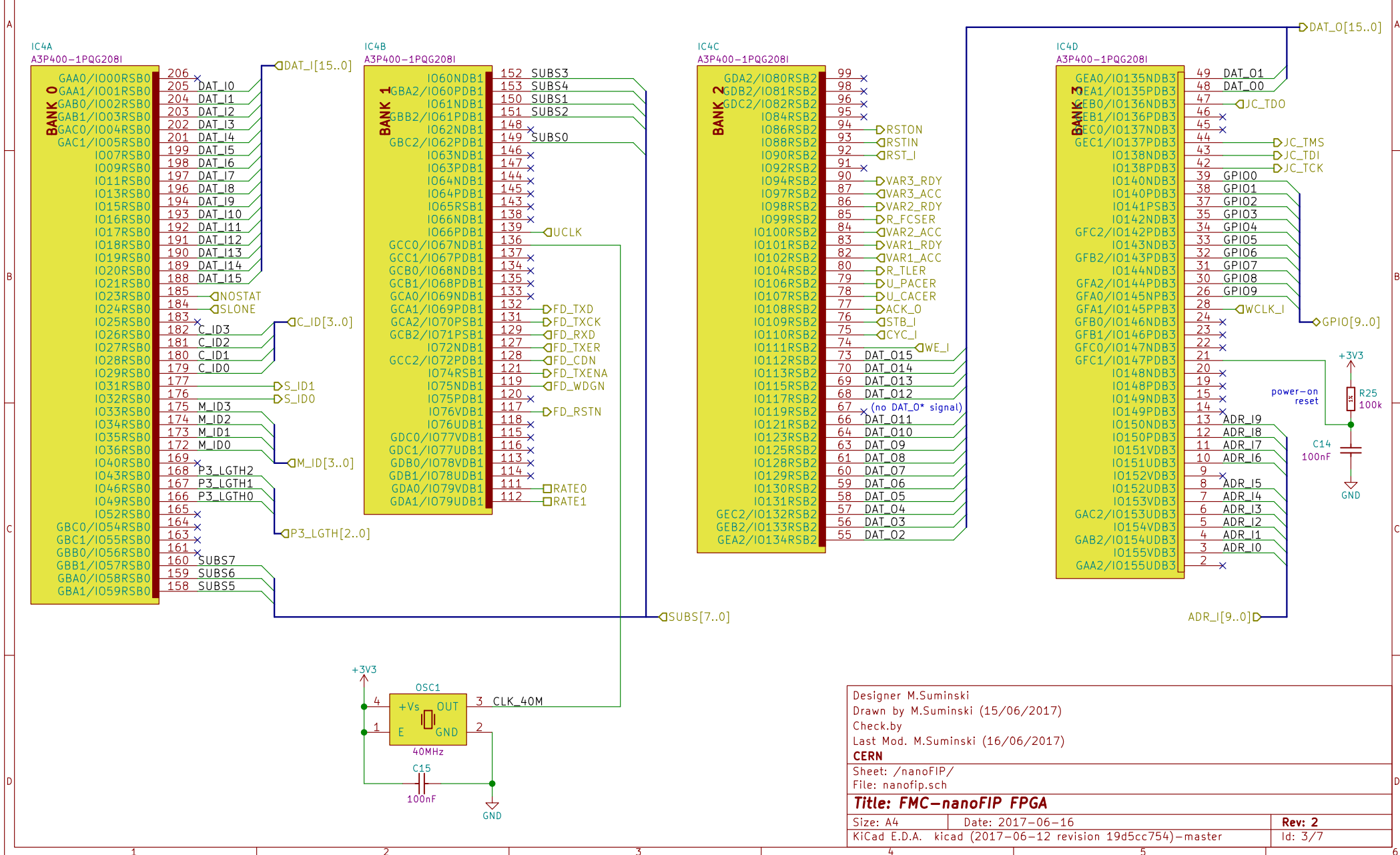
Designer M.Suminski	
Drawn by M.Suminski (15/06/2017)	
Check.by	
Last Mod. M.Suminski (16/06/2017)	
<b>CERN</b>	
Sheet: /	
File: fmc-nanofip.sch	
<b>Title: FMC-nanoFIP</b>	
Size: A4	Date: 2017-06-16
KiCad E.D.A. kicad (2017-06-12 revision 19d5cc754)-master	Rev: 2
	Id: 1/7



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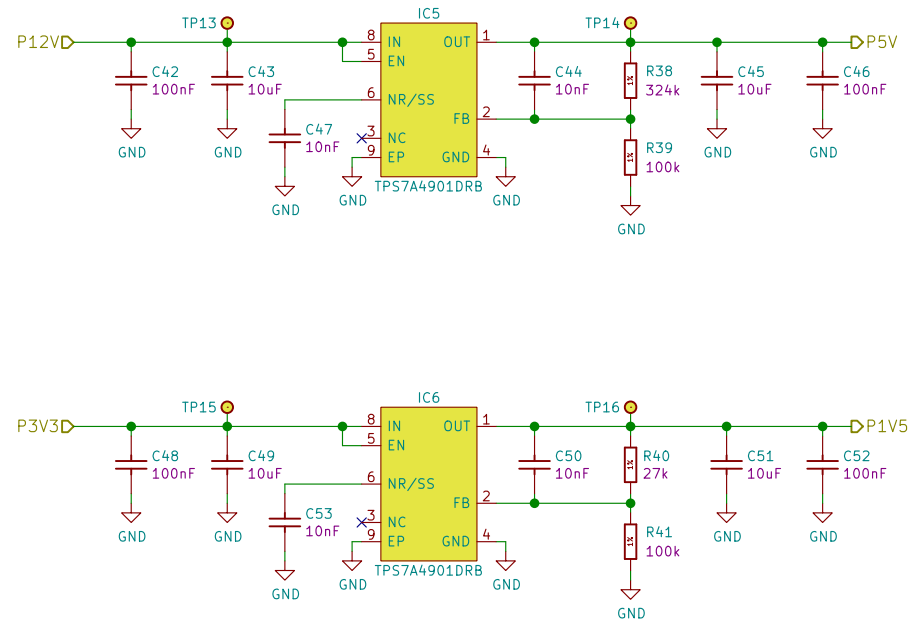
PIN SWAPPING NOT POSSIBLE



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CERN	
Sheet: /nanoFIP/	
File: nanofip.sch	
<b>Title: FMC-nanoFIP FPGA</b>	
Size: A4	Date: 2017-06-16
KiCad E.D.A. kicad (2017-06-12 revision 19d5cc754)-master	Rev: 2
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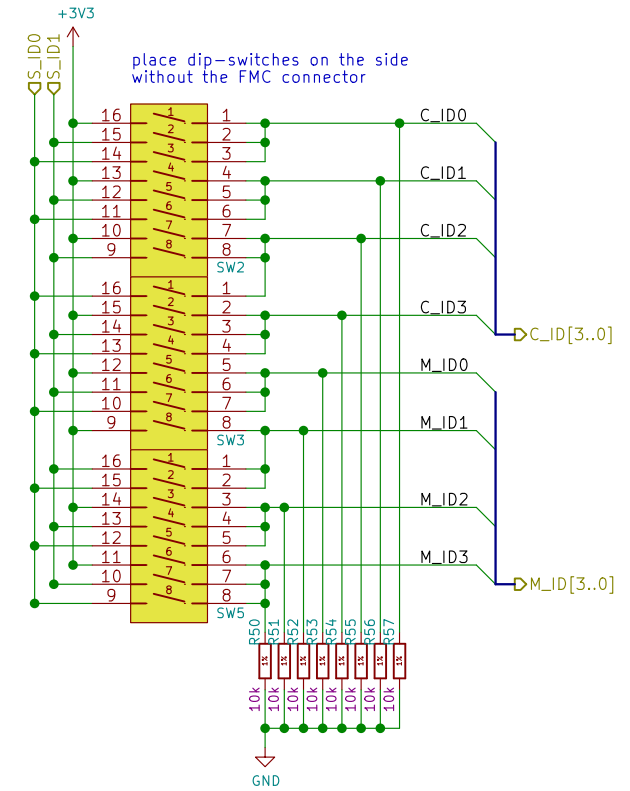
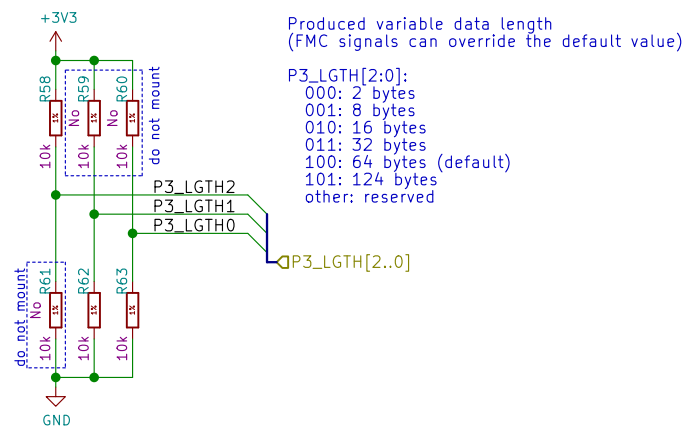
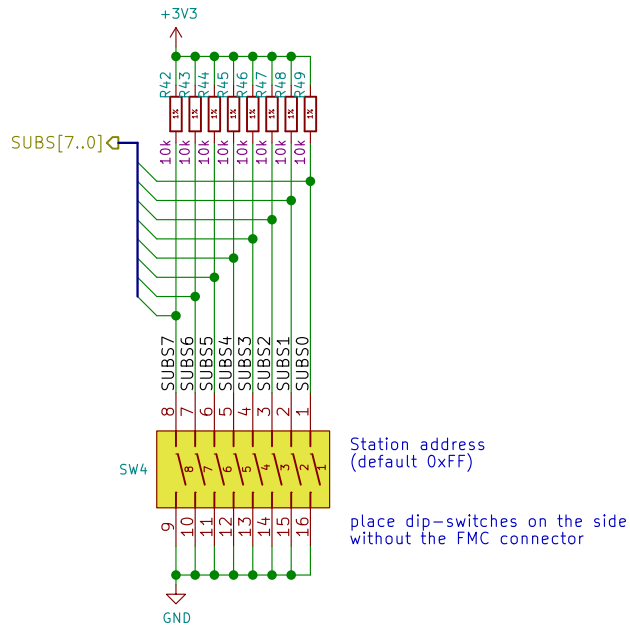
**CERN**

Sheet: /Power supply/  
 File: power.sch

**Title: FMC-nanoFIP power supply**

Size: A4	Date: 2017-06-16	<b>Rev: 2</b>
KiCad E.D.A. kicad (2017-06-12 revision 19d5cc754)-master		Id: 5/7

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<b>CERN</b>	
Sheet: /nanoFIP settings/ File: settings.sch	
<b>Title: FMC-nanoFIP settings</b>	
Size: A4	Date: 2017-06-16
KiCad E.D.A. kicad (2017-06-12 revision 19d5cc754)-master	Rev: 2 Id: 6/7

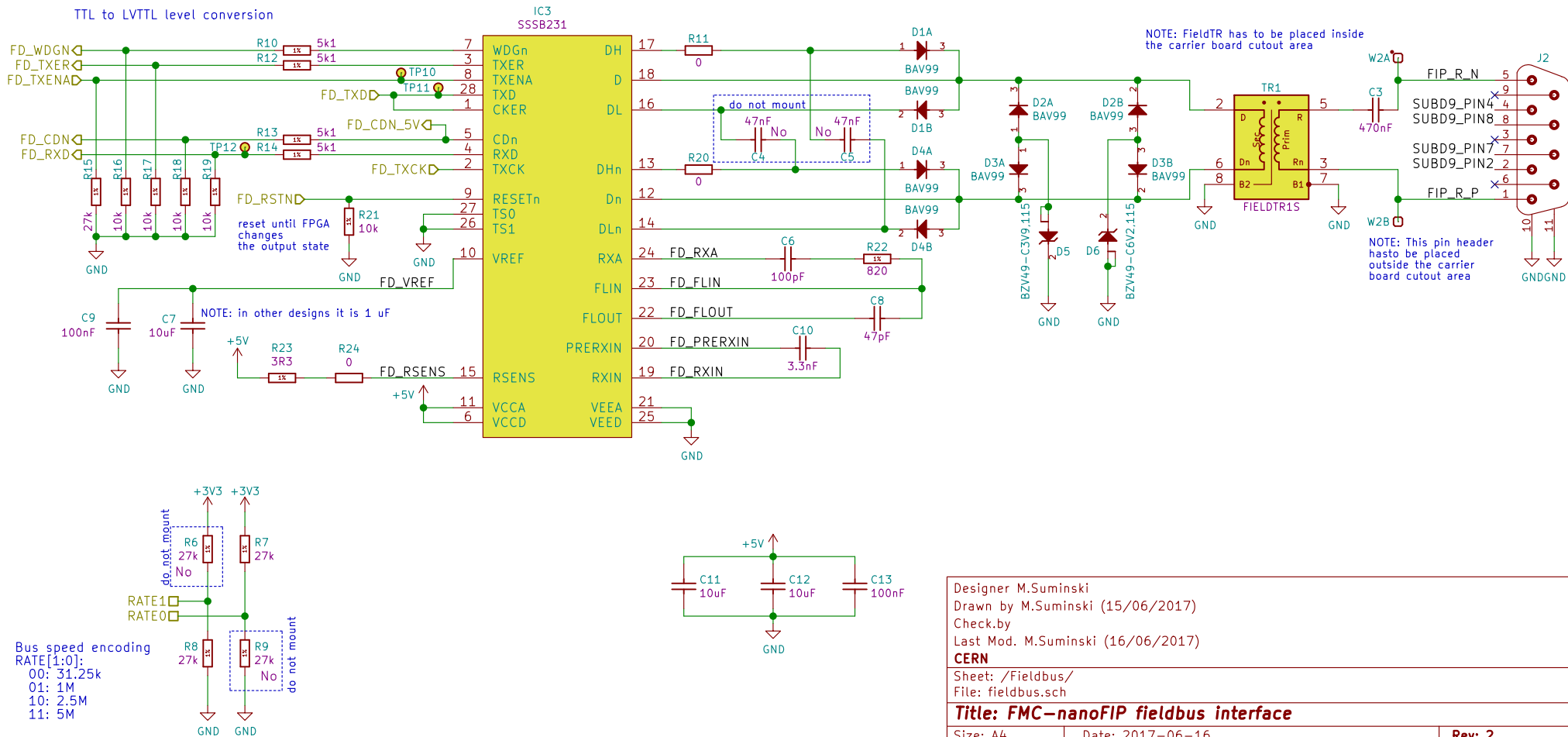
nanoFIP bus speed

marking	31.25k <input type="checkbox"/> R2 <input type="checkbox"/>	1M <input type="checkbox"/> R3 <input type="checkbox"/>	2.5M <input type="checkbox"/> R4 <input type="checkbox"/>	5M <input type="checkbox"/> R5 <input type="checkbox"/>
resistor	do_not_mount 0 No	do_not_mount 0 No	do_not_mount 0 No	do_not_mount 0 No
C3	3.3uF	470nF	100nF	100nF
C10	100nF	3.3nF	1.5nF	1.5nF
C8	1nF	47pF	27pF	27pF
C6	15nF	100pF	33pF	33pF
C4, C5	47nF	not mounted	not mounted	not mounted
D6	BZV49-C8V2	BZV49-C6V2	BZV49-C6V2	BZV49-C6V2
D2, D3	BAT54S	BAV99	BAV99	BAV99
D5	BZV49-C4V7	BZV49-C3V9	BZV49-C3V9	BZV49-C3V9
R24	3R3	0R	0R	0R
R22	330R	820R	820R	820R
R6	not mounted	not mounted	27k	27k
R7	not mounted	27k	not mounted	27k
R8	27k	not mounted	not mounted	not mounted
R9	27k	not mounted	27k	not mounted
TR1	FIELDTR_31.25S	FIELDTR_1S	FIELDTR_2.5S	FIELDTR_5S

NOTE: place a label indicating bus speed next to the marking resistors

selected variant

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<b>CERN</b>	
Sheet: /Fieldbus/	
File: fieldbus.sch	
<b>Title: FMC-nanoFIP fieldbus interface</b>	
Size: A4	Date: 2017-06-16
KiCad E.D.A. kicad (2017-06-12 revision 19d5cc754)-master	Rev: 2
	Id: 7/7