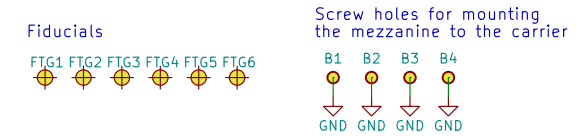
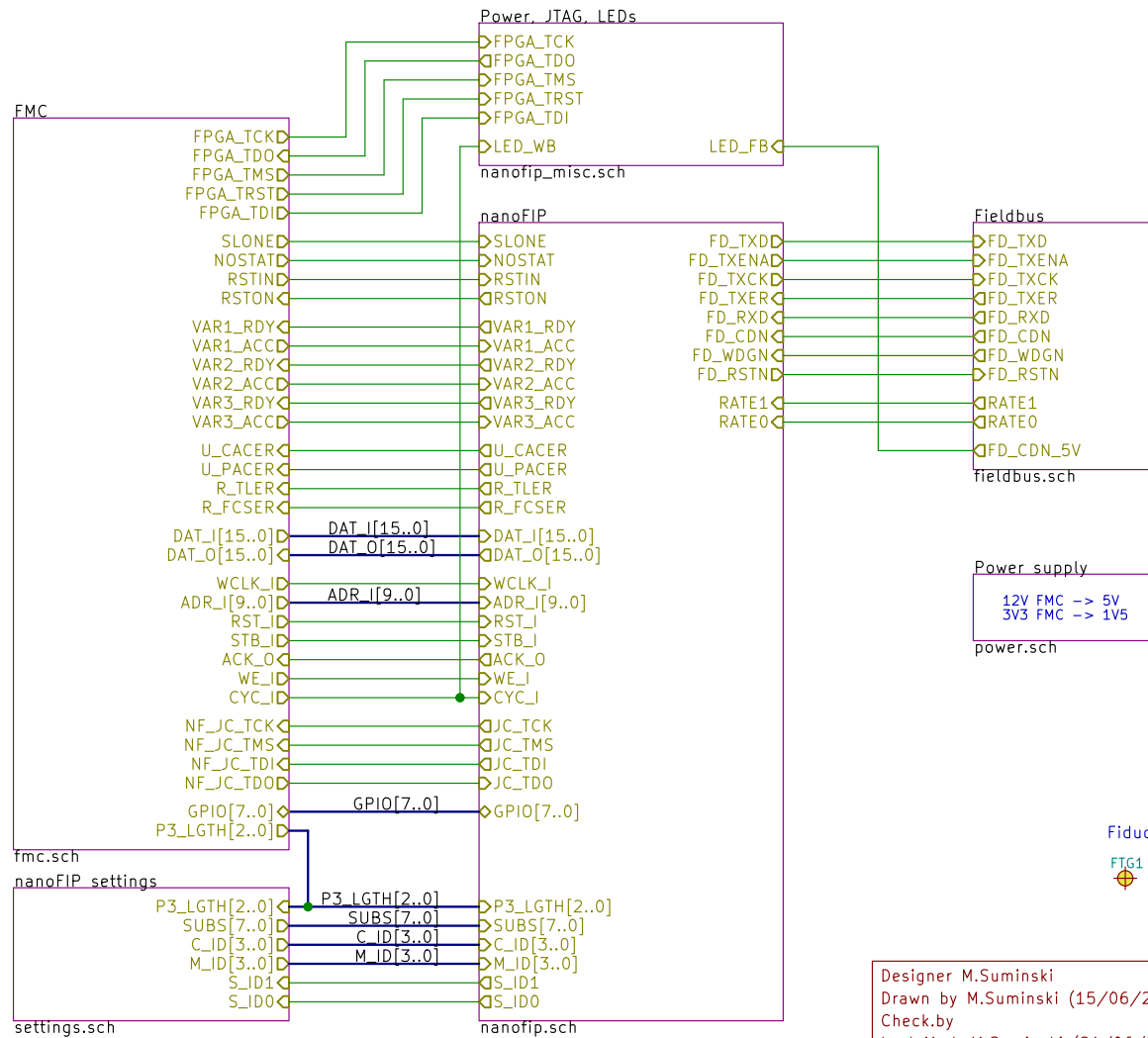


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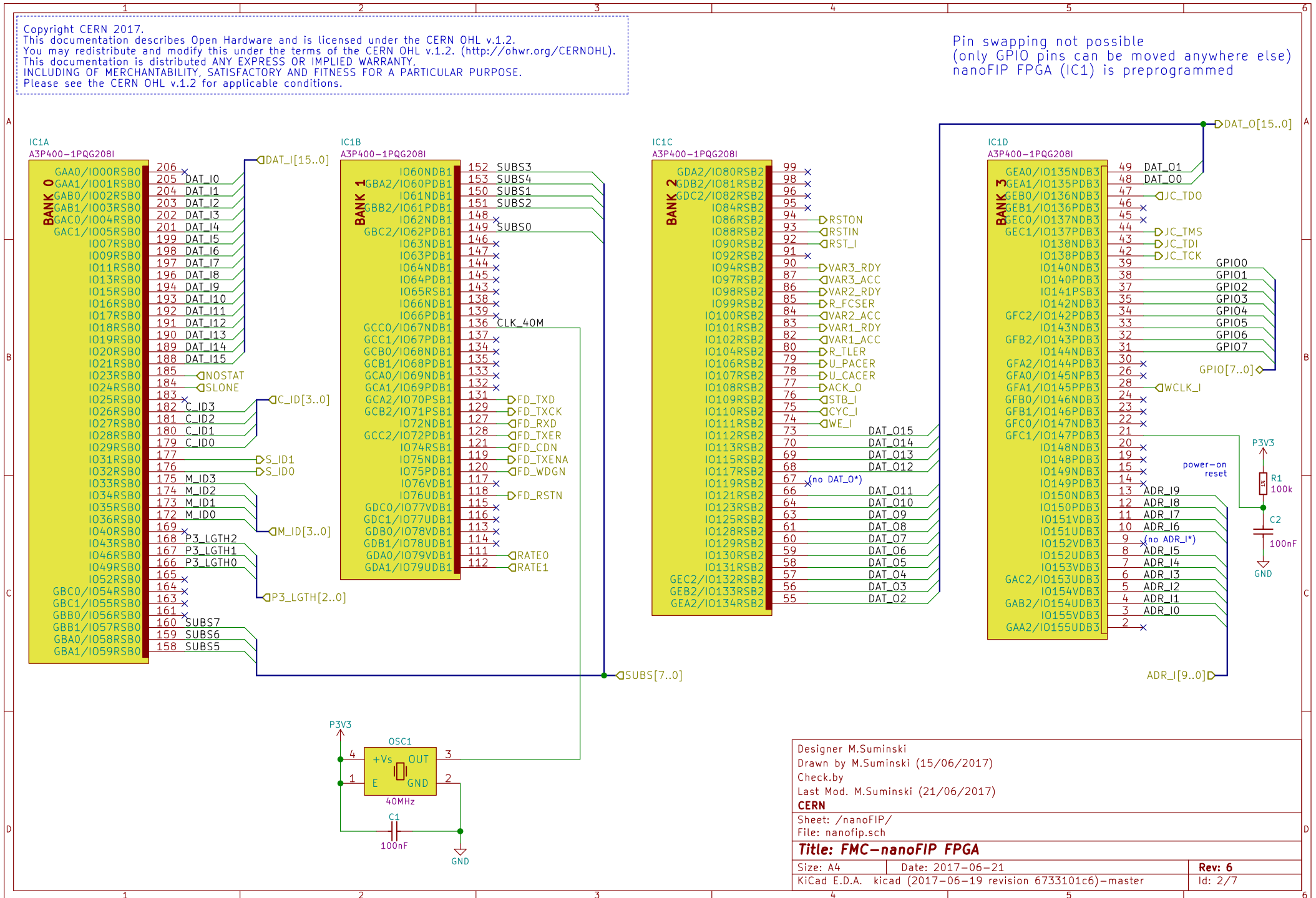
<http://www.ohwr.org/projects/fmc-nanofip>



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 Sheet: /
 File: fmc-nanofip.sch
Title: FMC-nanoFIP
 Size: A4 | Date: 2017-06-21 | Rev: 6
 KiCad E.D.A. kicad (2017-06-19 revision 6733101c6)-master | Id: 1/7

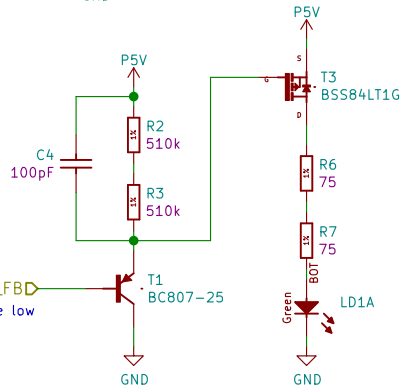
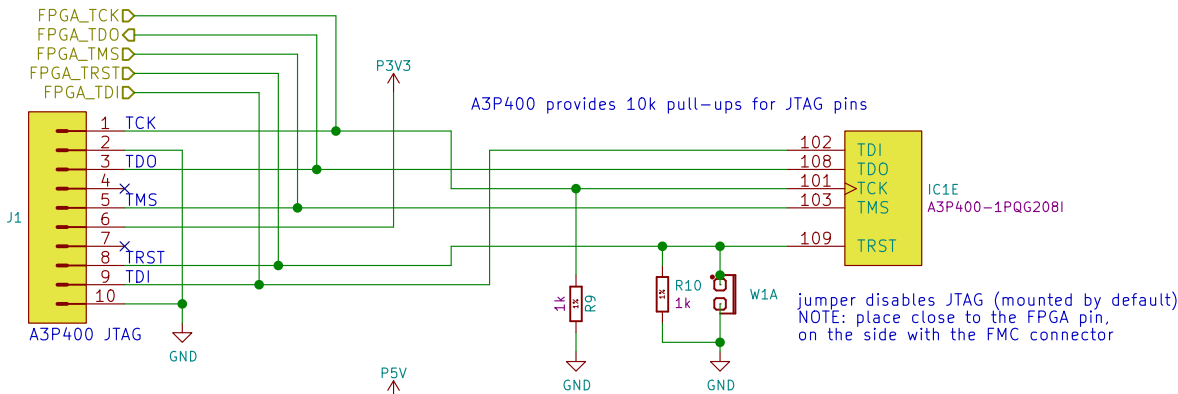
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Pin swapping not possible
 (only GPIO pins can be moved anywhere else)
 nanoFIP FPGA (IC1) is preprogrammed

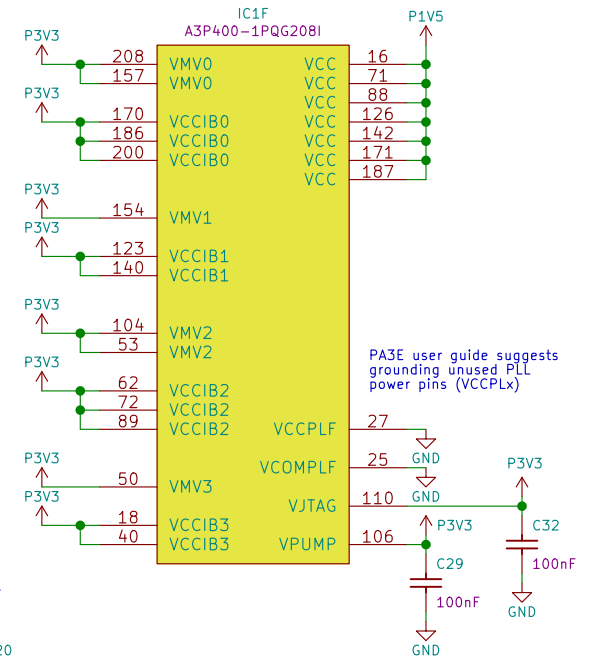
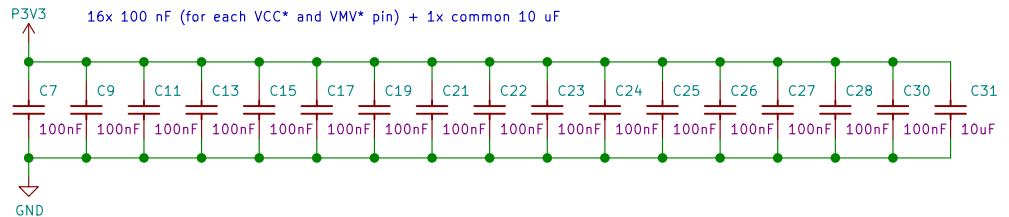
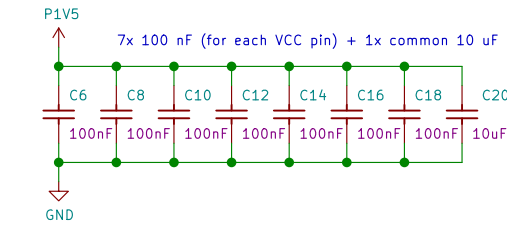
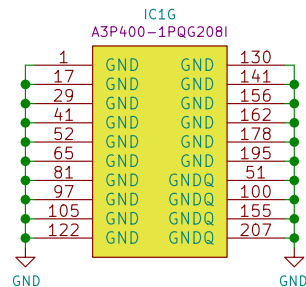
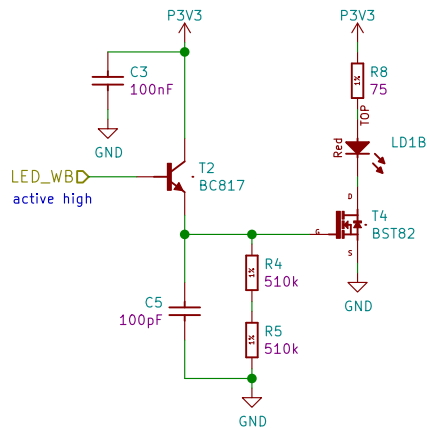


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Sheet: /nanoFIP/	
File: nanofip.sch	
Title: FMC-nanoFIP FPGA	
Size: A4	Date: 2017-06-21
KiCad E.D.A. kicad (2017-06-19 revision 6733101c6)-master	Rev: 6
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one-shot triggers to extend LED pulse duration



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Sheet: /Power, JTAG, LEDs/
 File: nanofip_misc.sch

Title: FMC-nanoFIP JTAG, power, LEDs

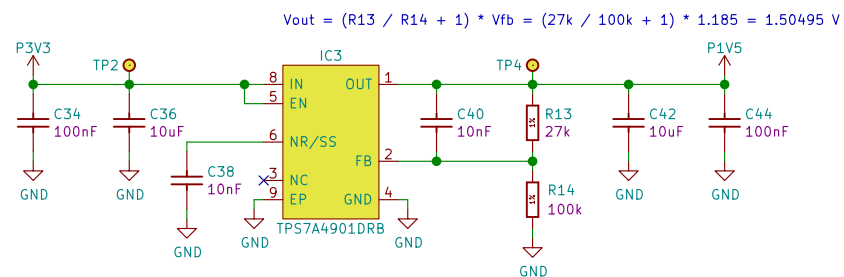
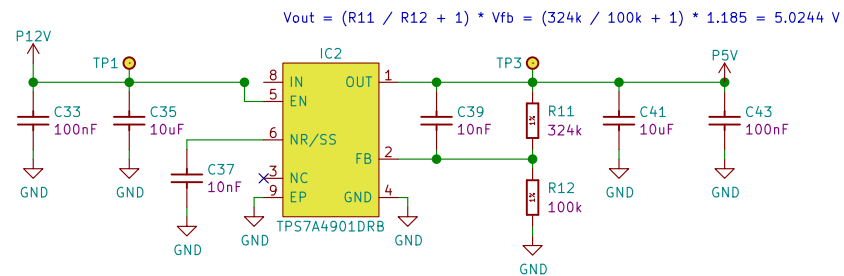
Size: A4 Date: 2017-06-21

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Sheet: /Power supply/
 File: power.sch

Title: FMC-nanoFIP power supply

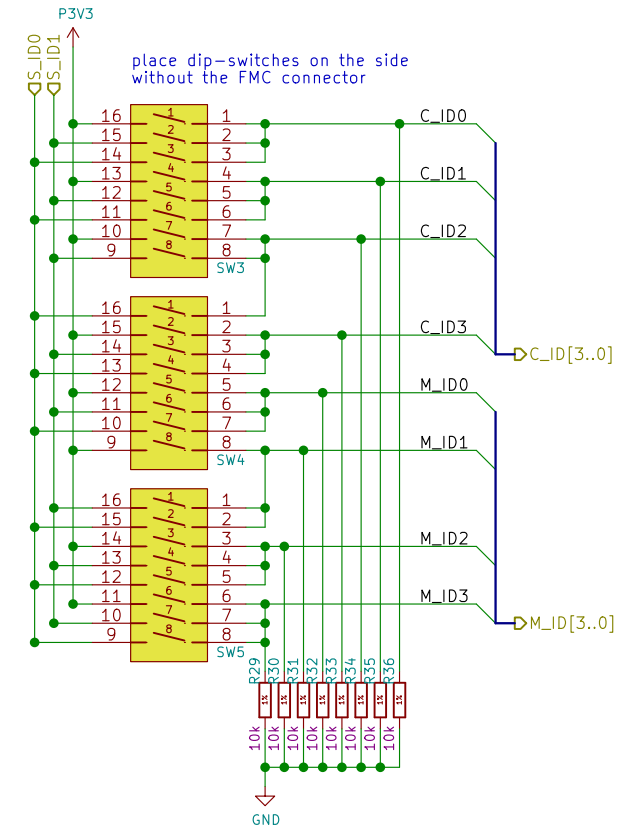
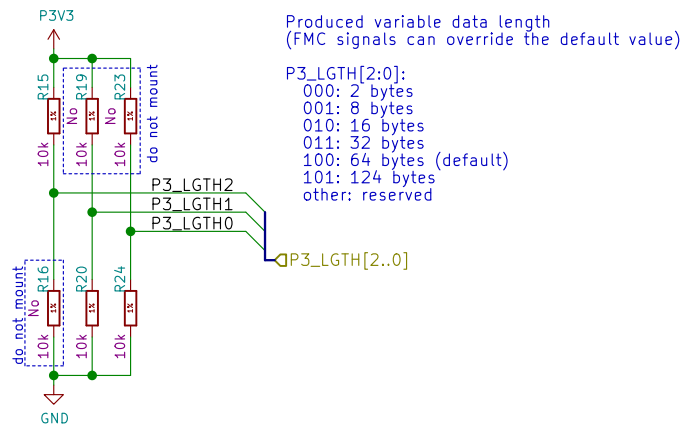
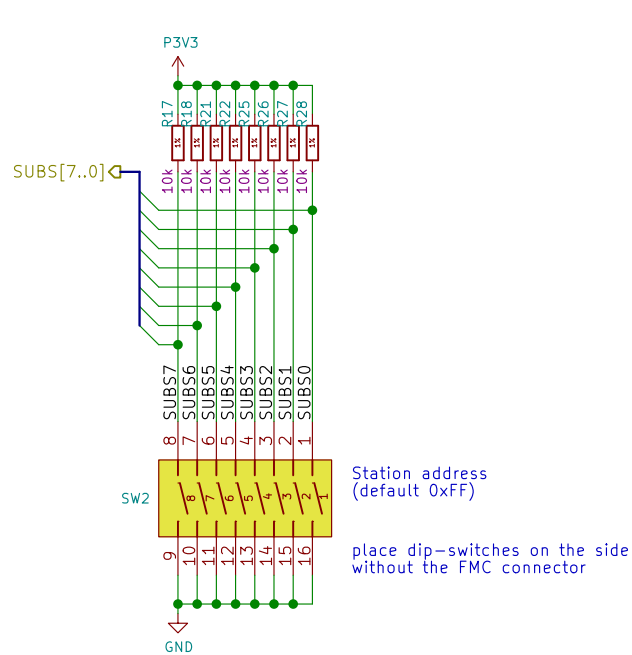
Size: A4 Date: 2017-06-21

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Constructor & Model ID (default 0x00)

C_ID[i]/M_ID[i] connected to: Gnd S_ID0 S_ID1 Vcc
 Constructor/Model[2*i] 0 1 0 1
 Constructor/Model[2*i+1] 0 0 1 1

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Sheet: /nanoFIP settings/
 File: settings.sch

Title: FMC-nanoFIP settings

Size: A4 Date: 2017-06-21

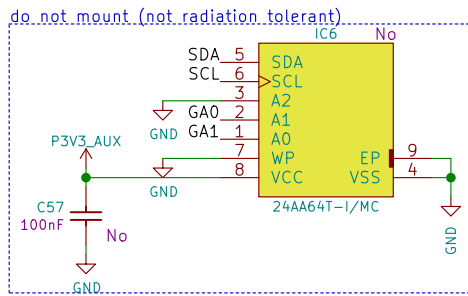
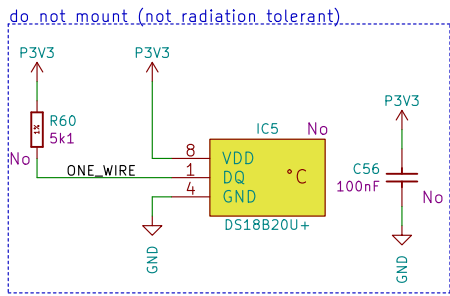
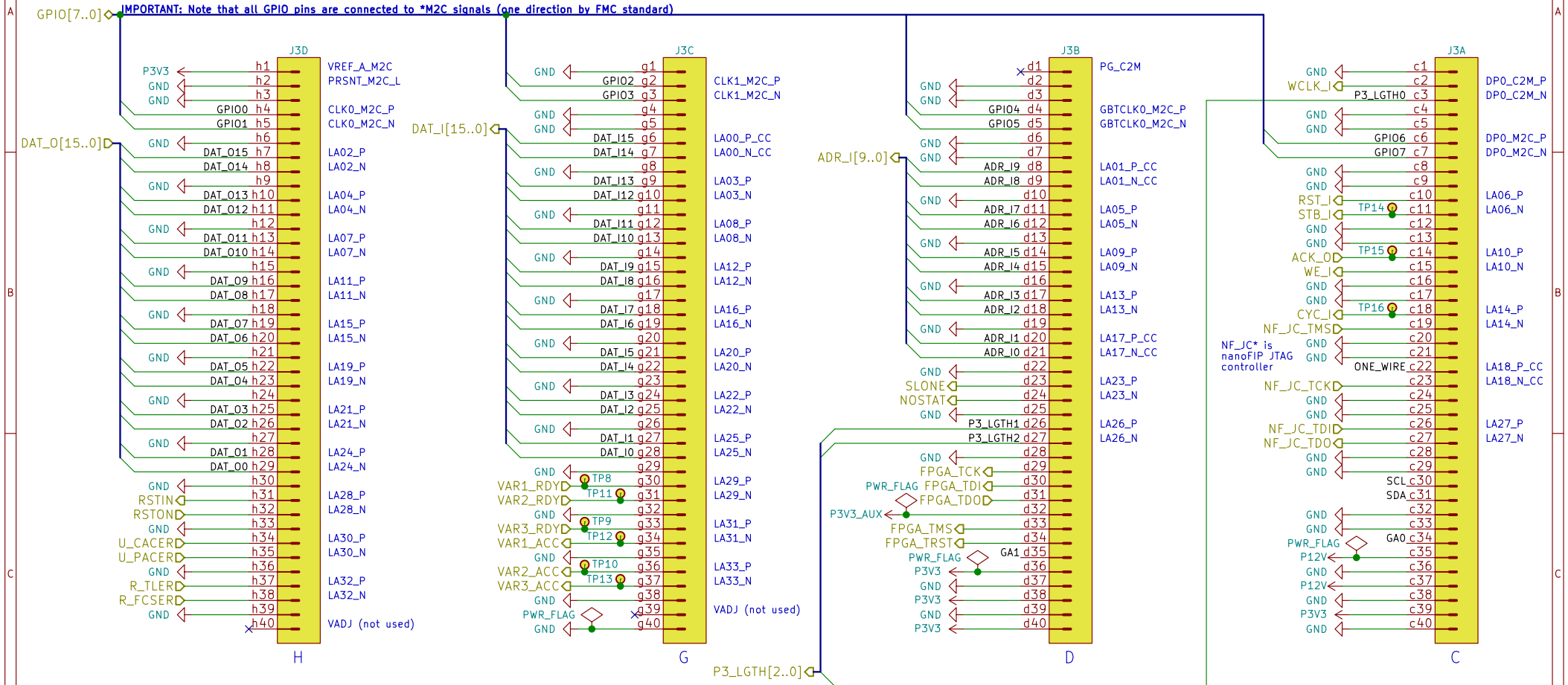
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NOTE: pin swapping between LA* pins possible
 Place test points and pin headers on the side without the FMC connector



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Sheet: /FMC/
 File: fmc.sch

Title: FMC-nanoFIP FMC connector (LPC)

Size: A4	Date: 2017-06-21	Rev: 6
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