

the VREF could not be used for the 2nd mezzanine
The 2nd mezzanine has 2 differential IO pairs less

A

B

C

D

E

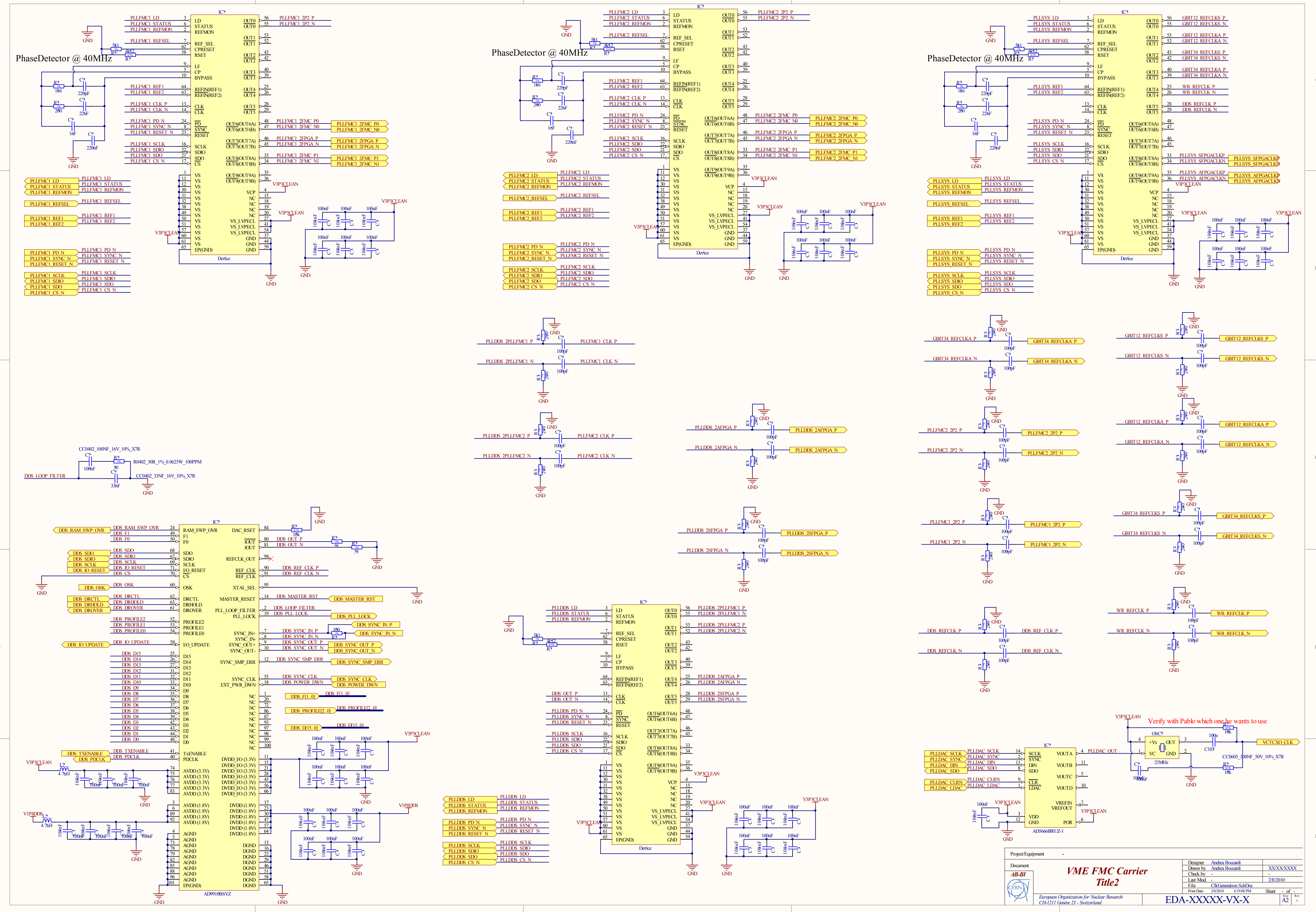
A

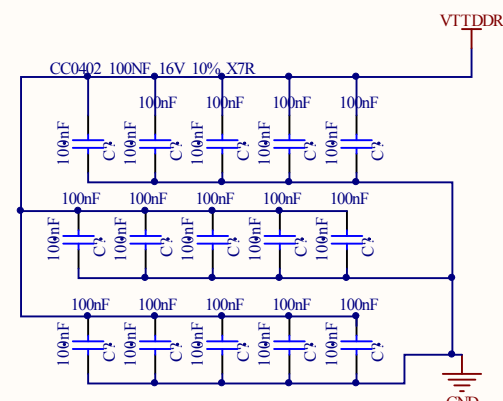
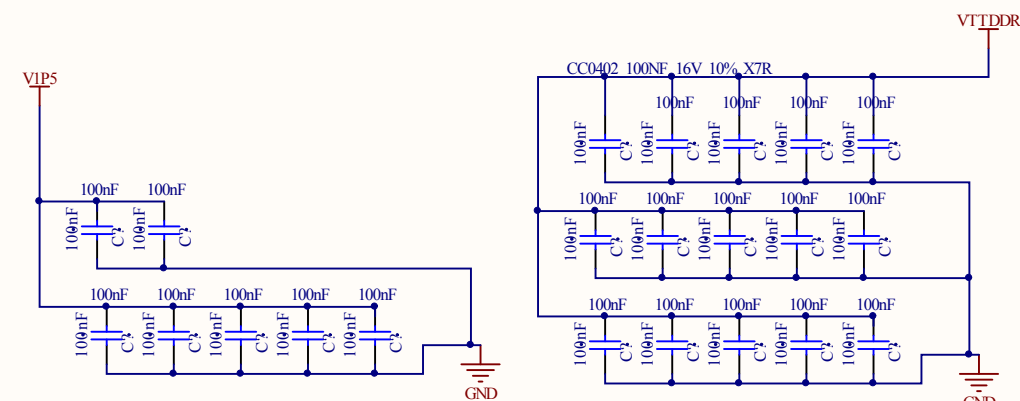
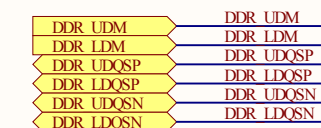
B





C

D

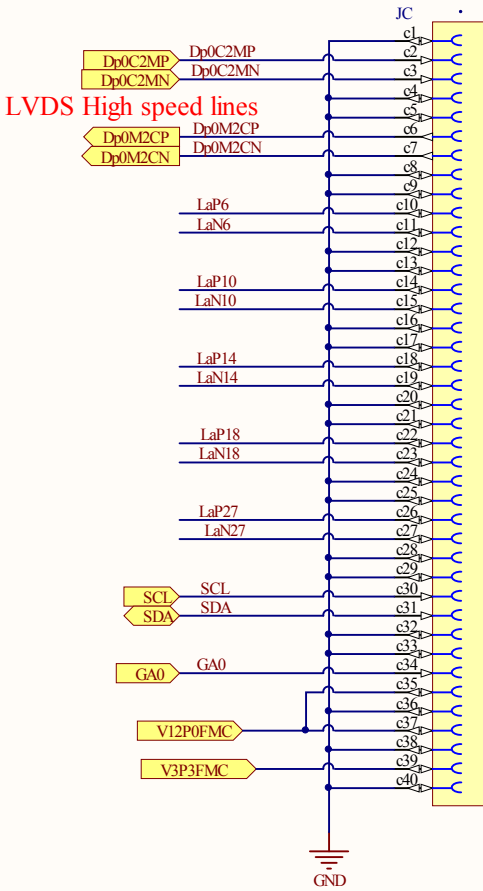
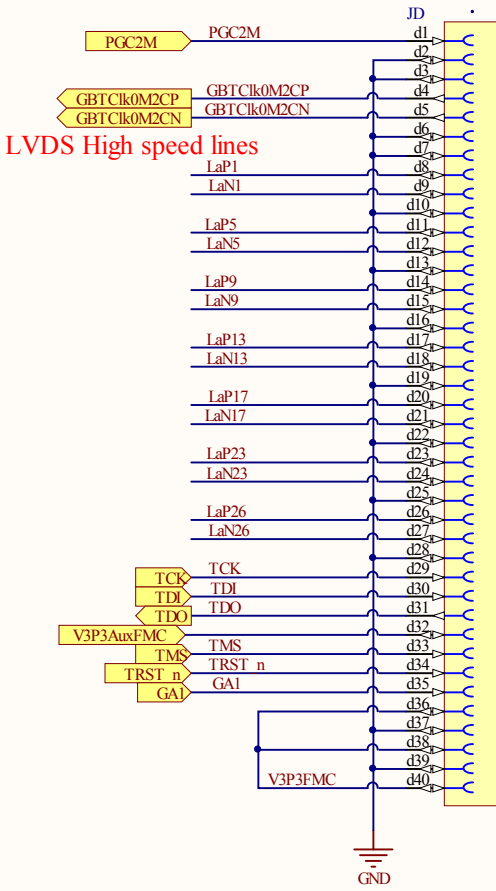
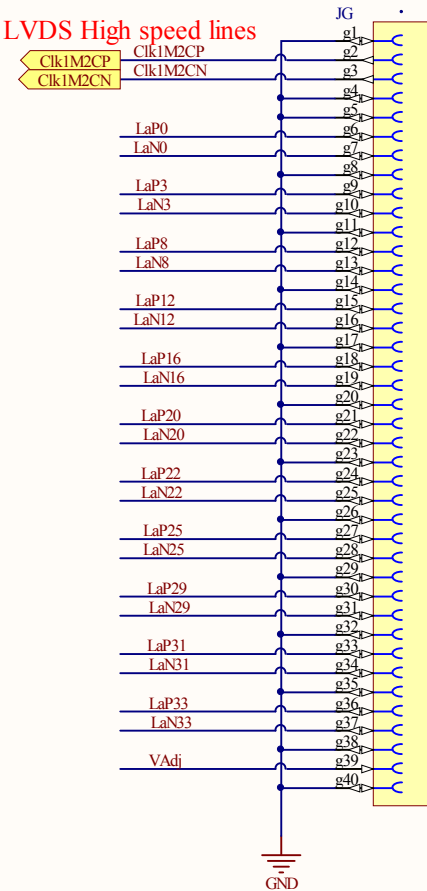
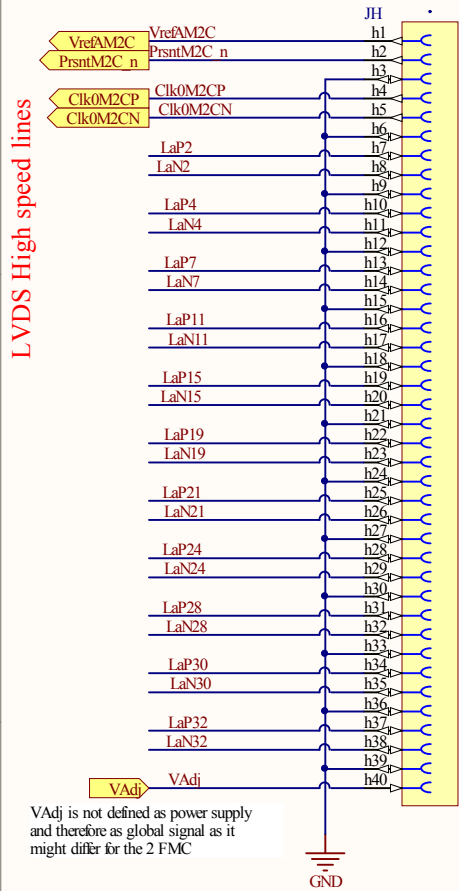
E



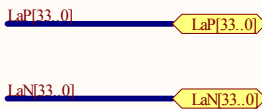


Project/Equipment		-	
Document		Designer	Andrea Boccardi
 		Drawn by	Andrea Boccardi
		Check by	-
		Last Mod.	-
		File	DDR3_SchDoc
		Print Date	28/2010 6:19:08 PM
		Sheet	- of -
		Size	A3
			Rev

Low Pin Count Rows

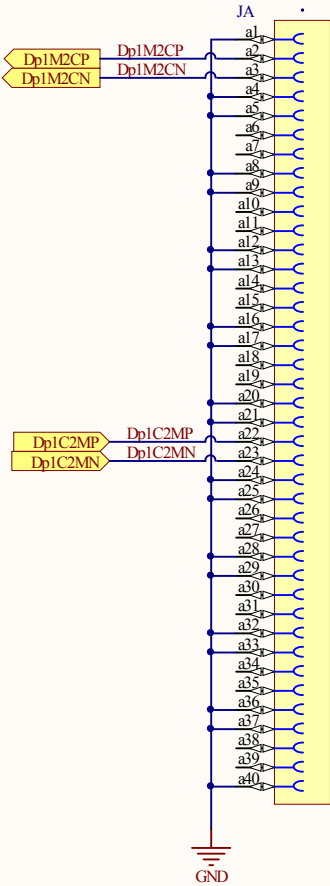
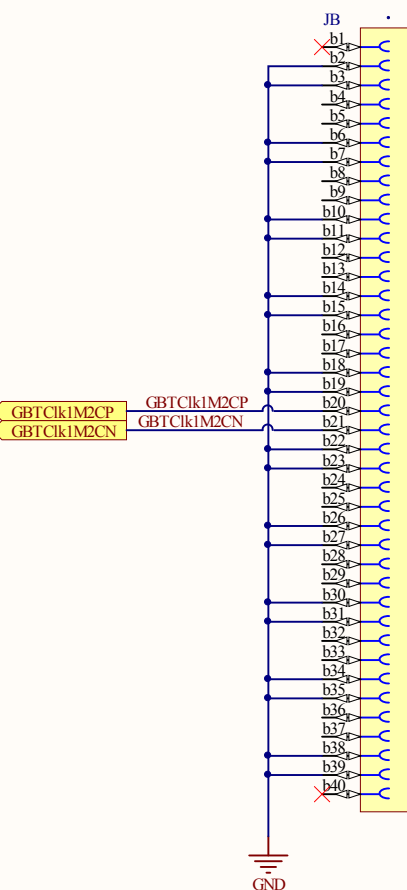
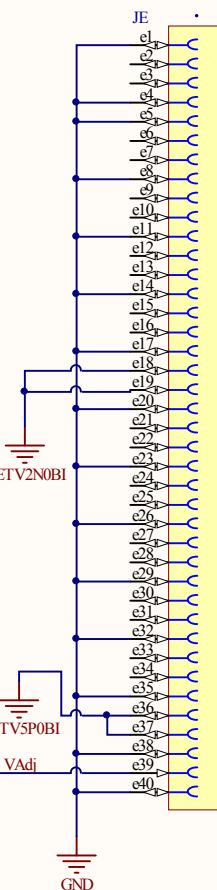
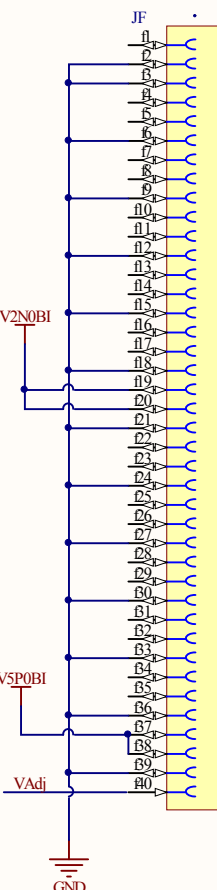
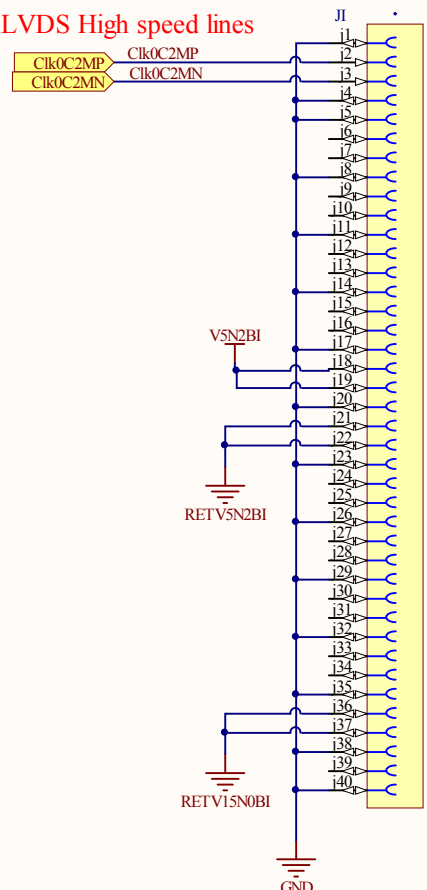
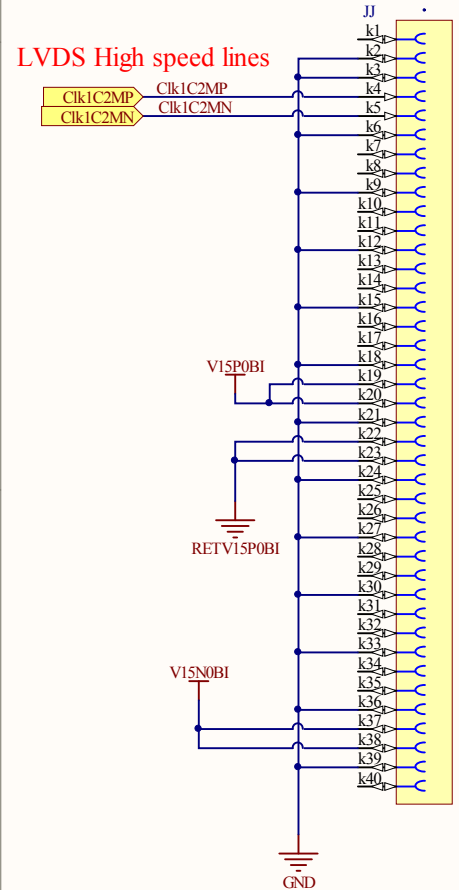


LaP and LaN are LVDS lines

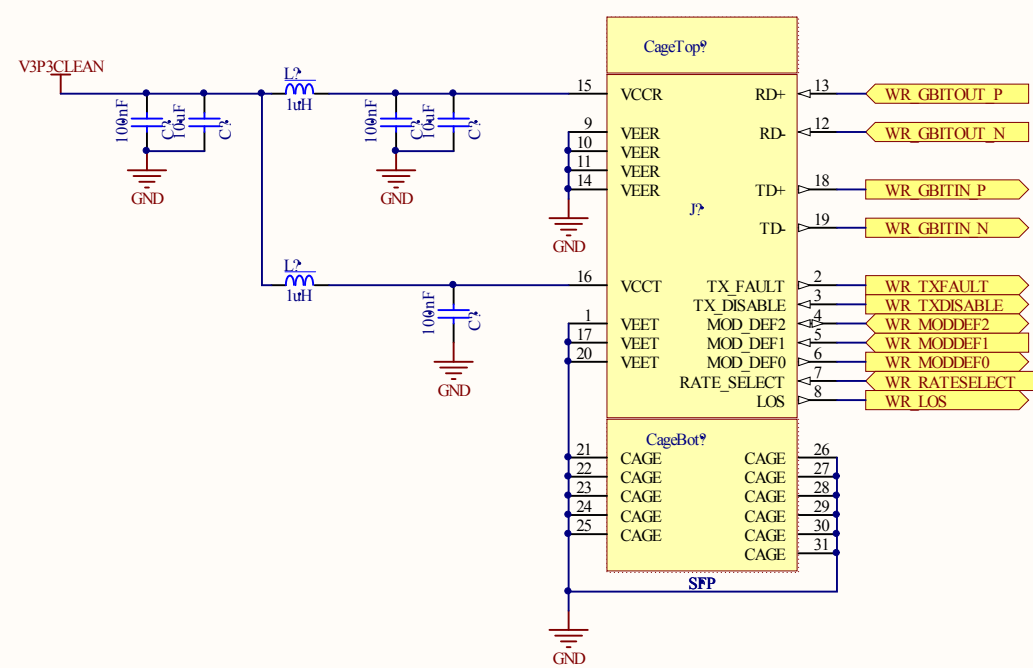
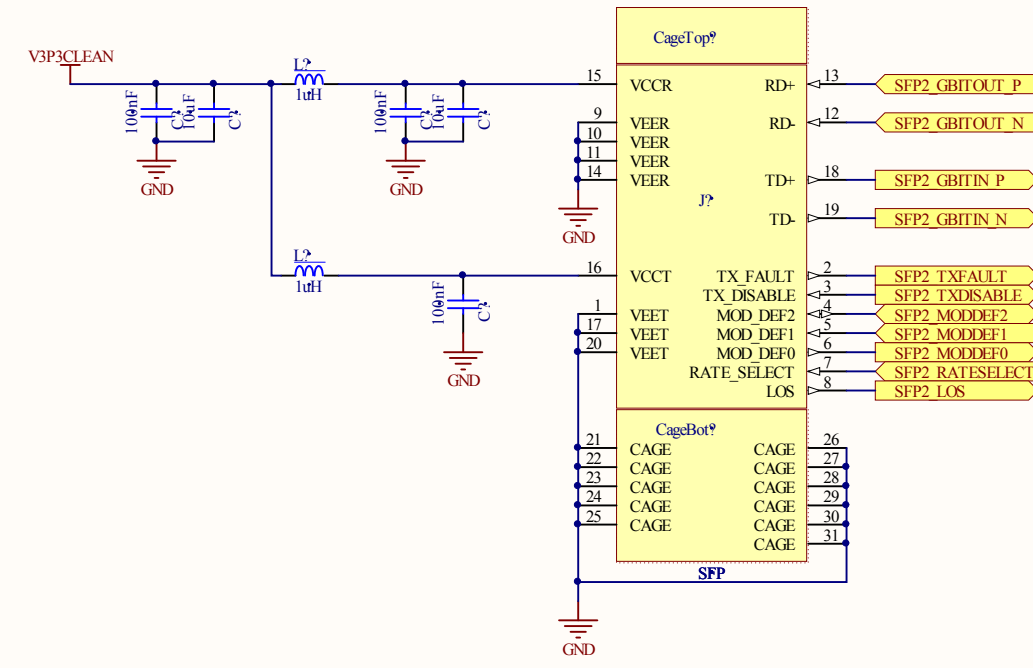
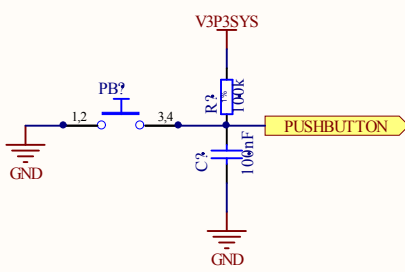
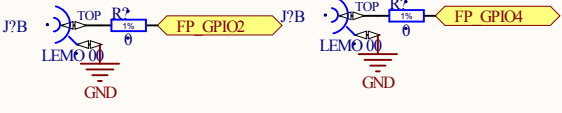
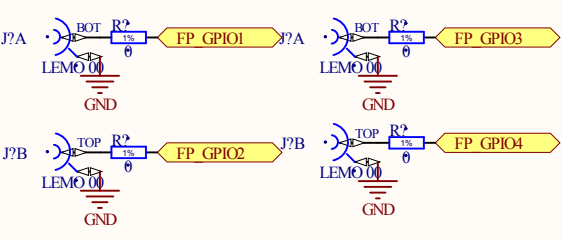
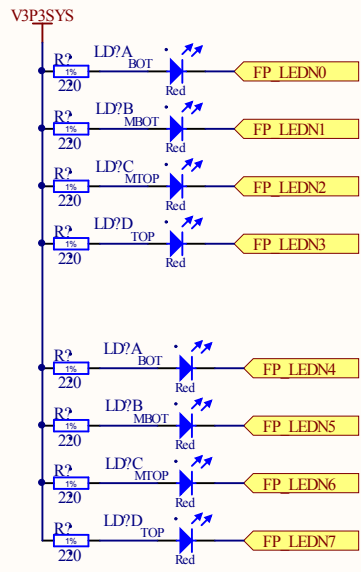


NB: the LVDS pairs must have a differential impedance of 100 ohm and be routed with no skew between the P and the N lines. The skew between the various La pairs should be kept as low as possible.

High Pin Count Rows



Project/Equipment		-	
Document		Designer	Andrea Boccardi
BE-BI-OP		Drawn by	Andrea Boccardi
		Check by	-
CERN		Last Mod.	2/8/2010
		File	FmcConnector.SchDoc
European Organization for Nuclear Research CH-1211 Genève 23 - Switzerland		Print Date	2/8/2010 6:19:09 PM
		Sheet	- of -
EDA-XXXXX-VX-X		Size	A3
		Rev	-



A

B

C

D

E

2

3

4

5

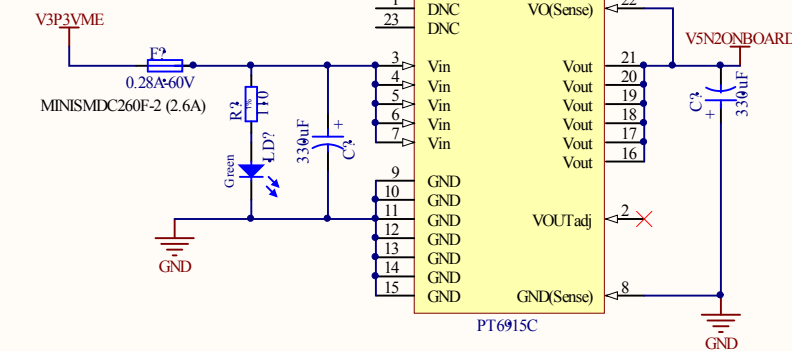
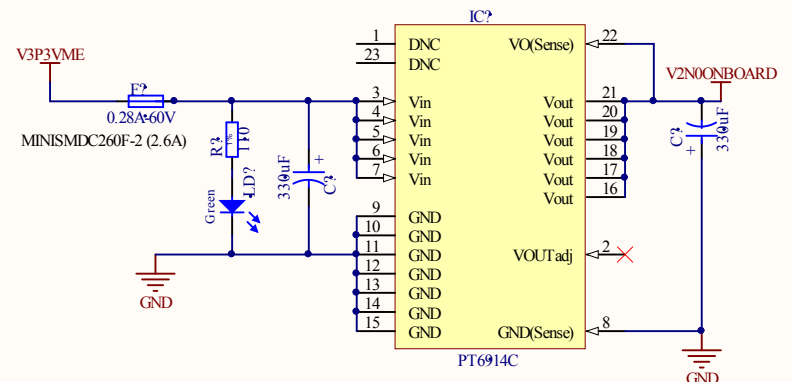
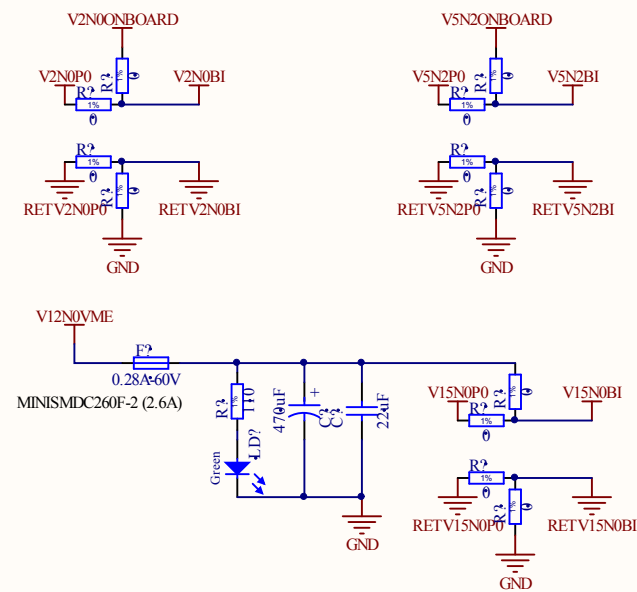
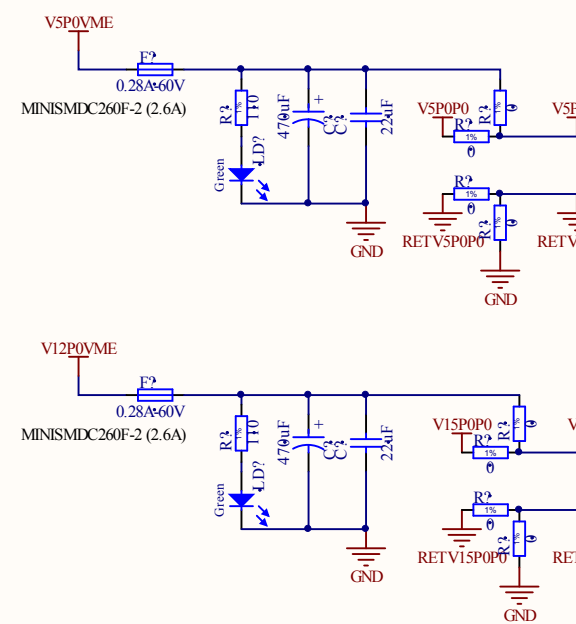
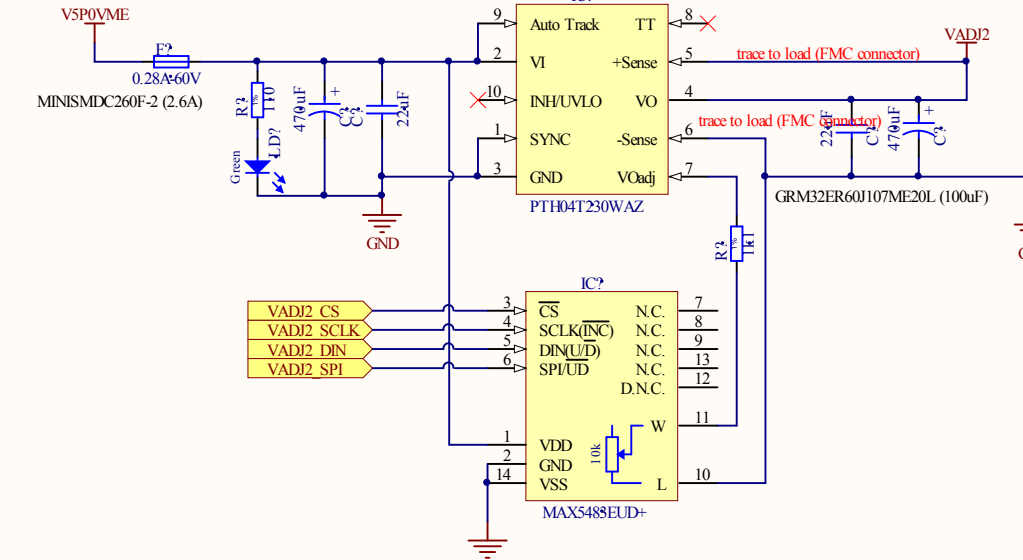
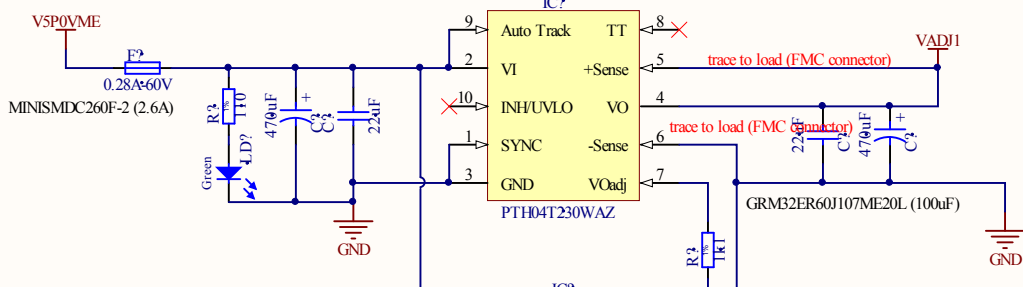
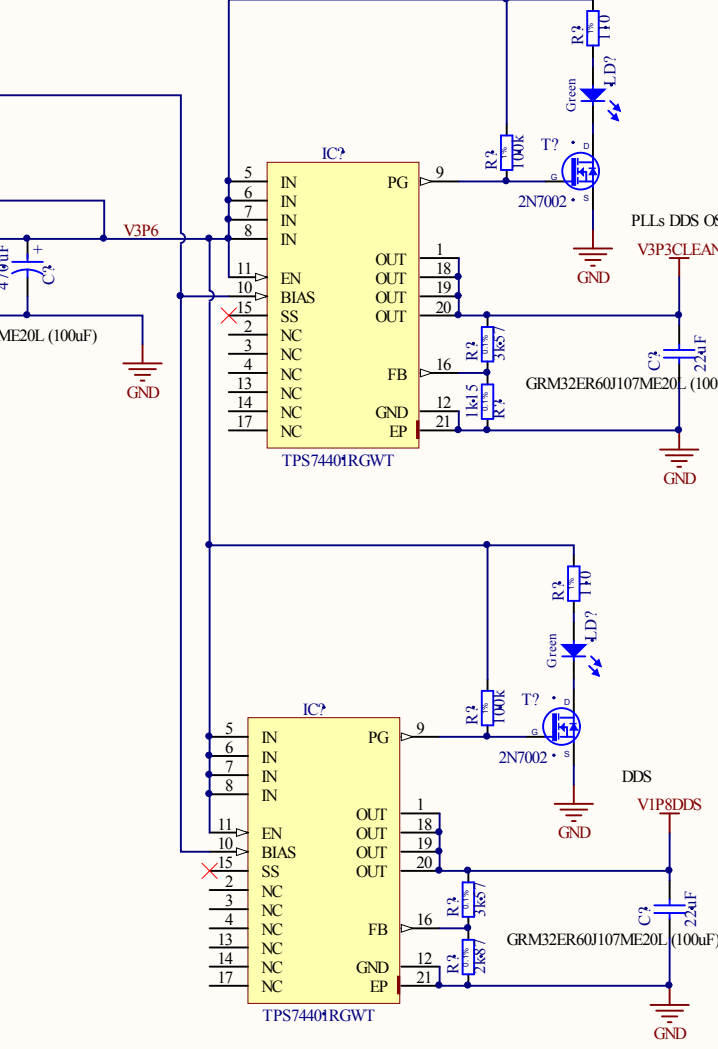
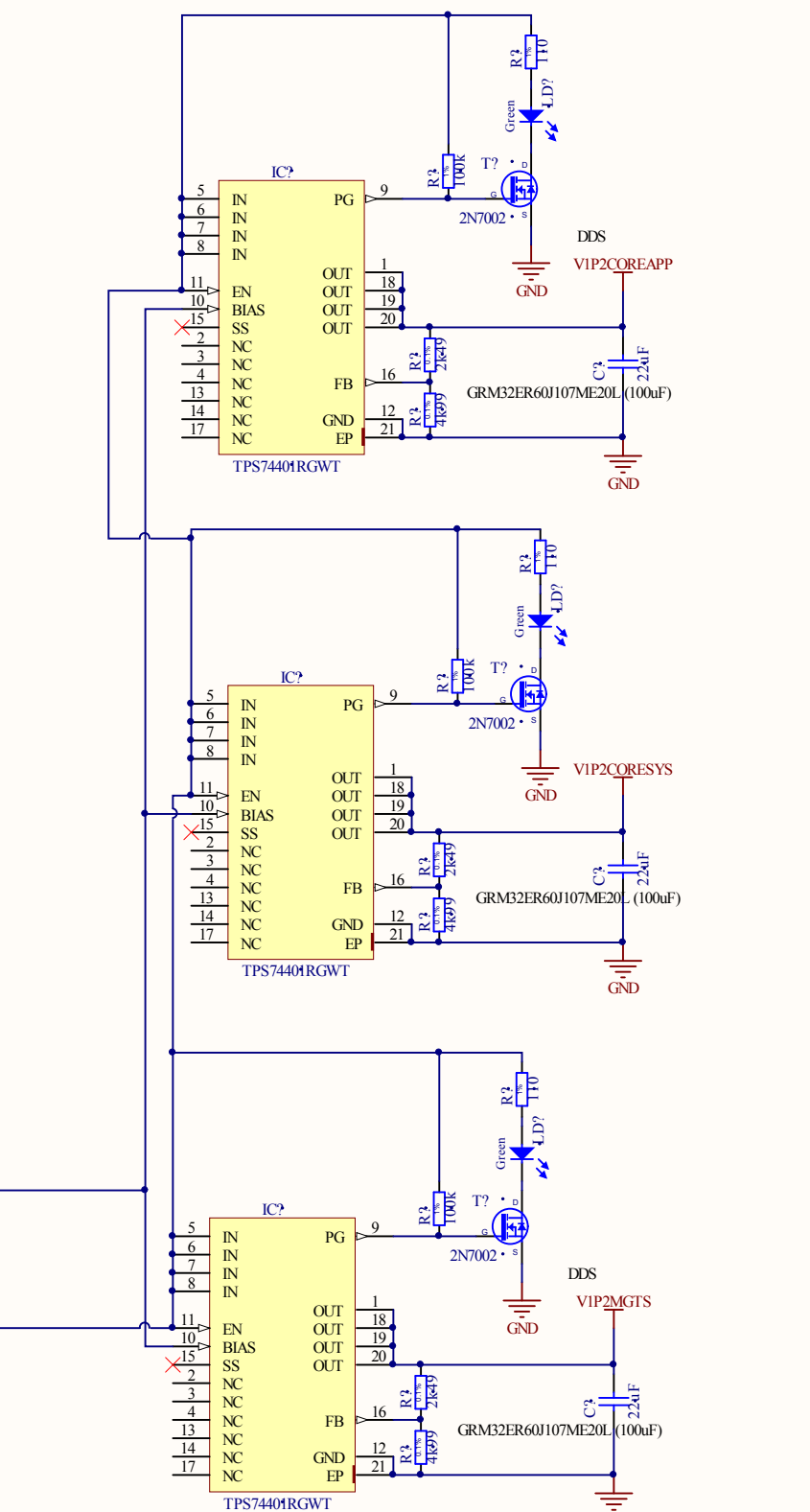
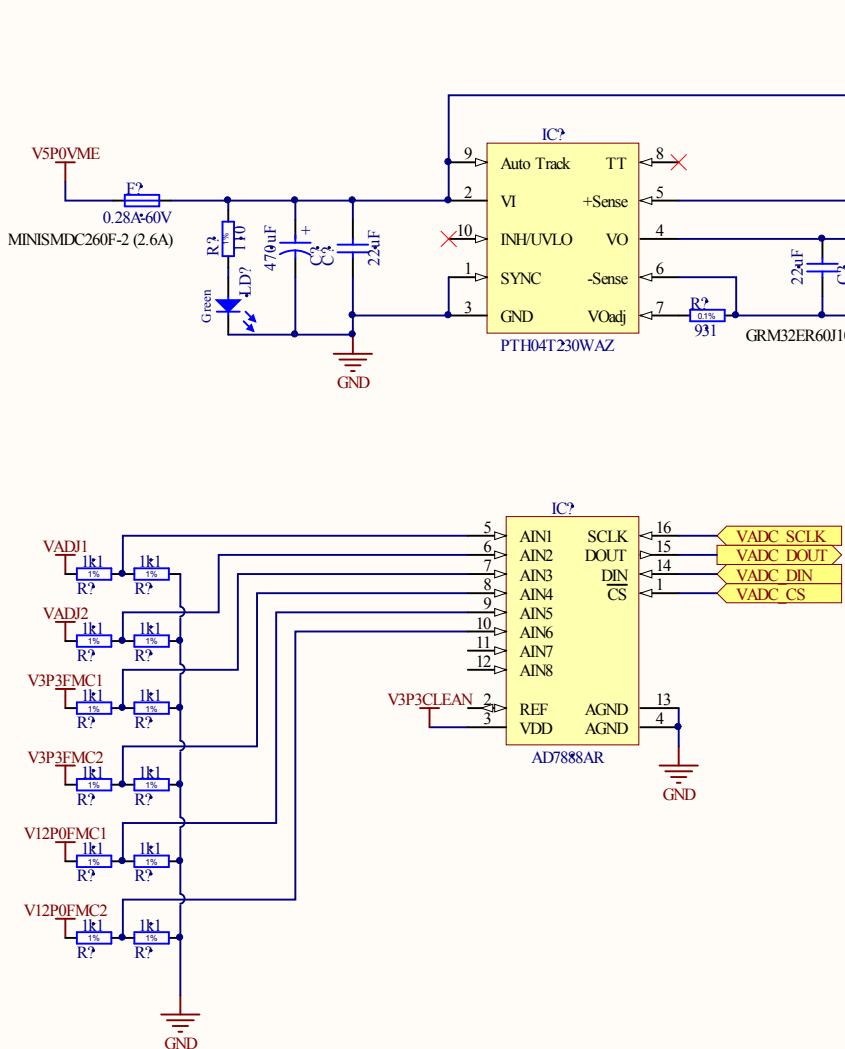
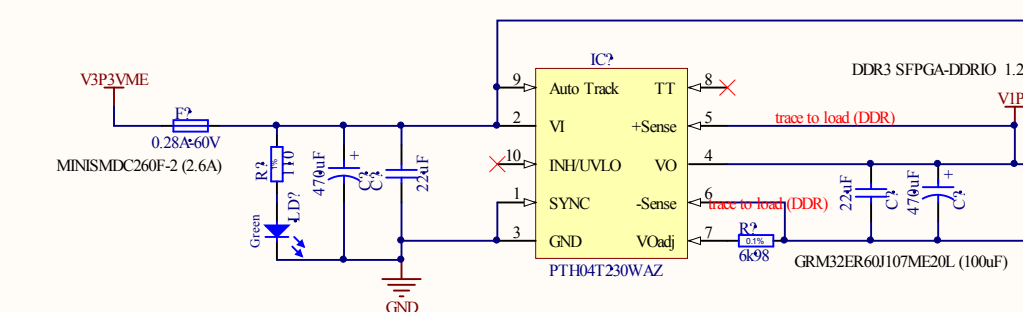
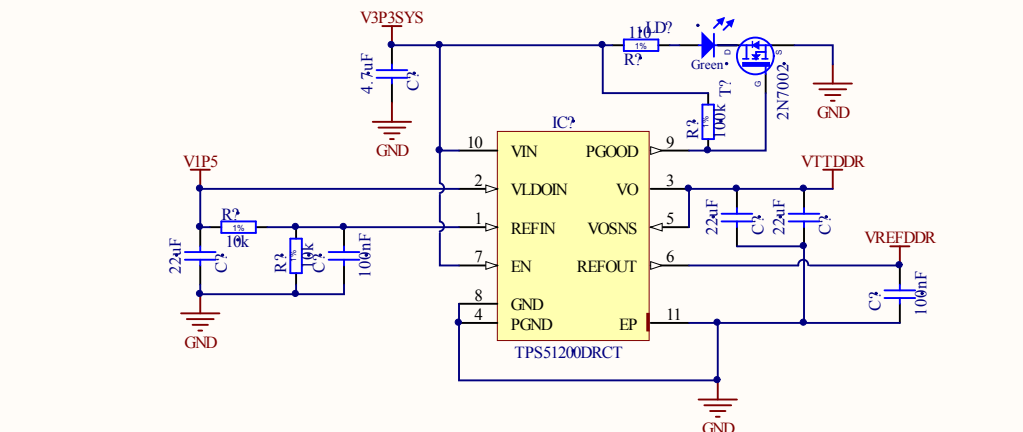
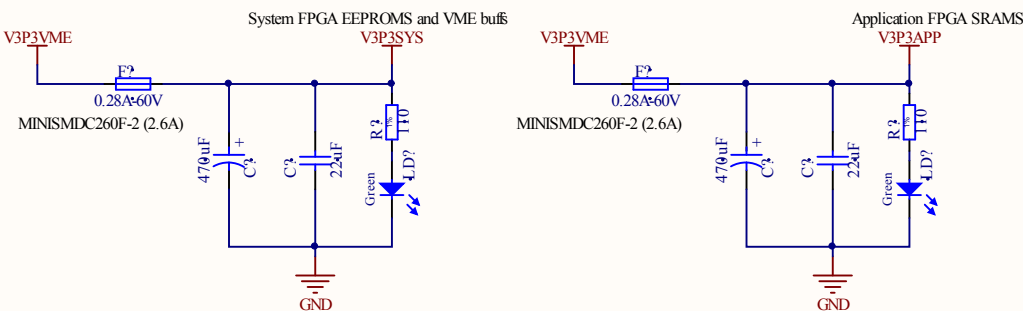
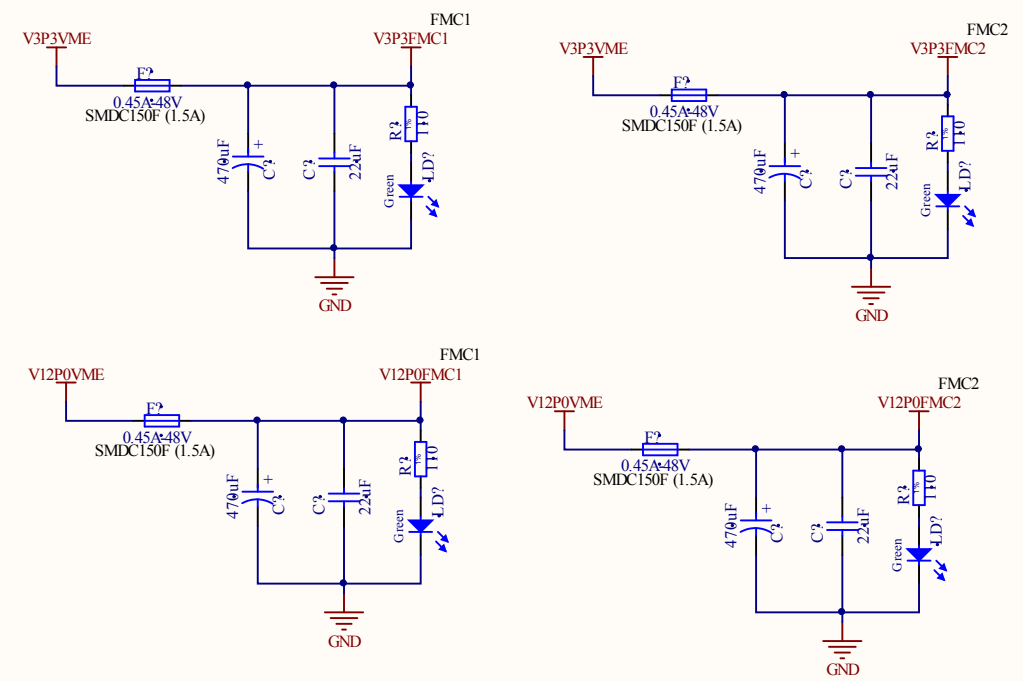
A

B

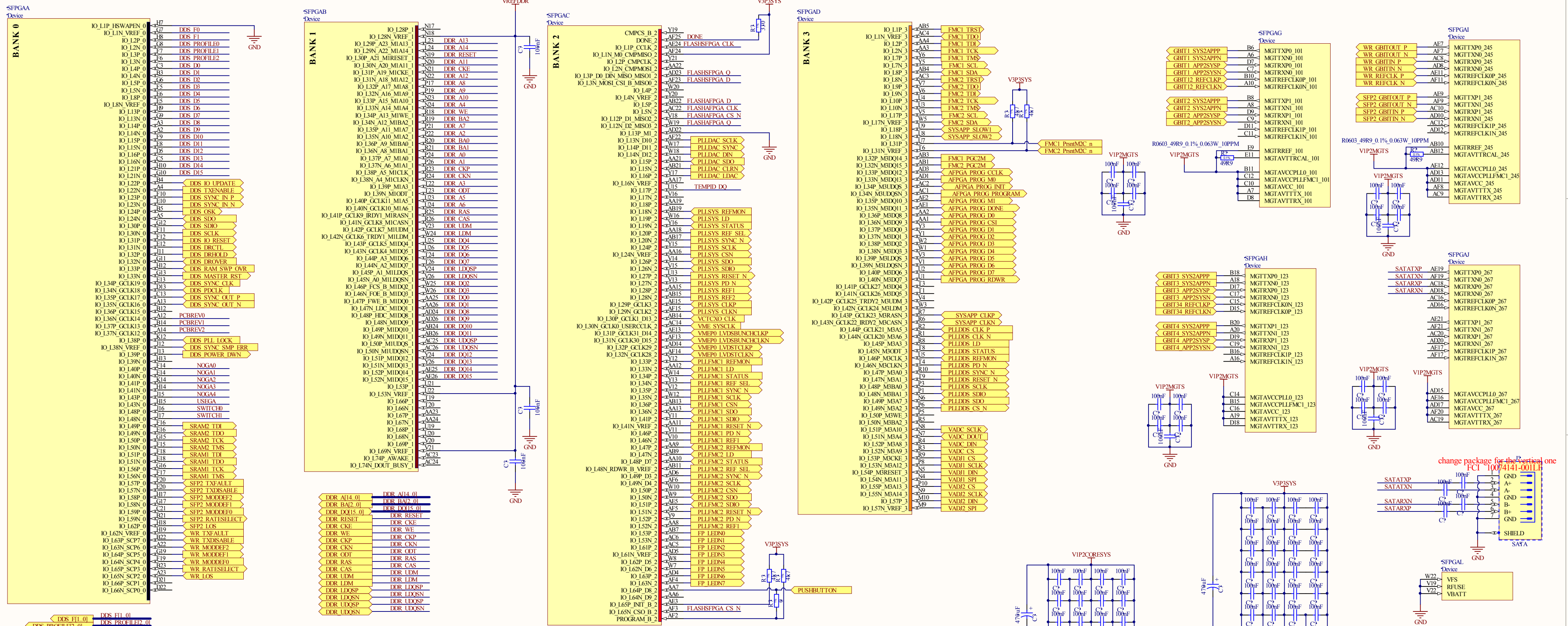
C

D

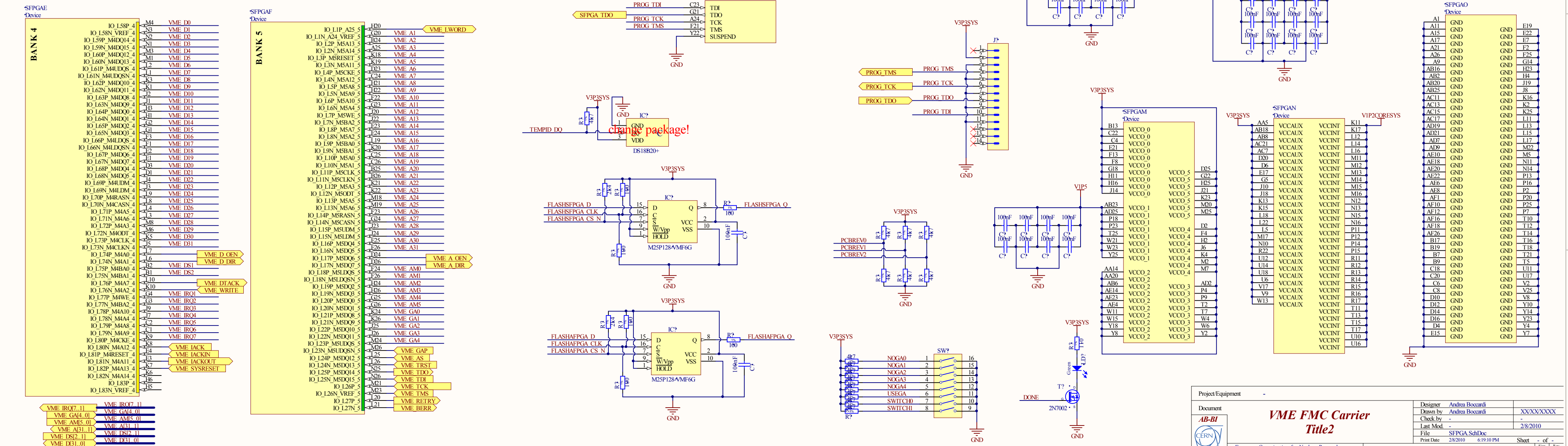
E



Project/Equipment	-	Designer	Andrea Boccardi	Check by	Andrea Boccardi	XX/XX/XXXX
Document	-	Drawn by	Andrea Boccardi	Last Mod.	-	2/8/2010
File	PowerSupplies_SchDoc	Print Date	2/8/2010	6:19:09 PM	Sheet	- of -
EDA-XXXXX-VX-X	European Organization for Nuclear Research CERN-1211 Genève 23 - Switzerland	Rev	A2	-	-	-



Add some TP (just vias) if there are spare pins



ADDRESS[24..0] ADDRESS[24..0] DATA[35..0] DATA[35..0]

Replace with the 3V3 version (same pinout)

