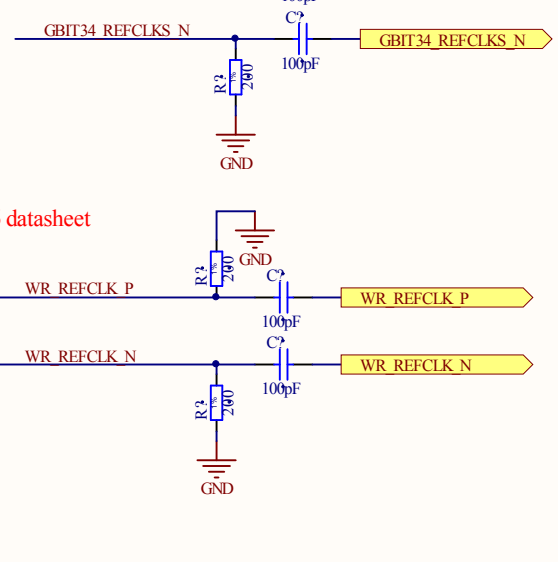
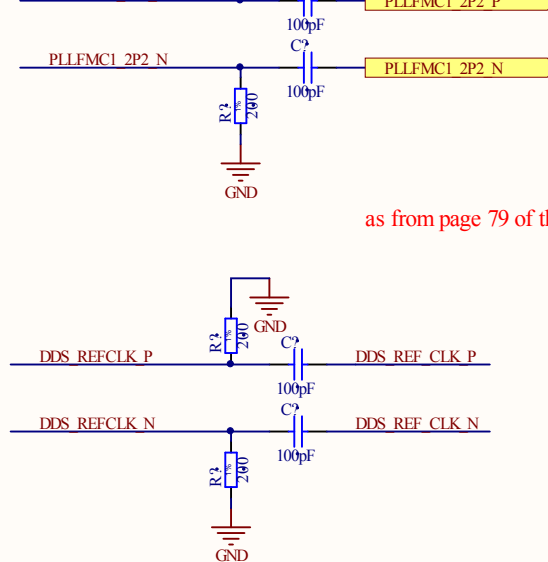
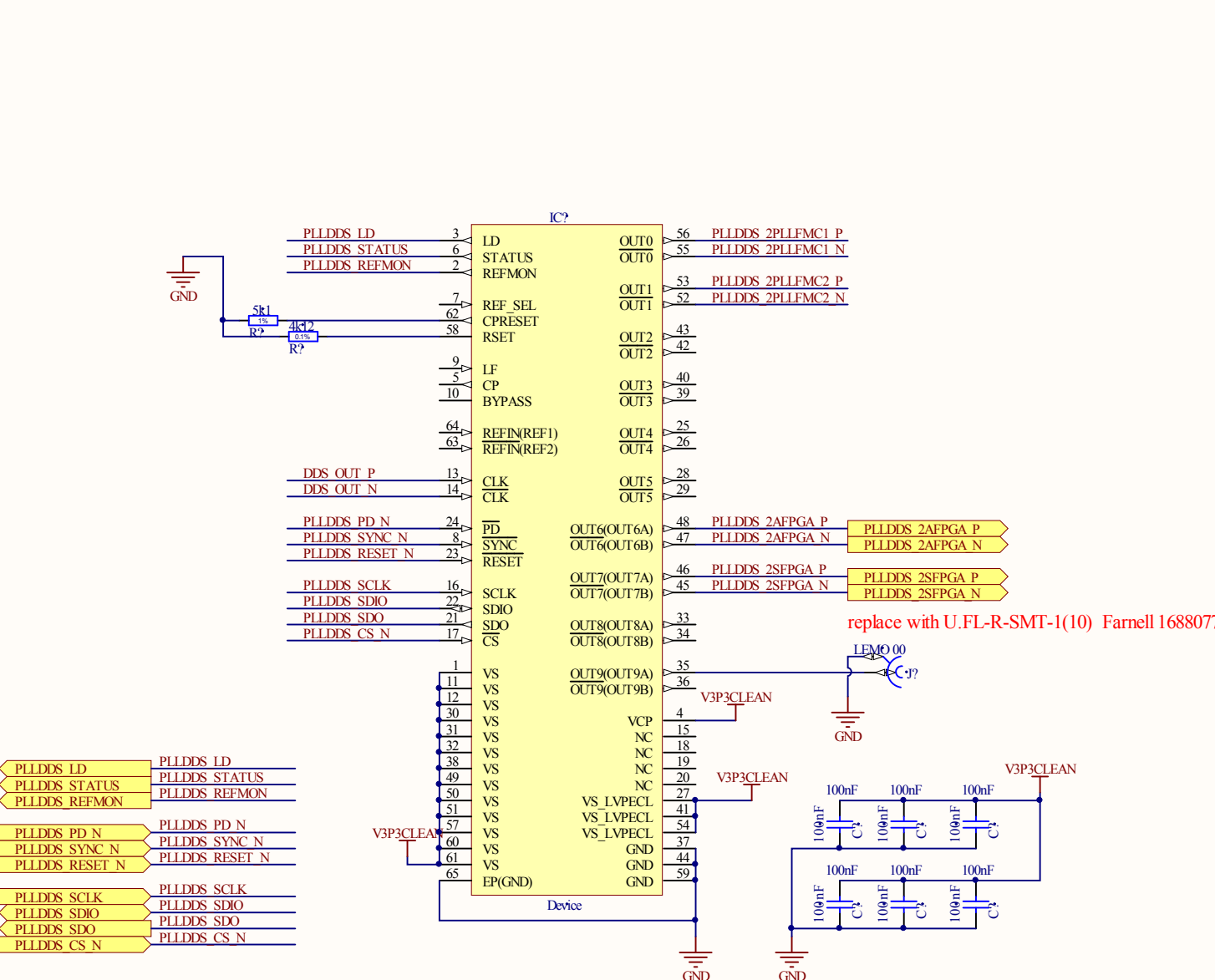
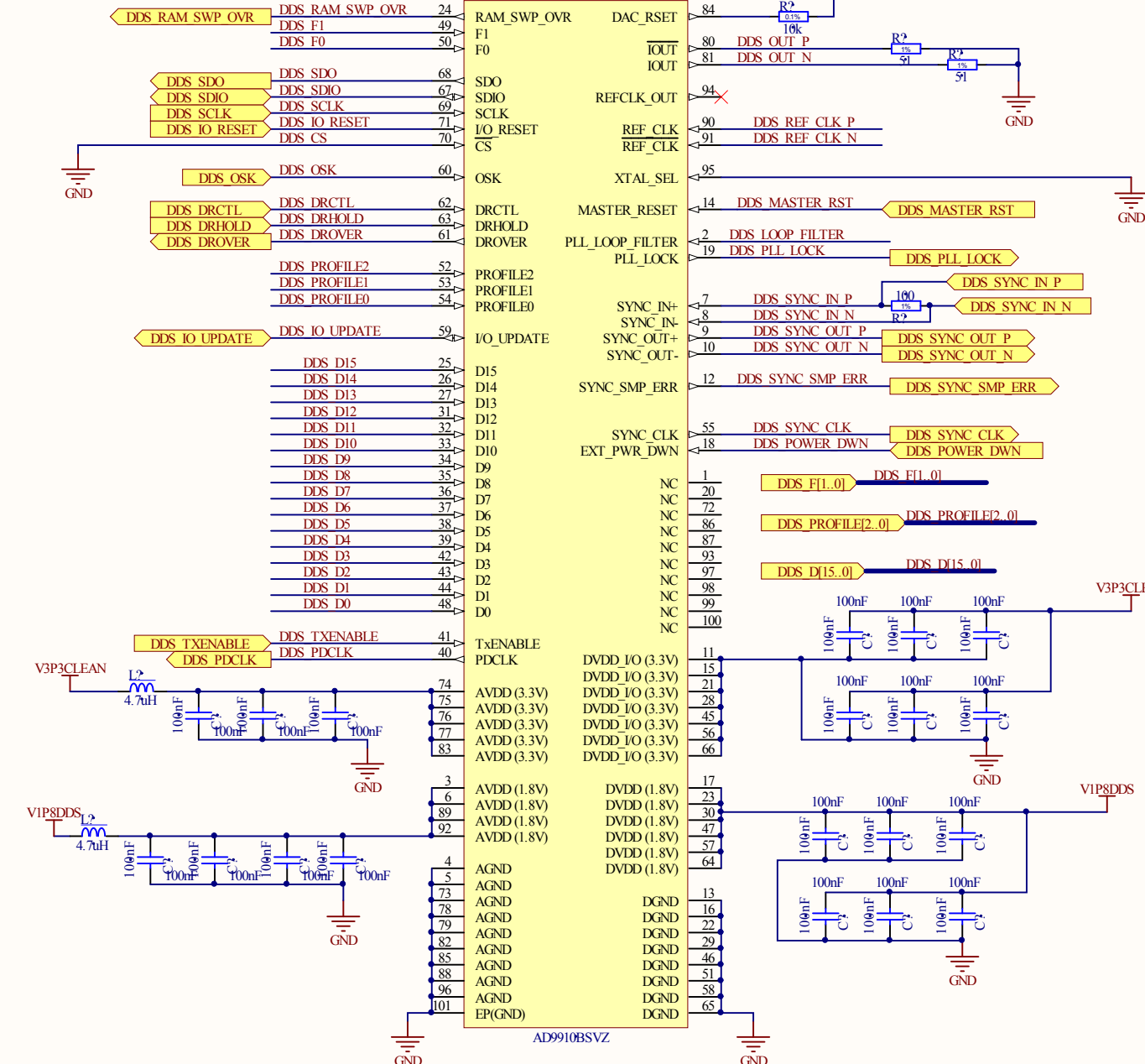
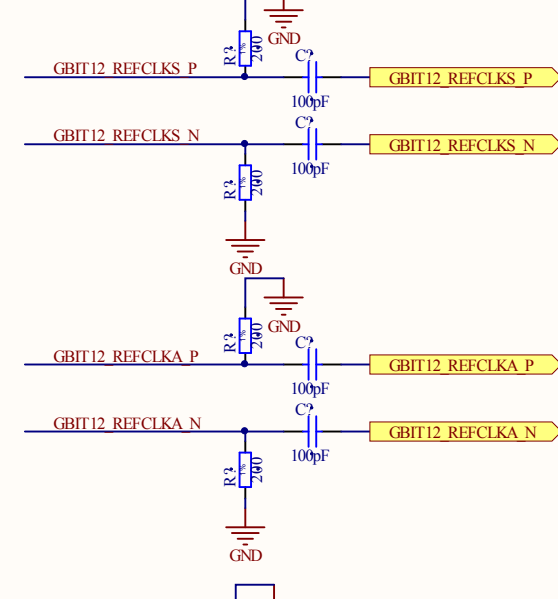
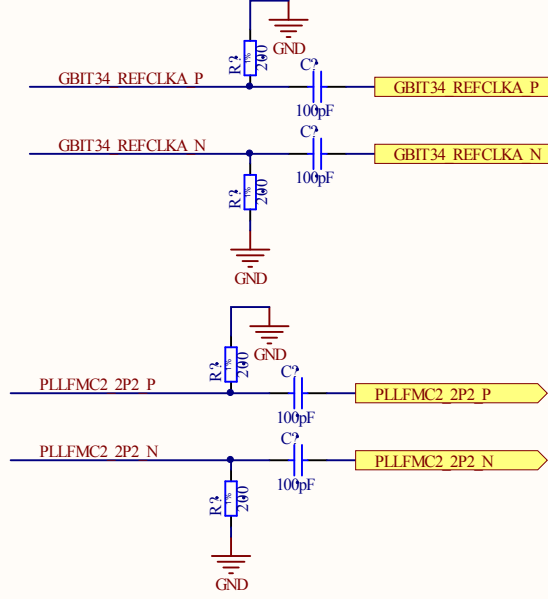
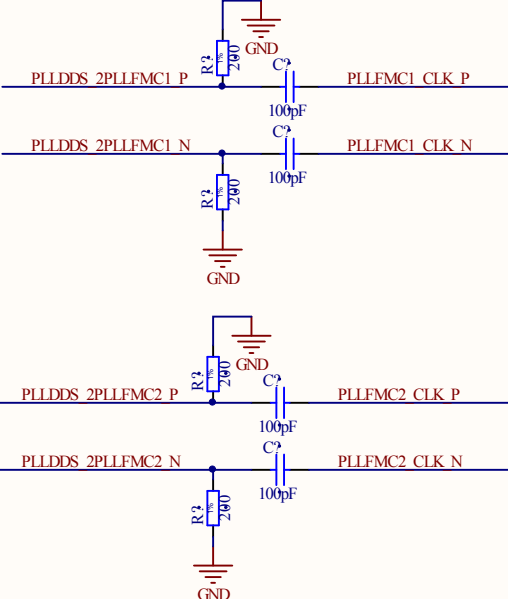
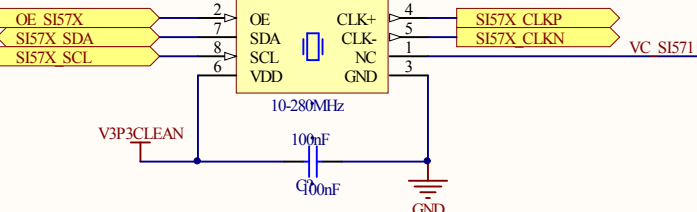
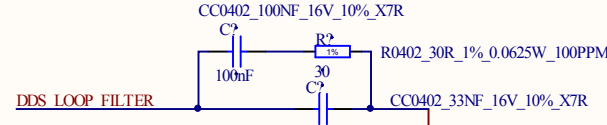
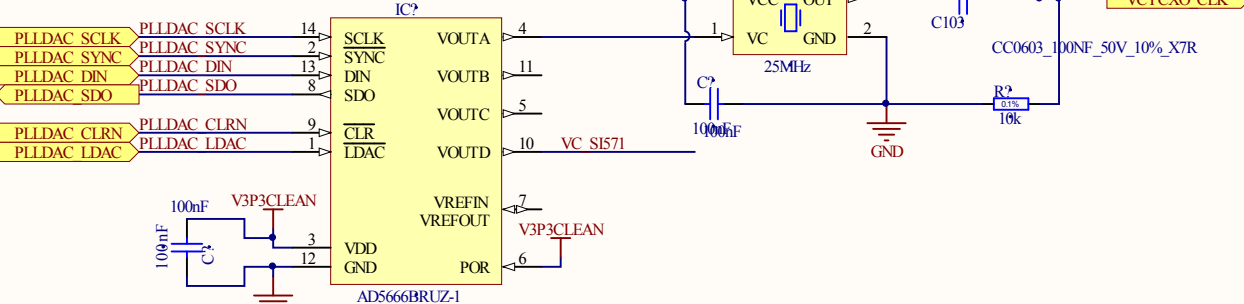
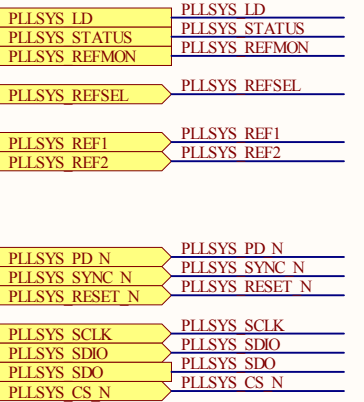
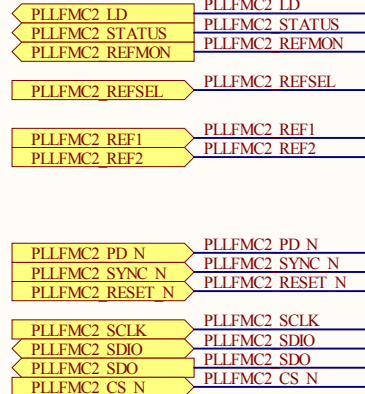
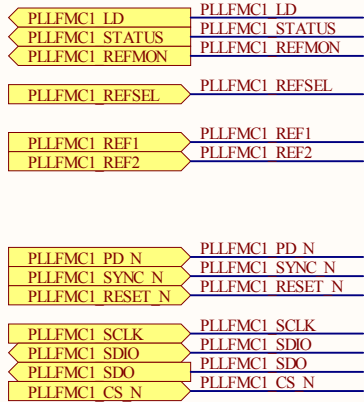
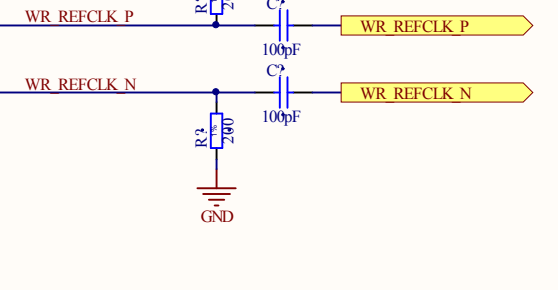
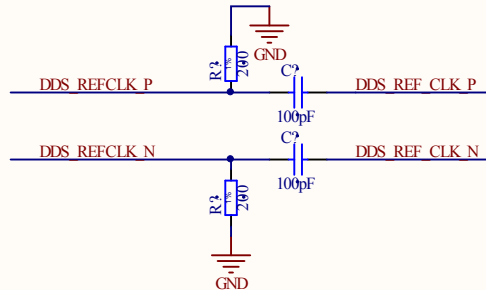
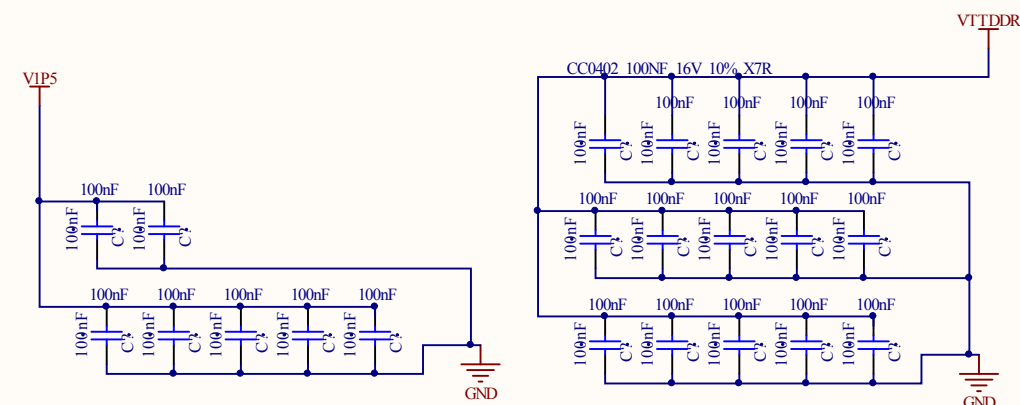
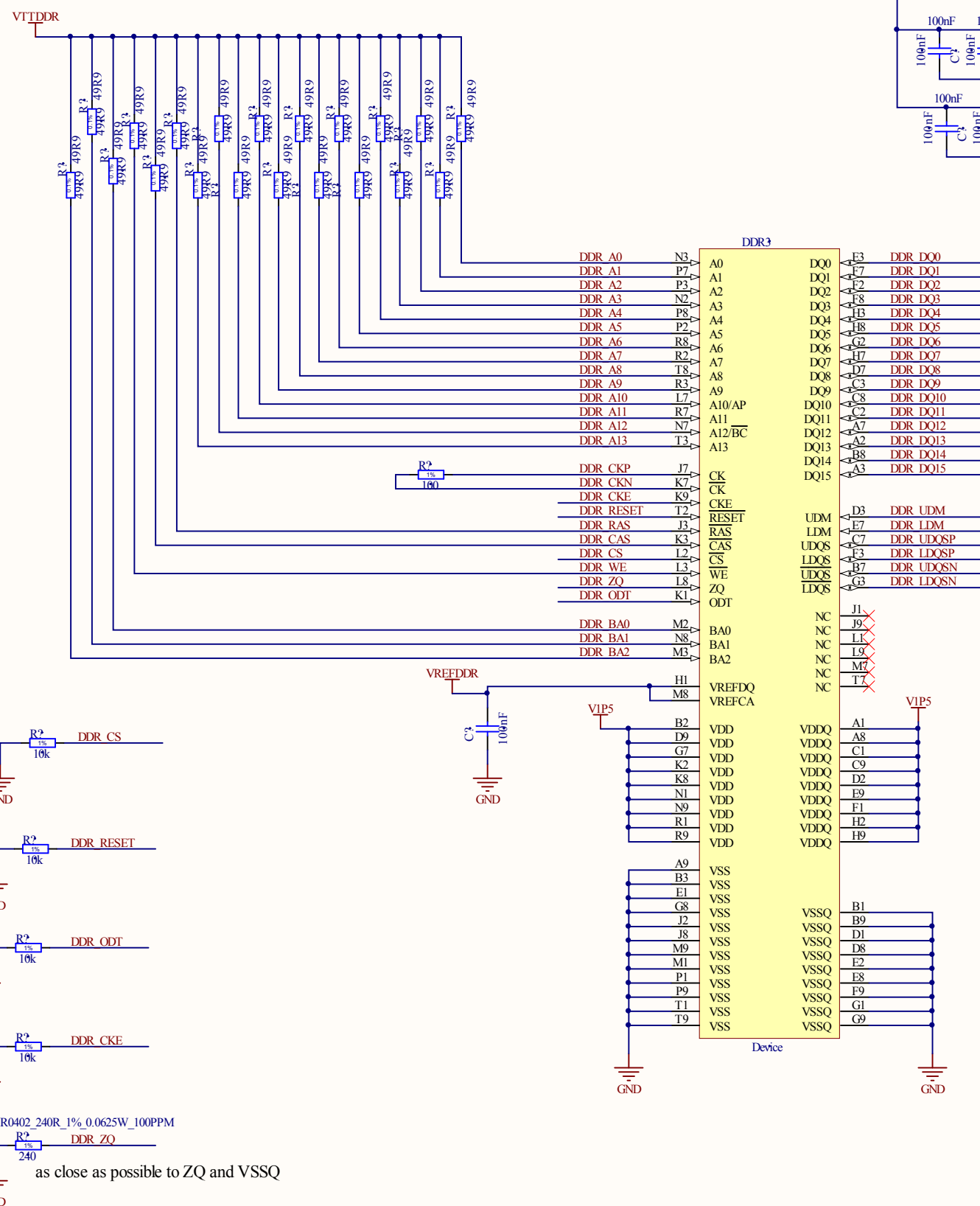




the VREF could not be used for the 2nd mezzanine
The 2nd mezzanine has 2 differential IO pairs less



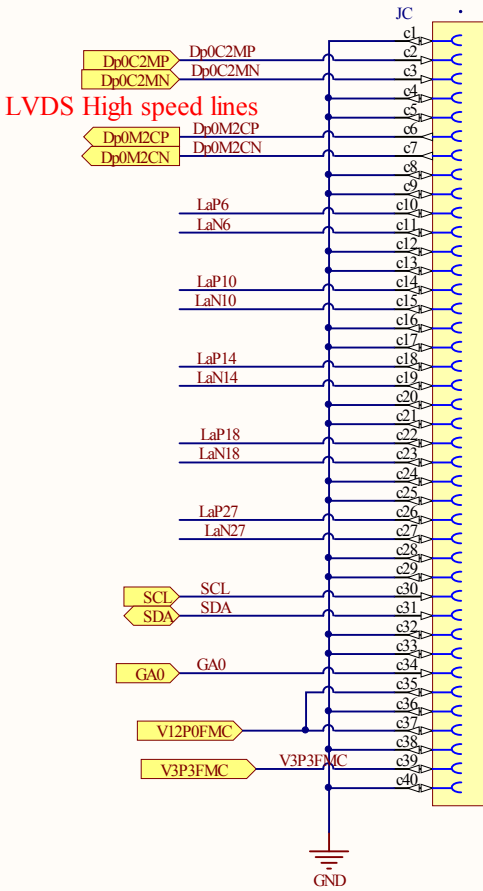
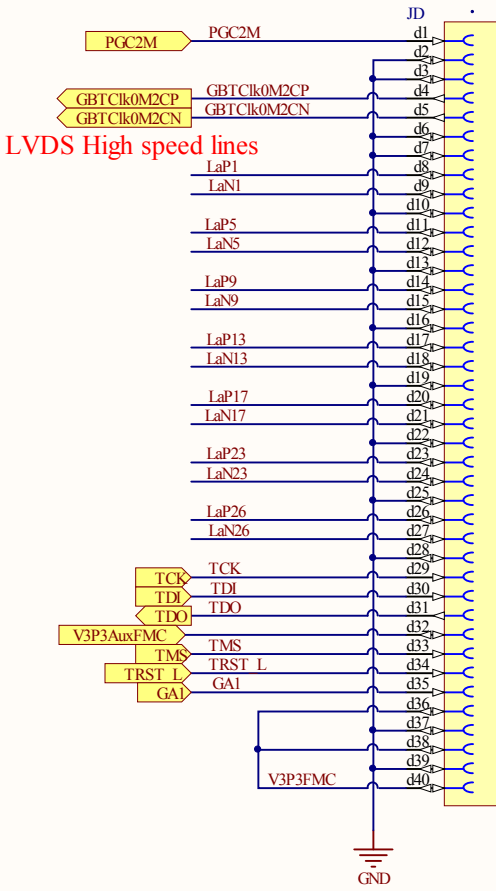
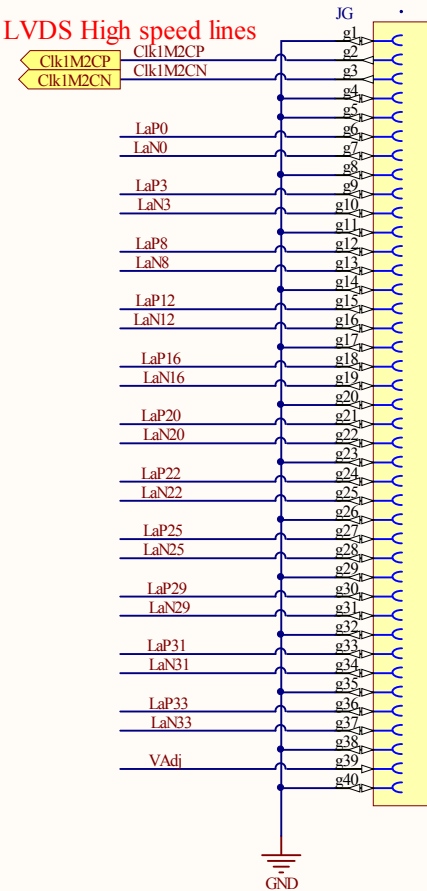
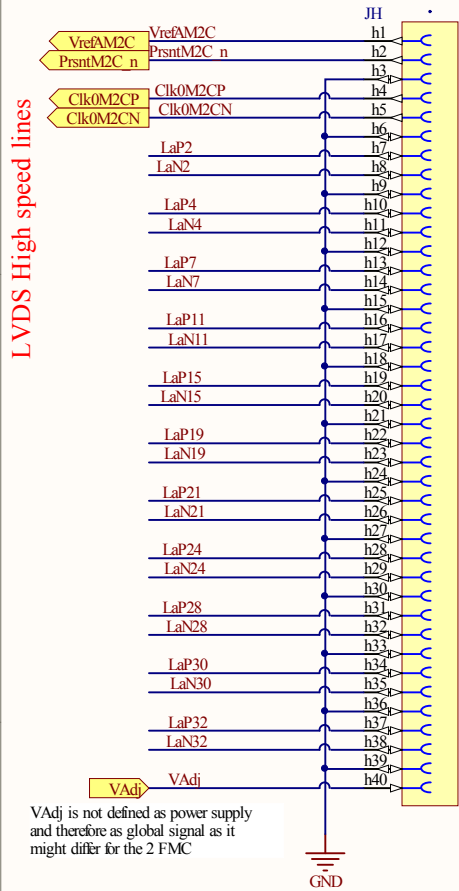
as from page 79 of the AD9516 datasheet



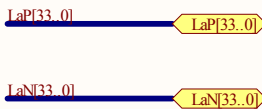


Project/Equipment				
Document	<div><div>BE-BI-QP</div><div></div></div> <div>VME FMC Carrier DDR3</div> <div>European Organization for Nuclear Research CH-1211, Genève 23, Switzerland.</div>	Designer	Andrea Boccardi	
<div><div>BE-BI-QP</div><div></div></div>		Drawn by	Andrea Boccardi	XX/XX/XXXX
		Check by	-	-
		Last Mod.	-	3/5/2010
		File	DDR3_SchDoc	
		Print Date	3/15/2010	10:44:35 AM
		Sheet	- of -	
		Size	A3	Rev
		EDA-XXXXXX-VX-X		-

Low Pin Count Rows

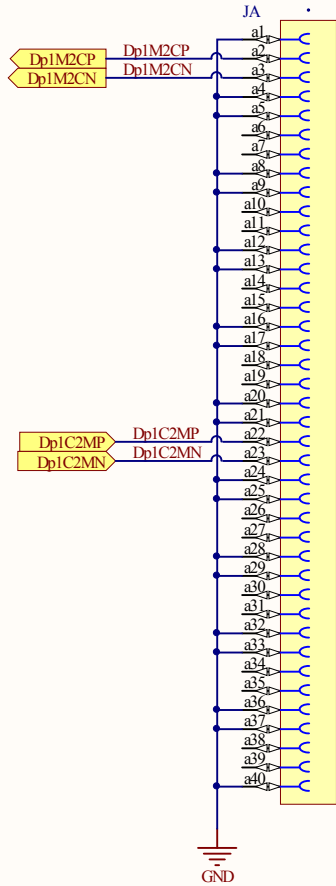
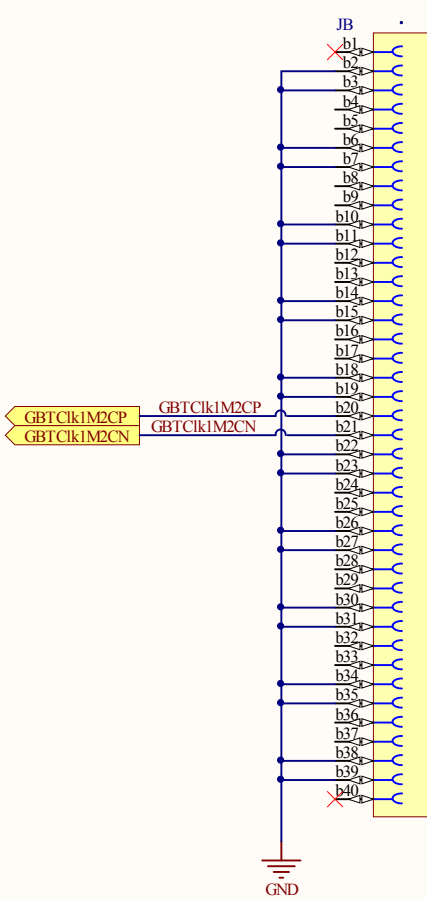
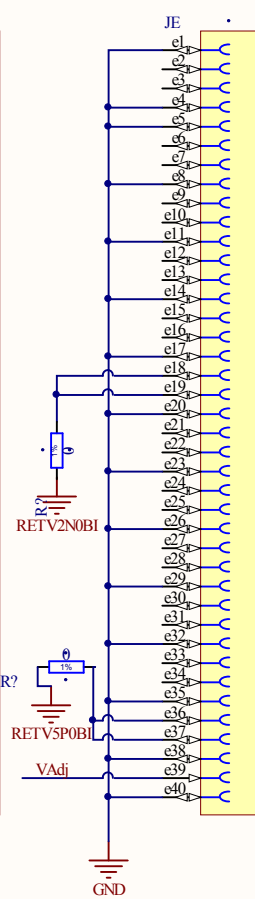
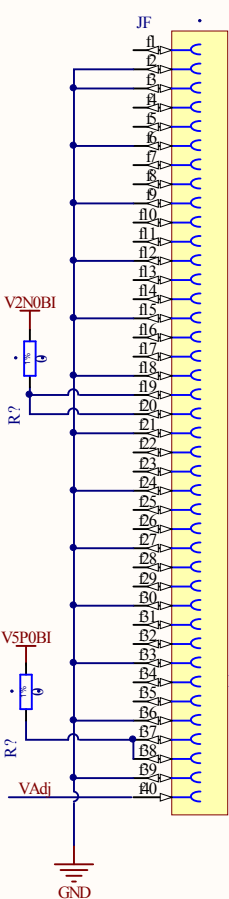
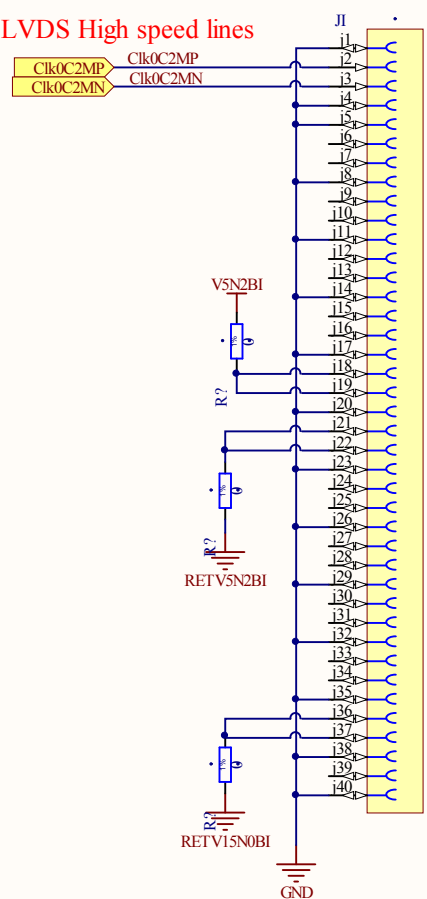
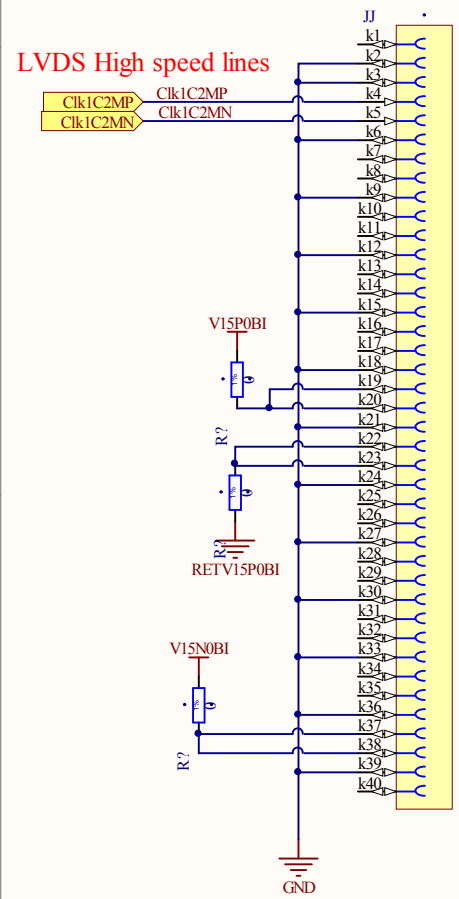



LaP and LaN are LVDS lines

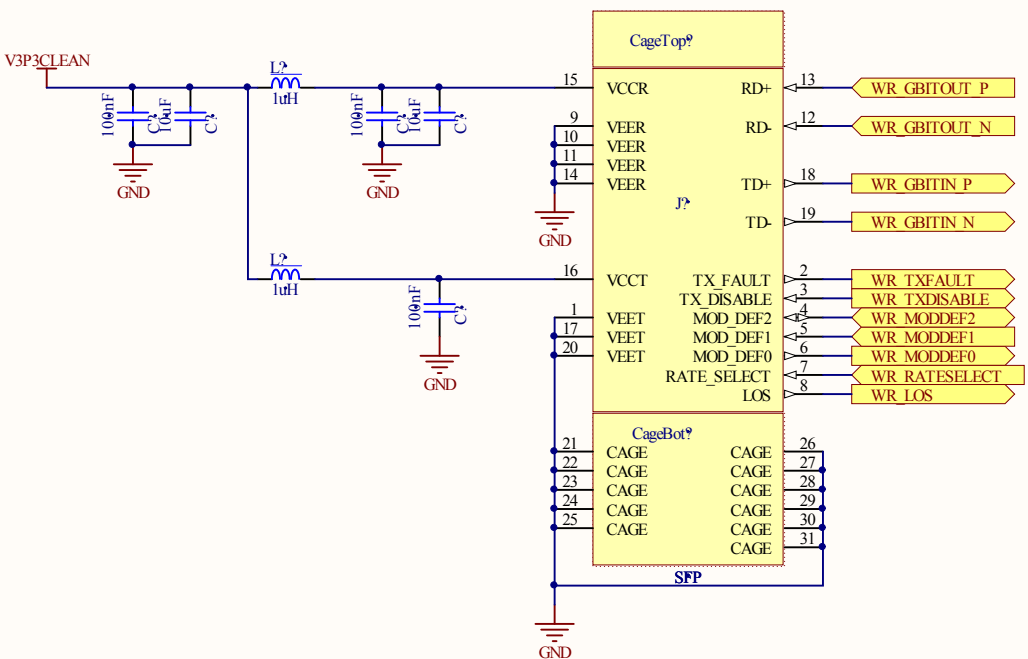
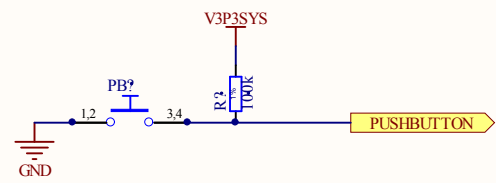
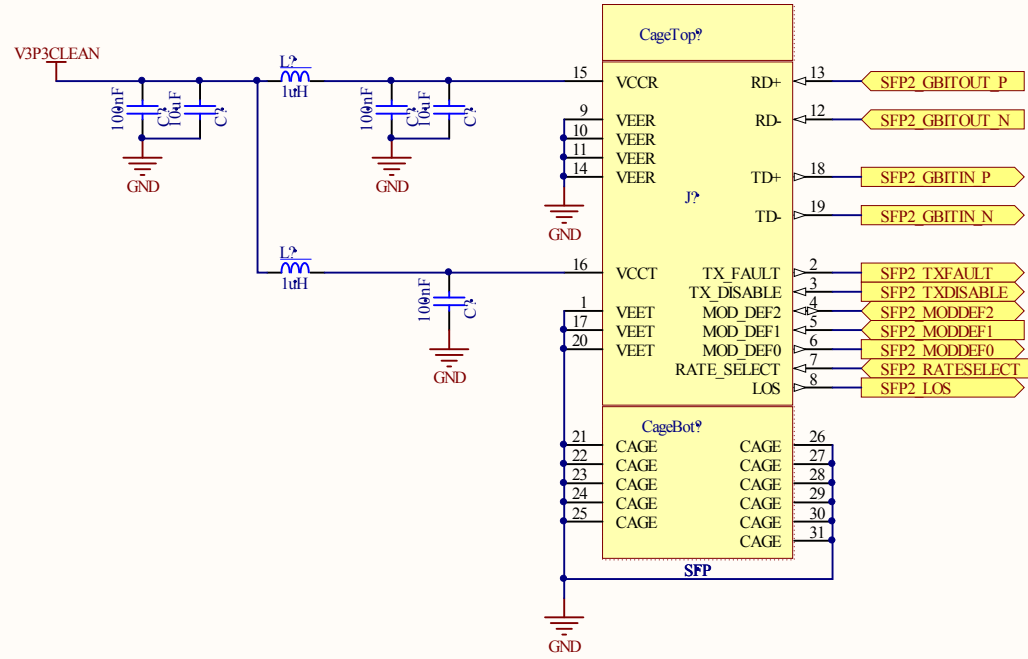
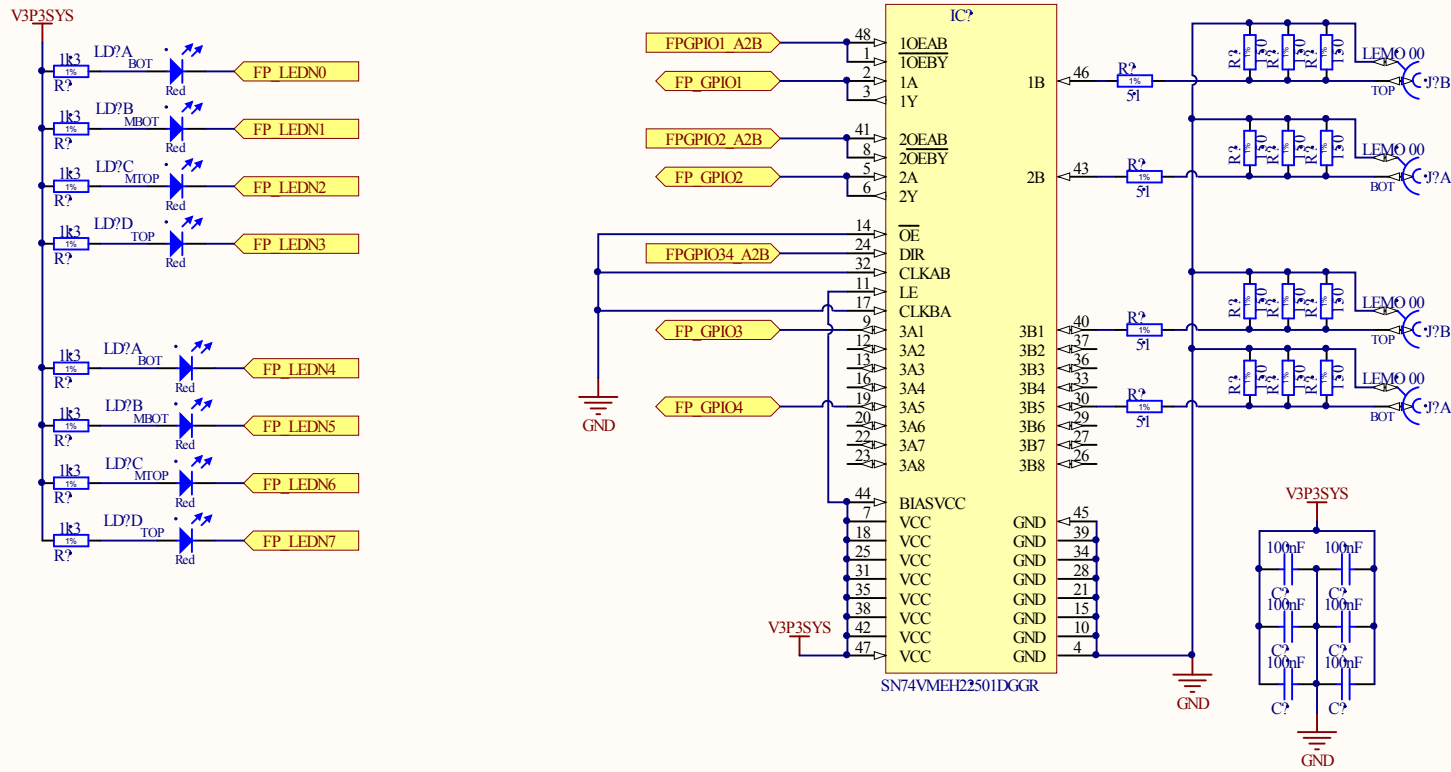


NB: the LVDS pairs must have a differential impedance of 100 ohm and be routed with no skew between the P and the N lines. The skew between the various La pairs should be kept as low as possible.

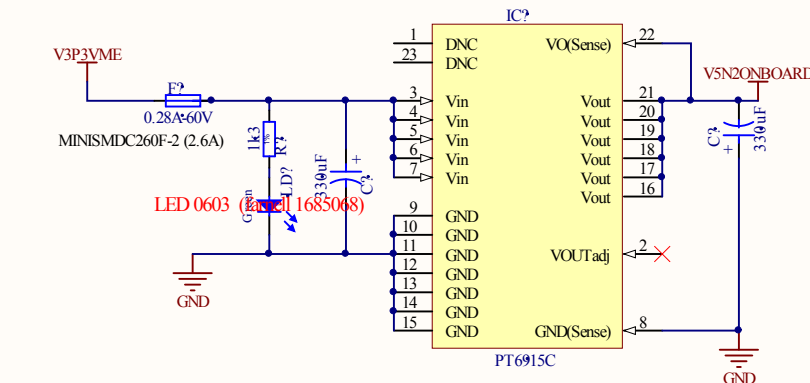
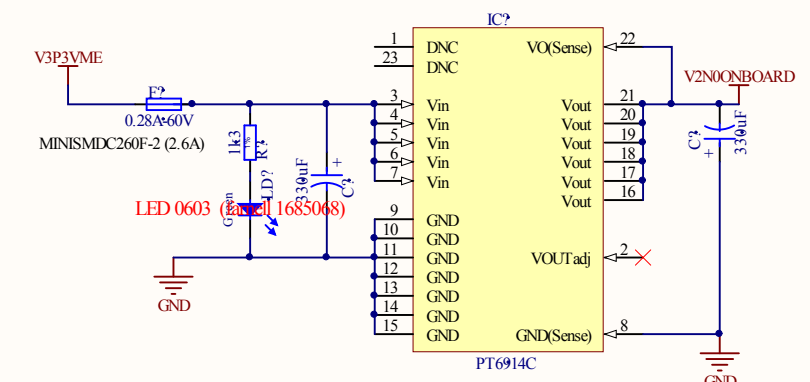
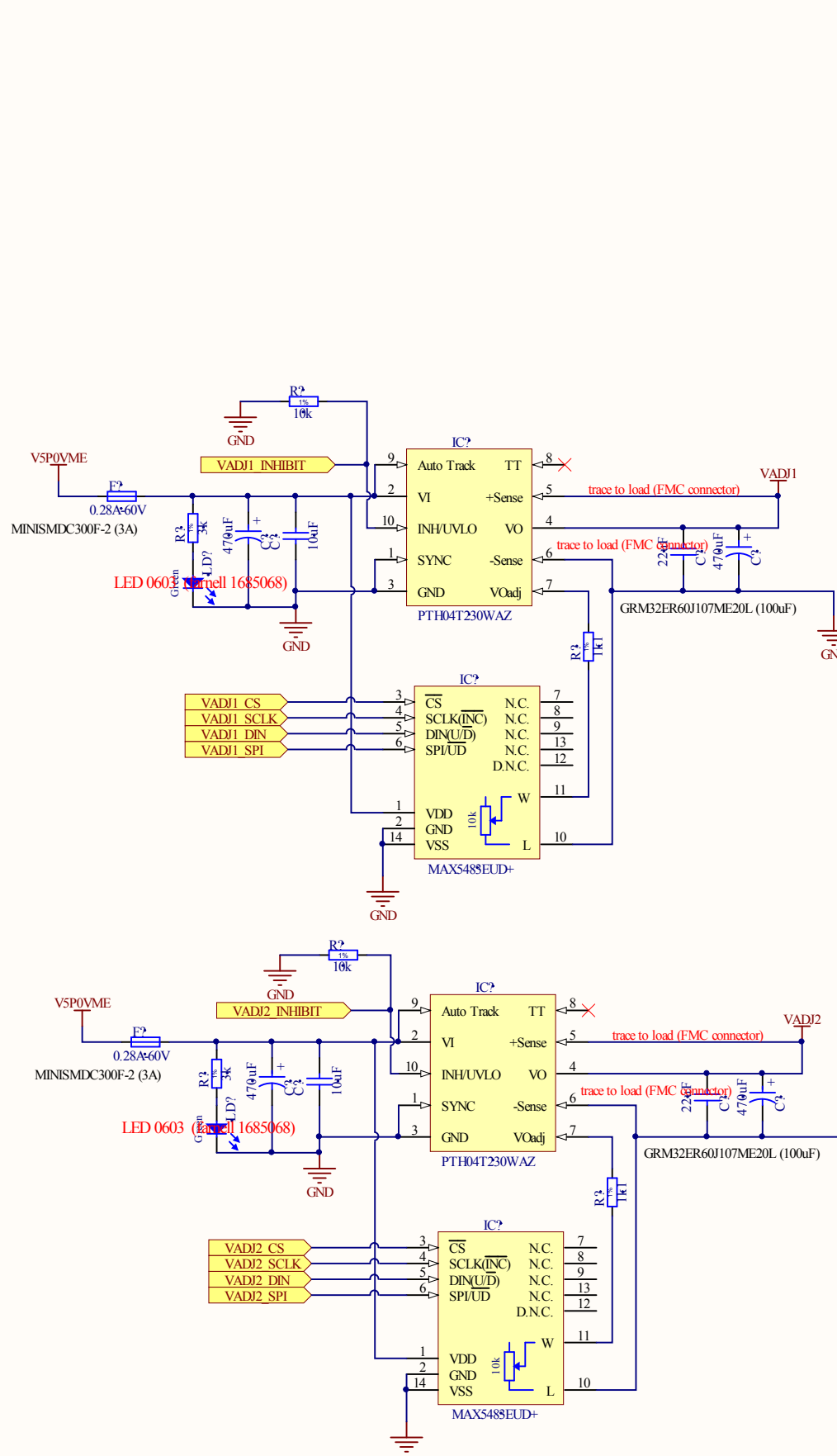
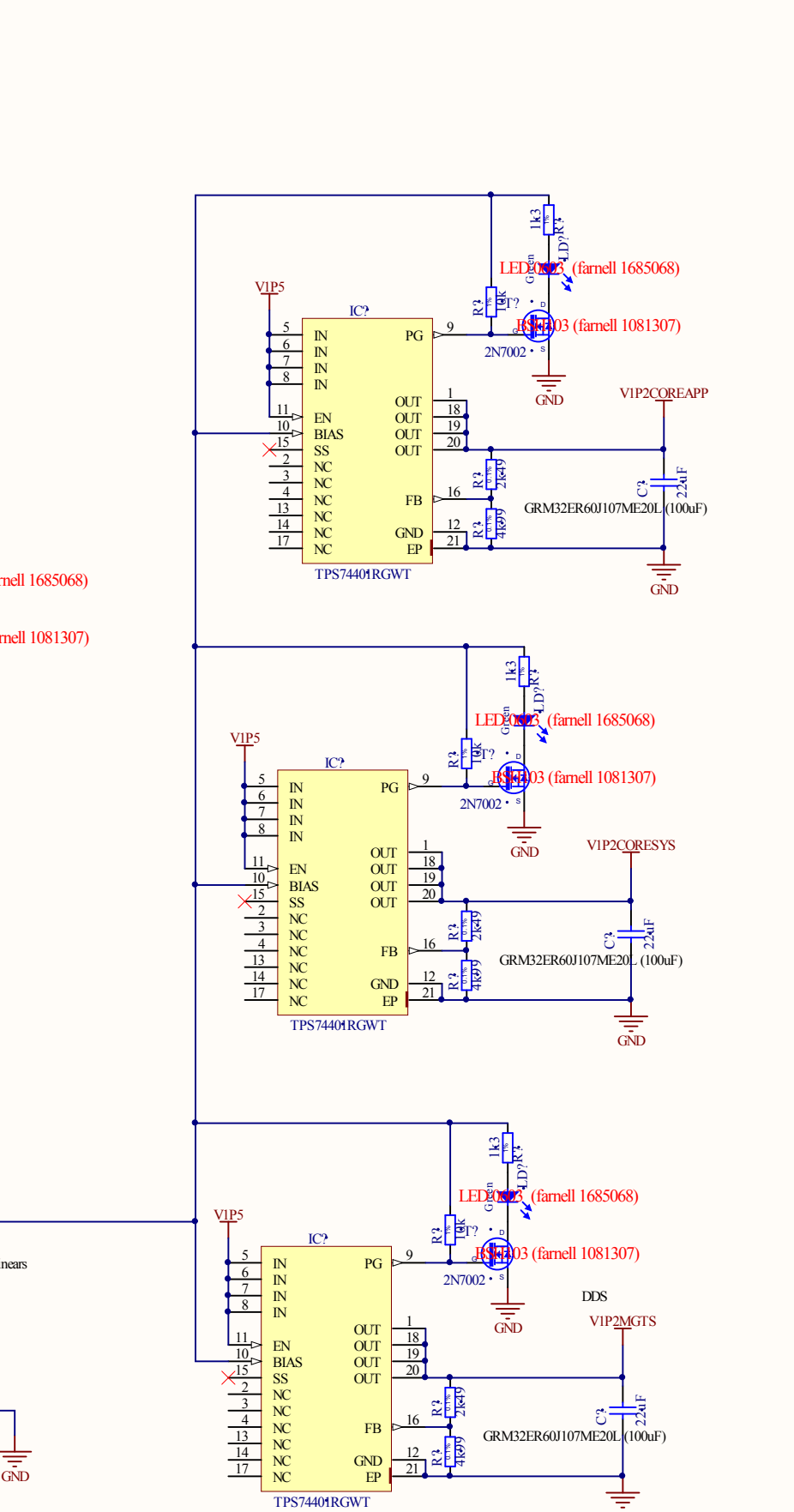
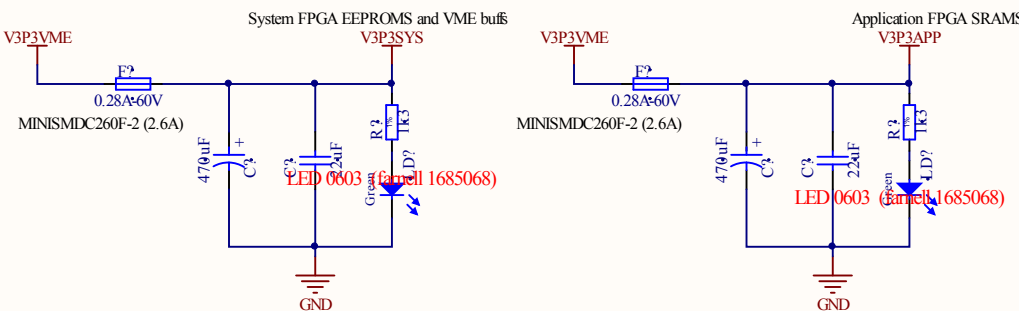
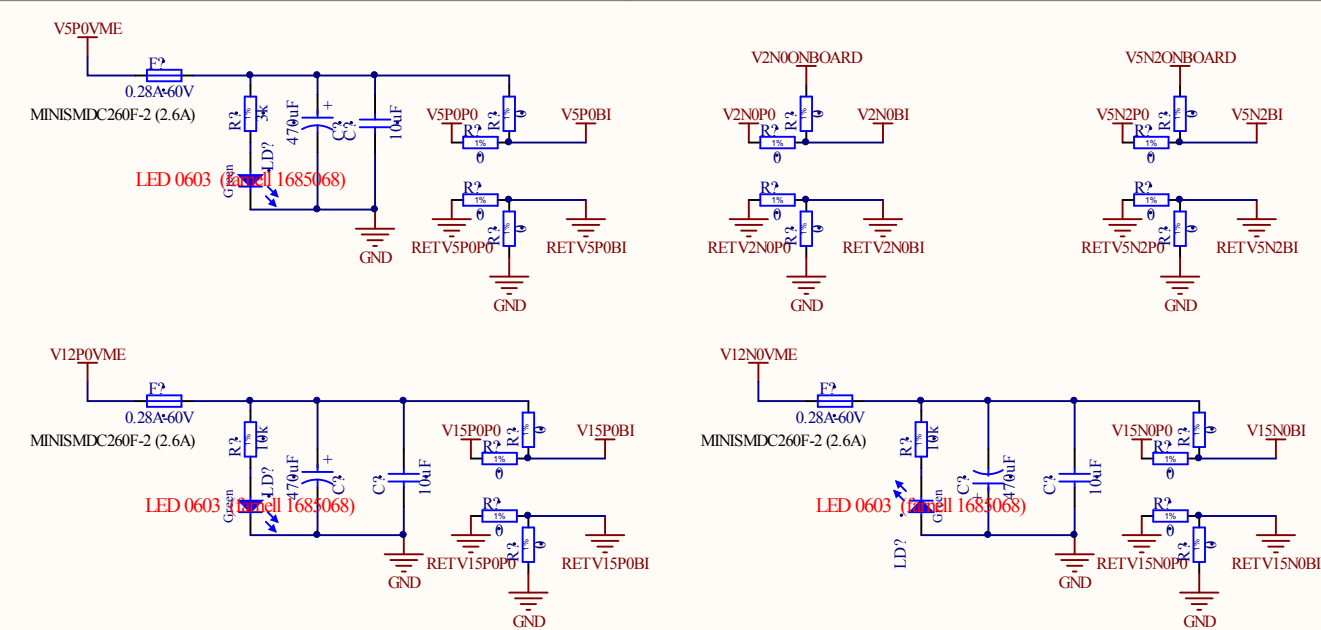
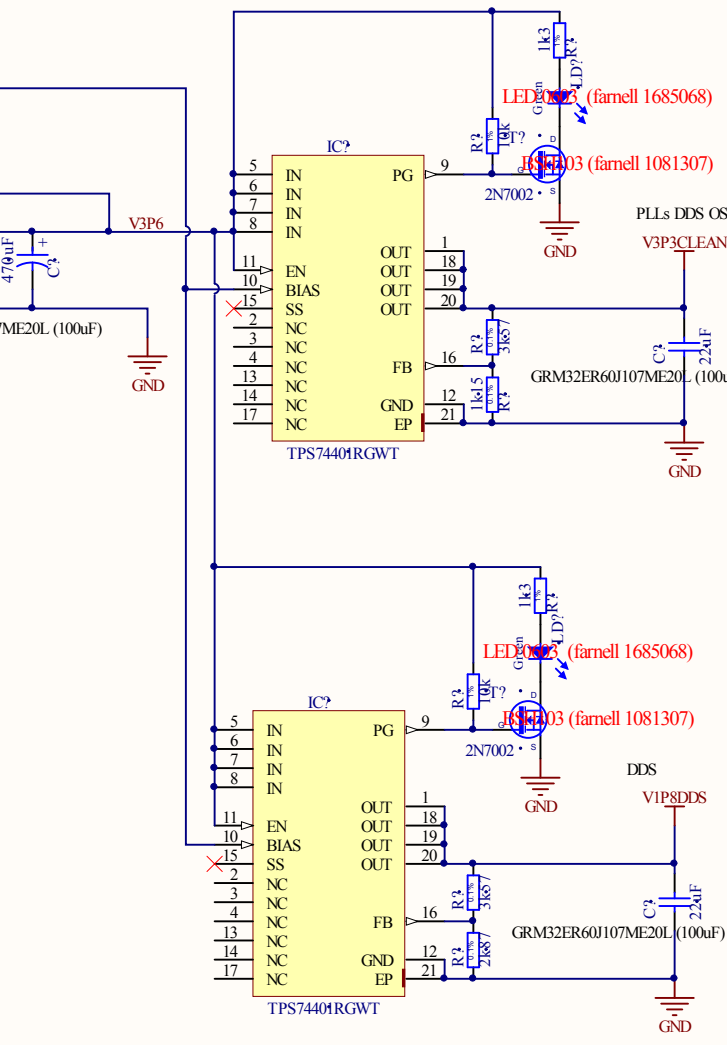
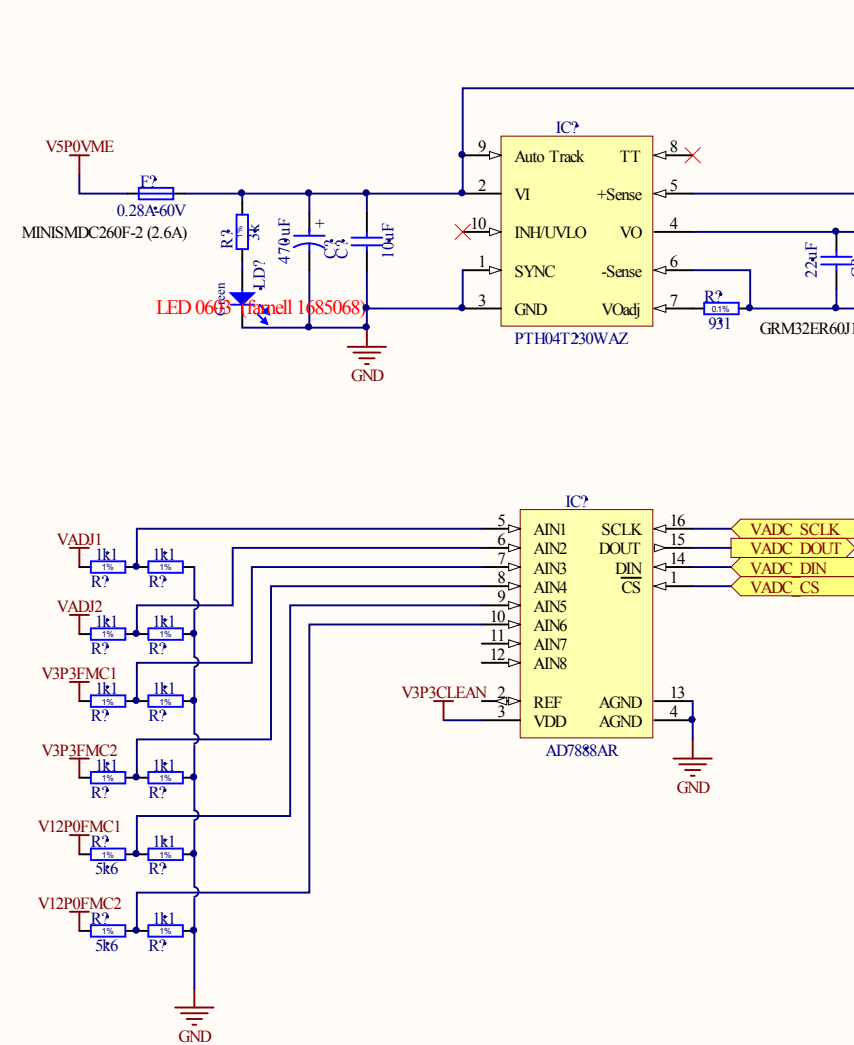
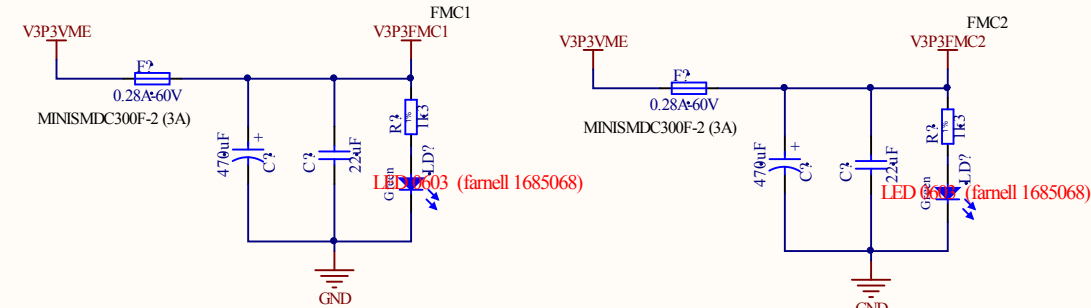
High Pin Count Rows



Project/Equipment		-	
Document		Designer	Andrea Boccardi
<div>BE-BI-OP</div> <div></div>	<div>VME FMC Carrier</div> <div>FMC Connector</div>	Drawn by	Andrea Boccardi
		Check by	-
		Last Mod.	3/5/2010
		File	FmcConnector.SchDoc
European Organization for Nuclear Research CH-1211, Genéve 23 - Switzerland		Print Date	3/15/2010 10:44:36 AM
EDA-XXXXXX-VX-X		Sheet	- of -
		Size	A3
		Rev	-



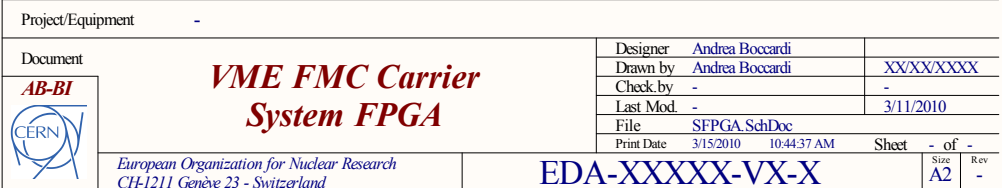
replace ALL the leds for ROHM - SML-311D TT86K - LED 0603 FAIBLE COURANT ORANGE (farnell 1685067)



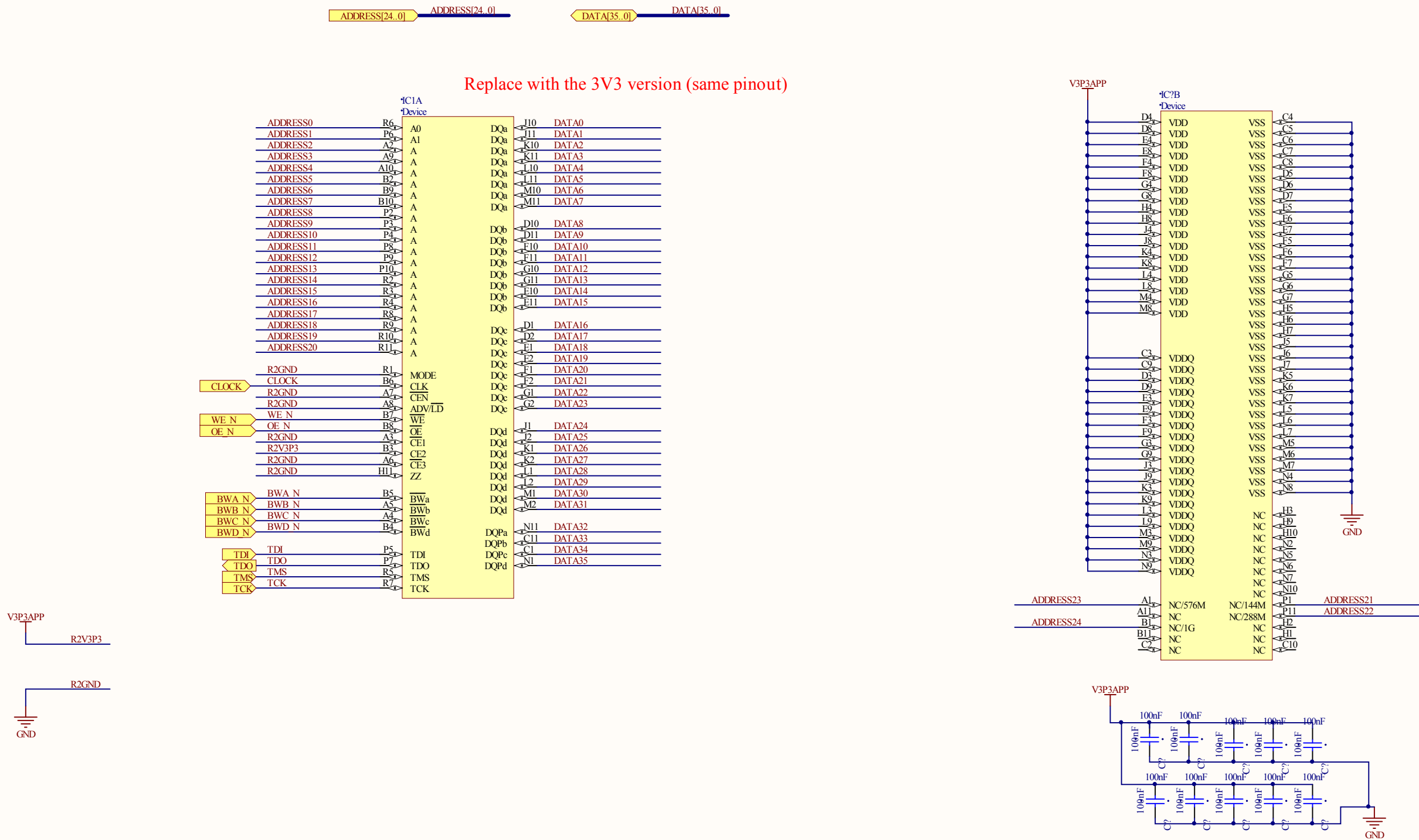
Project/Equipment	-	Designer	Andrea Boccardi	XX/XX/XXXX
Document	-	Drawn by	Andrea Boccardi	-
Check by	-	Last Mod.	-	3/11/2010
File	PowerSupplies_SchDoc	Print Date	3/15/2010 10:44:36 AM	Sheet - of -
Rev	A2	Rev	-	-

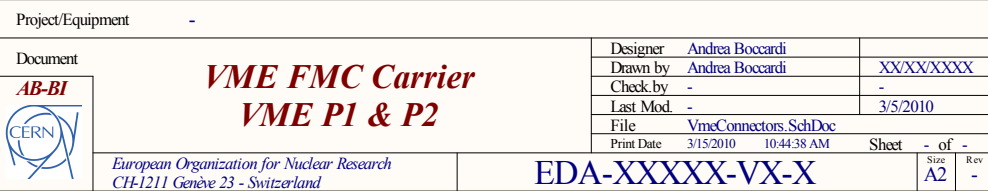
European Organization for Nuclear Research
CERN - CH-1211 Geneva 23 - Switzerland

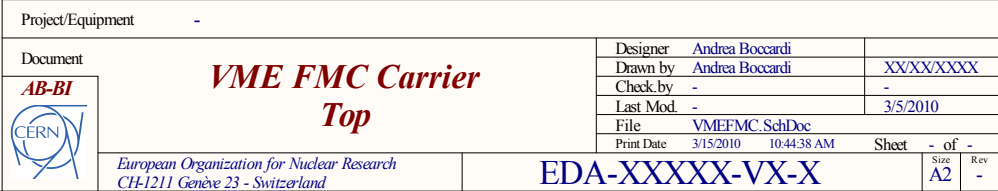
EDA-XXXXX-VX-X



Replace with the 3V3 version (same pinout)









Bill of Materials For Project [VmeFmcCarrier.PrjPCB] (No PCB Document)

[illegible]