



https://ohwr.org/project/sis1160-pci-io Front-end PCI board with LEMO connectors to interface with GPIO interconnect pins of the SIS1160 FMC carrier (Struck). Funded by Generalitat Valenciana under grant number CDEIGENT/2019/11. Fernando Hueso-González - IFIC (CSIC/UV)		
Sheet: File: BoardI0.kicad_pcb		
Title: SIS1160 PCI-L I/O add on		
Size: A4	Date: 2021-06-02	Rev: 0
KiCad E.D.A. kicad 5.99.0-unknown-ed66c0f14d128ubuntu18.04.1		Id: 1/1