



https://ohwr.org/project/sis1160-pci-io Front-end PCI board with LEMO connectors to interface with GPIO interconnect pins of the SIS1160 FMC carrier (Struck). Funded by Generalitat Valenciana under grant number CDEIGENT/2019/11. Fernando Hueso-González - IFIC (CSIC/UV)		
Sheet: File: BoardIO.kicad_pcb		
Title: SIS1160 PCI-L I/O add on		
Size: A4	Date: 2021-06-02	Rev: 0
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