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| <a href="https://ohwr.org/project/sis1160-pci-io">https://ohwr.org/project/sis1160-pci-io</a><br>Front-end PCI board with LEMO connectors<br>to interface with GPIO interconnect pins of the SIS1160 FMC carrier (Struck).<br>Funded by Generalitat Valenciana under grant number CDEIGENT/2019/11.<br><b>Fernando Hueso-González - IFIC (CSIC/UV)</b> |                  |         |
| Sheet:   |                  |         |
| File: BoardIO.kicad_pcb  |                  |         |
| <b>Title: SIS1160 PCI-L I/O add on</b>   |                  |         |
| Size: A4   | Date: 2021-06-02 | Rev: 0  |
| KiCad E.D.A. kicad 5.99.0-unknown-ed66c0f14d128ubuntu18.04.1   |                  | Id: 1/1 |