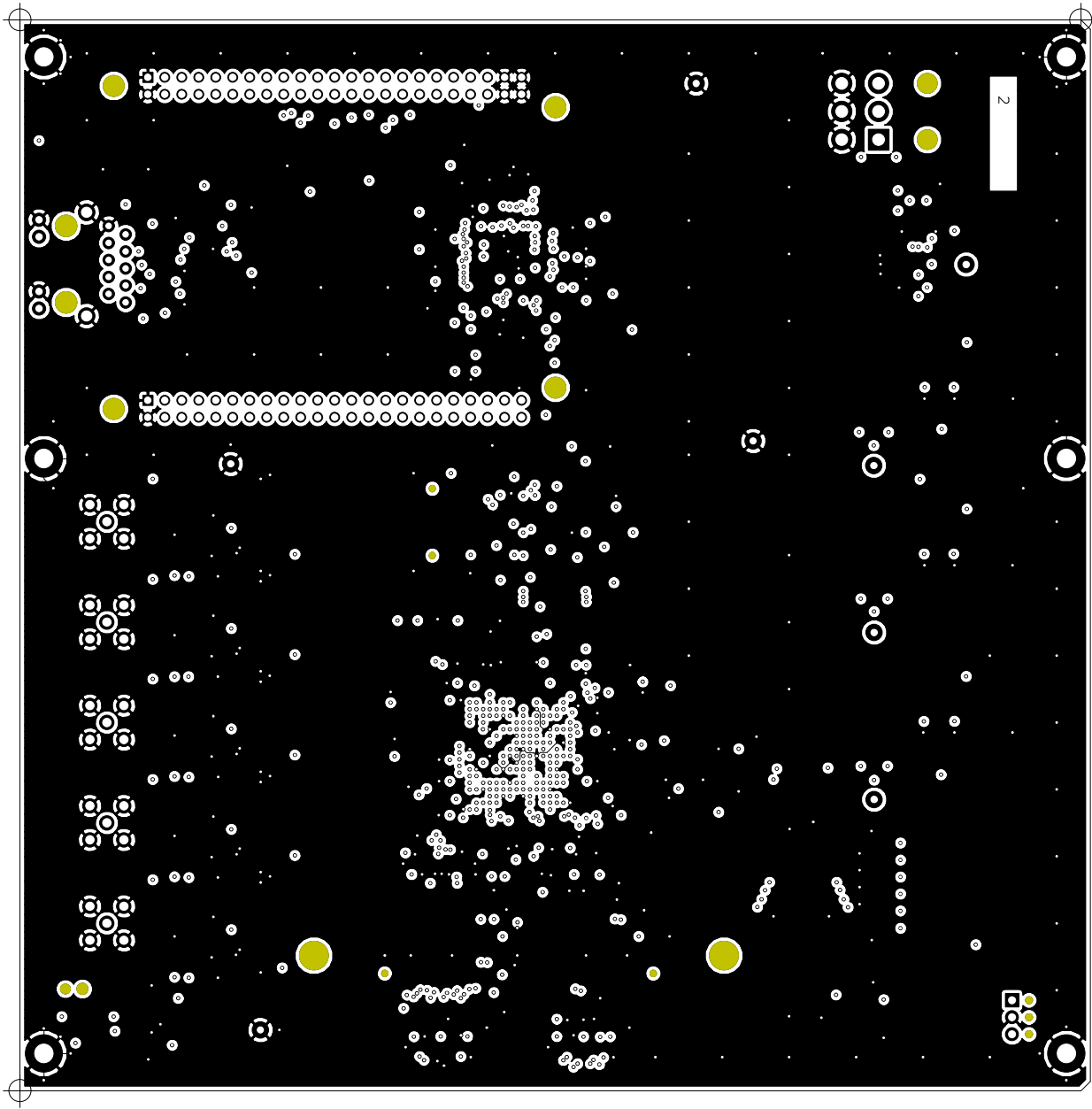
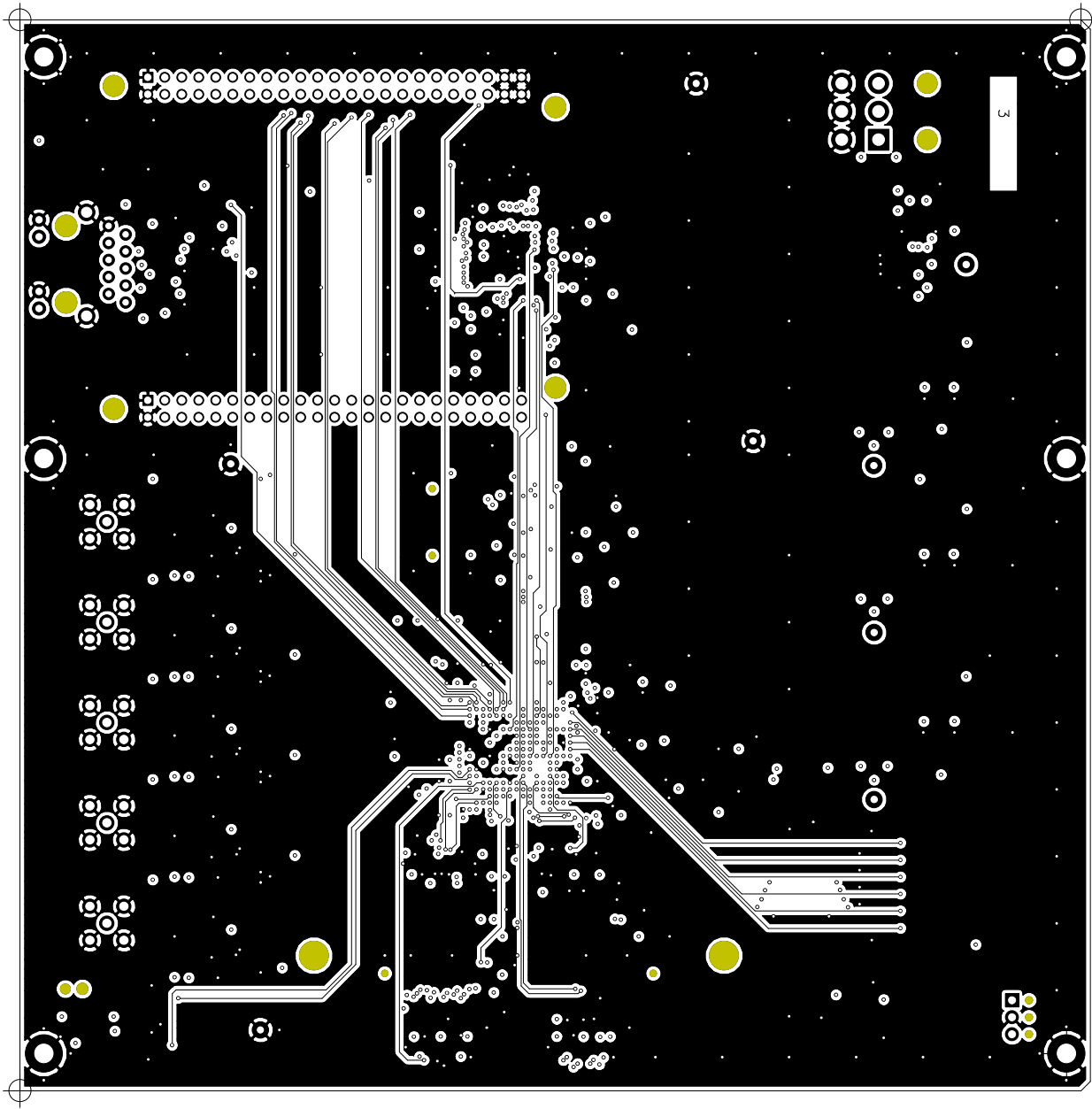


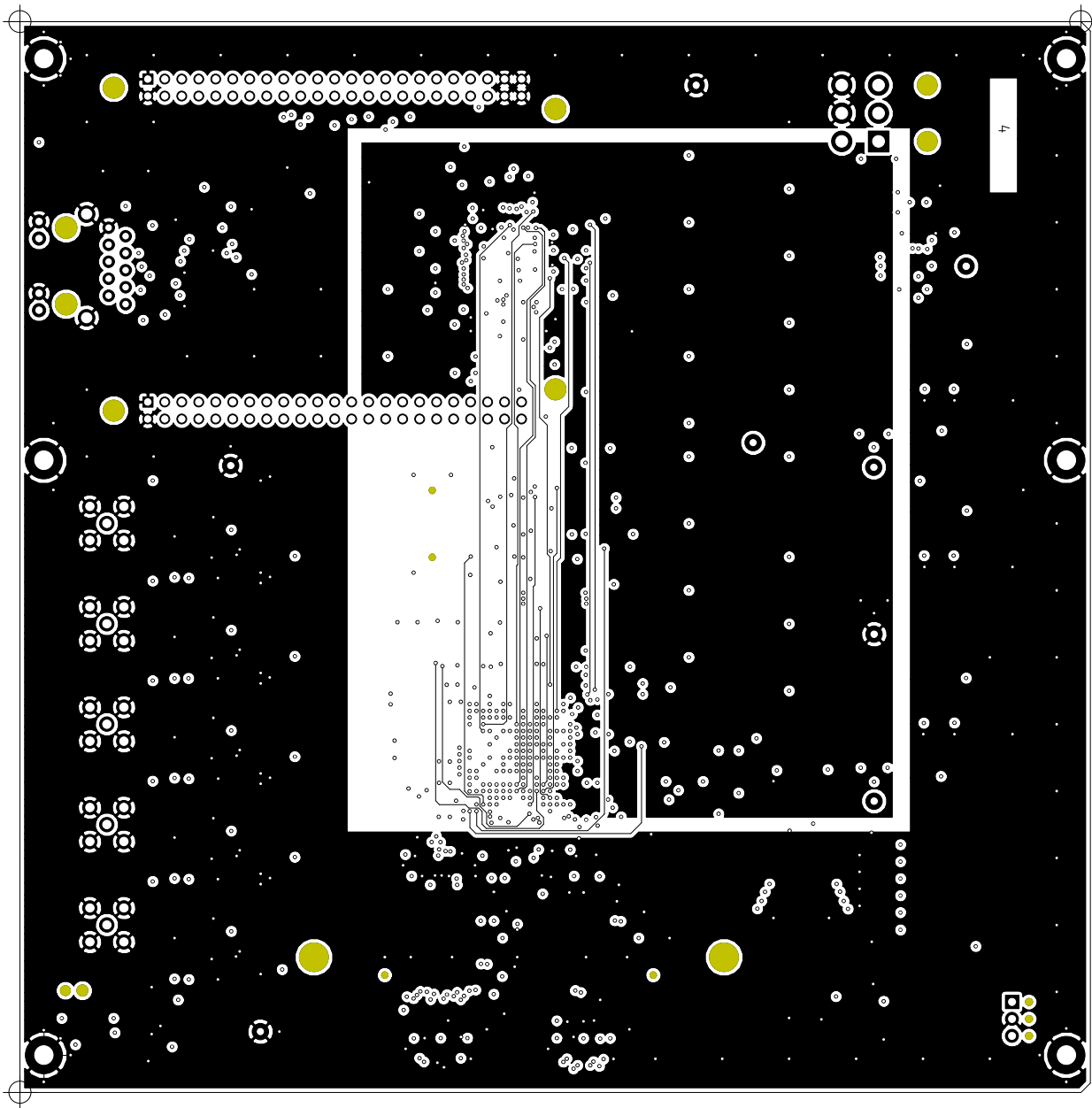
Jean-Paul Ricaud TimIQ-CPU		
TimIQ CPU board		
Sheet: File: TimIQ_CPU.kicad_pcb		
Title: <i>TimIQ-CPU // TimEX3b</i>		
Size: A3	Date: 2016-06-20	Rev:
KiCad E.D.A. pcbnew 4.0.2-stable	Id: 1/1	



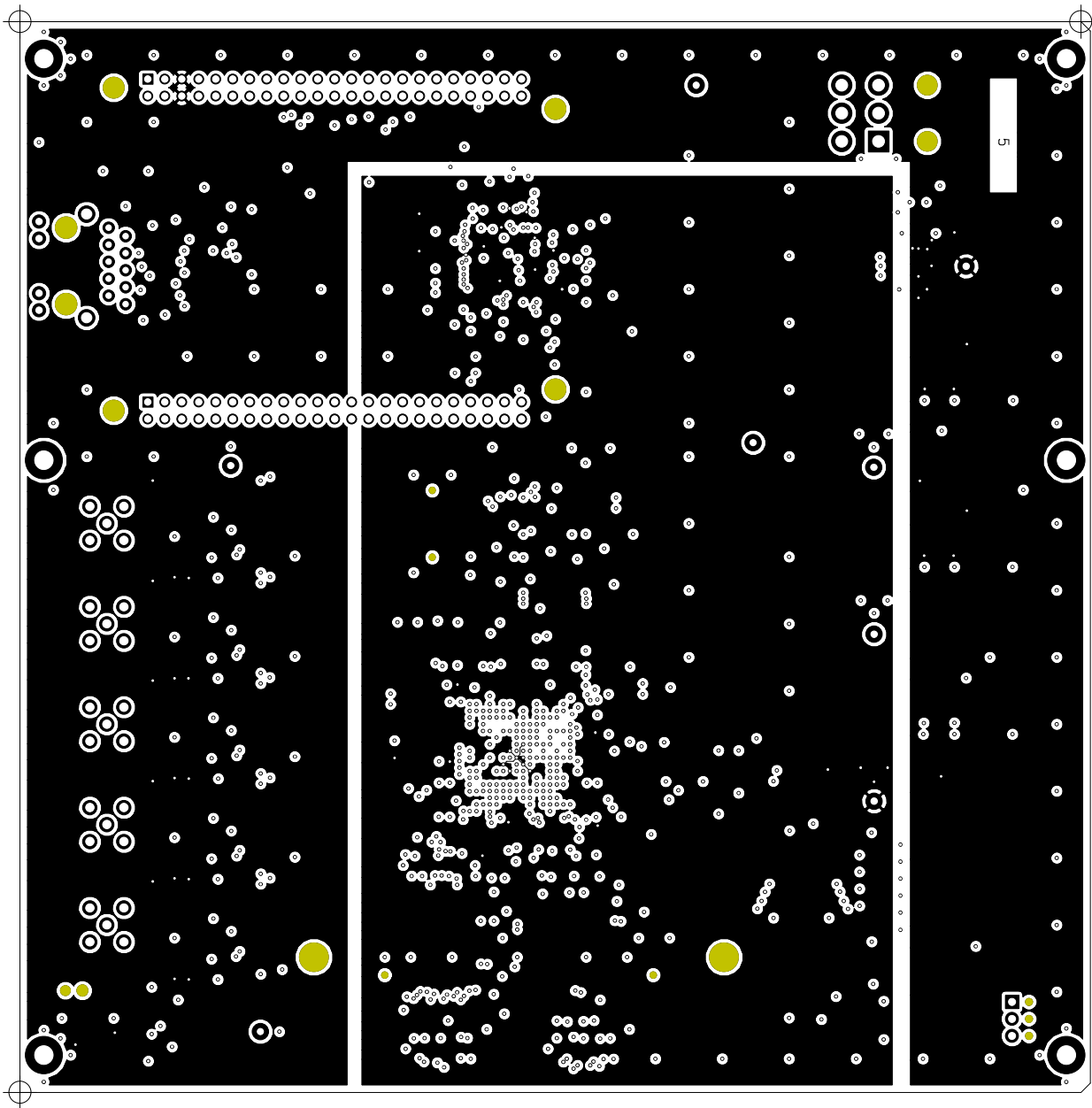
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TimIQ CPU board		
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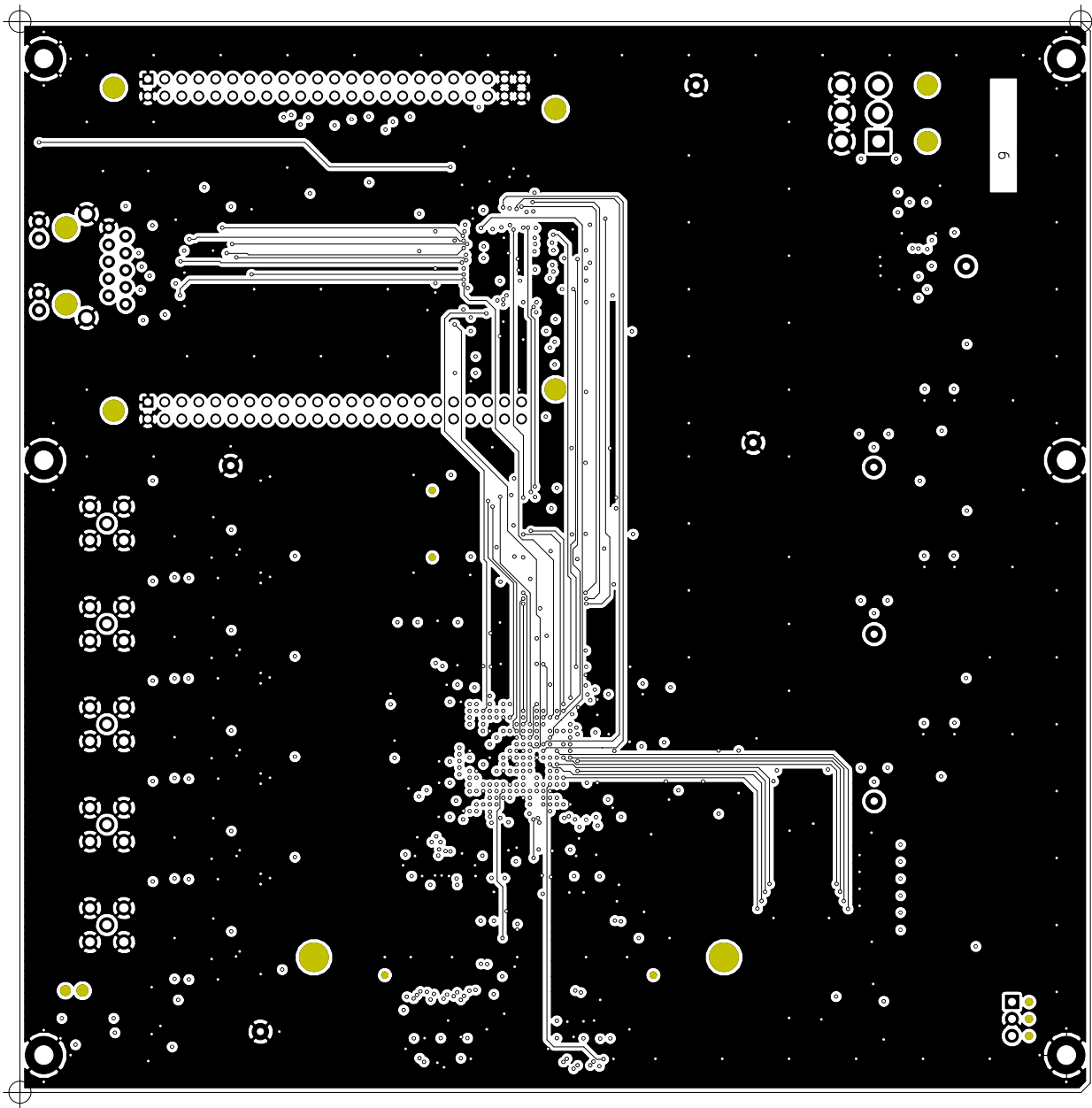
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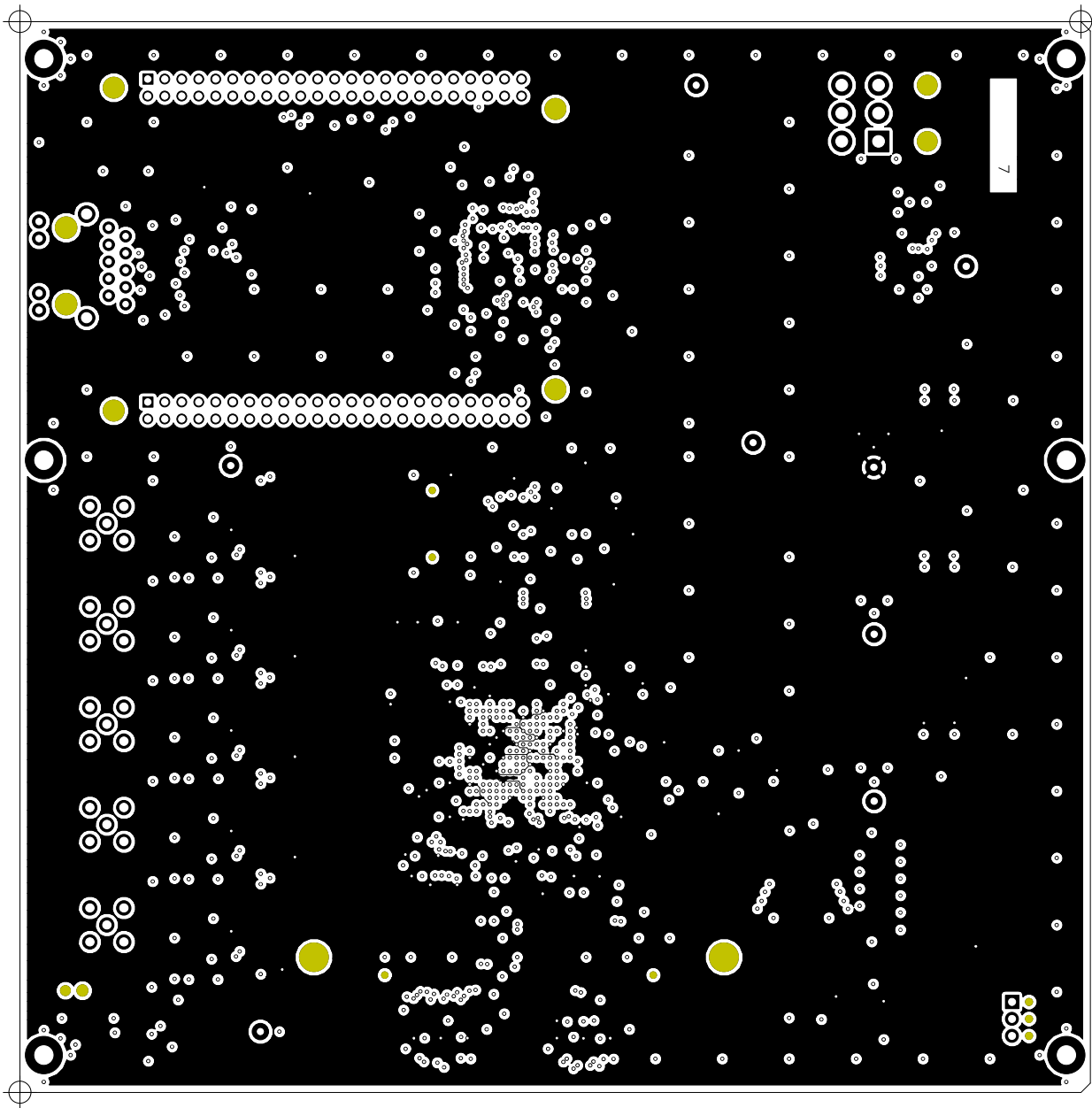
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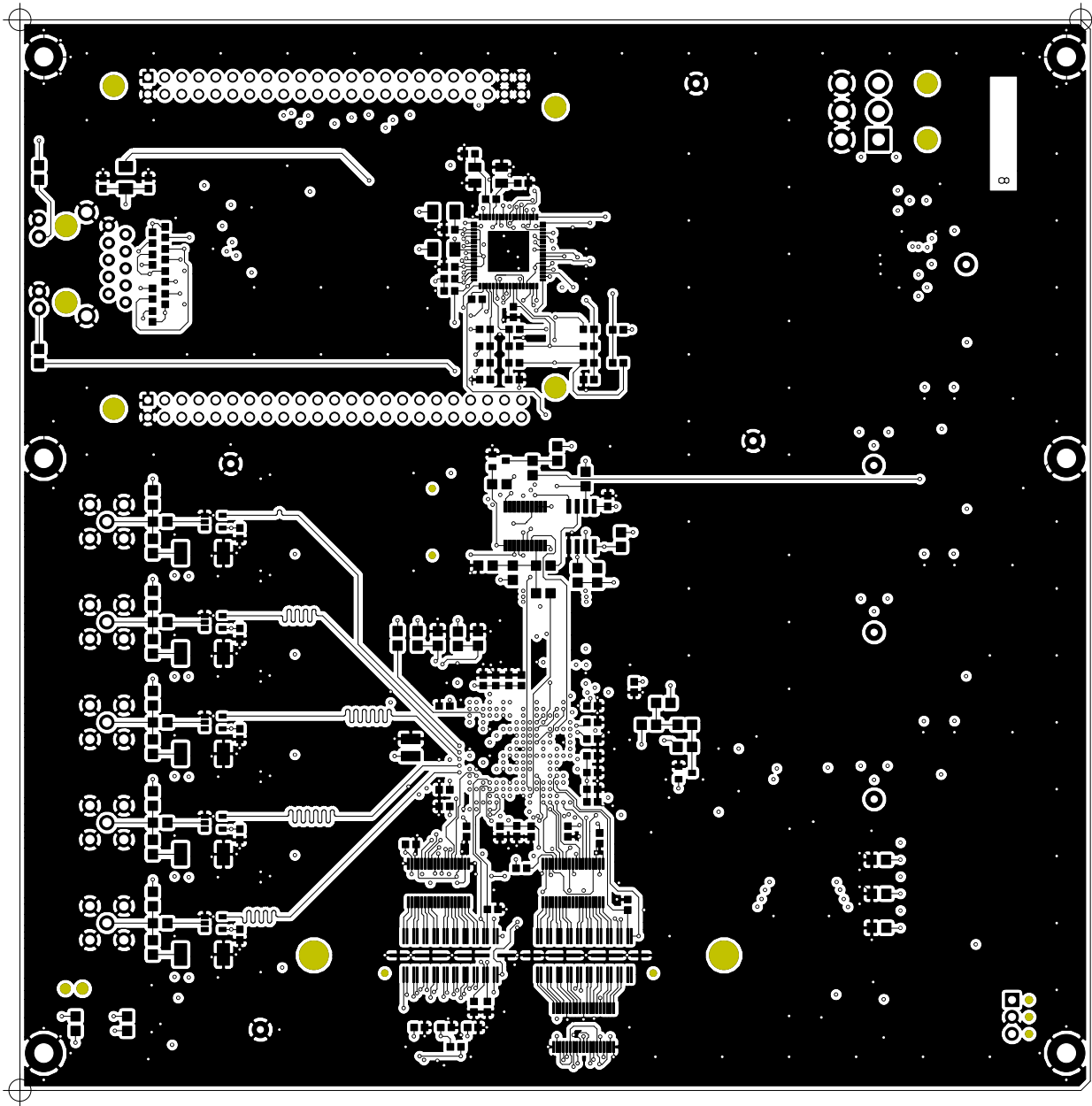
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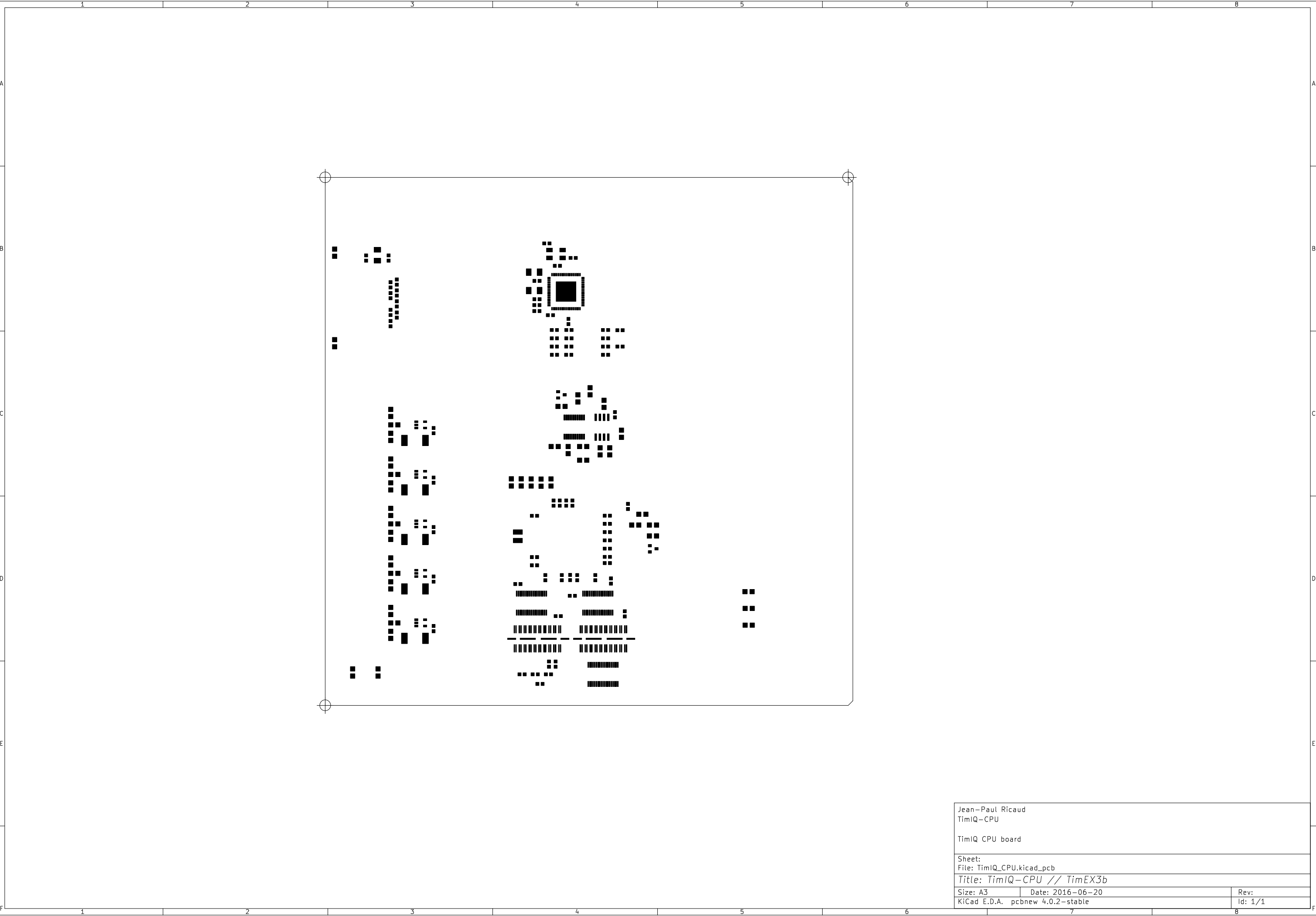


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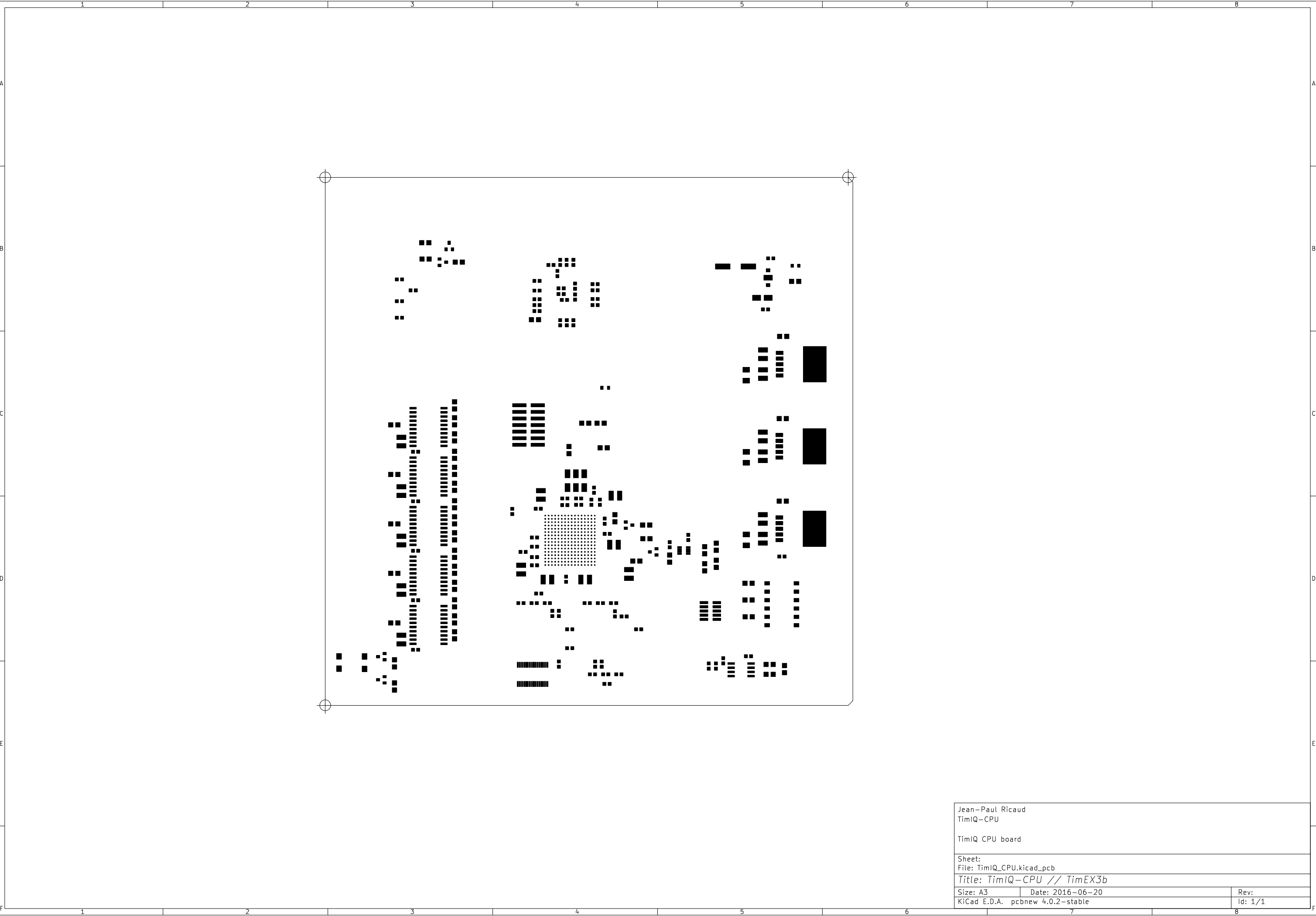


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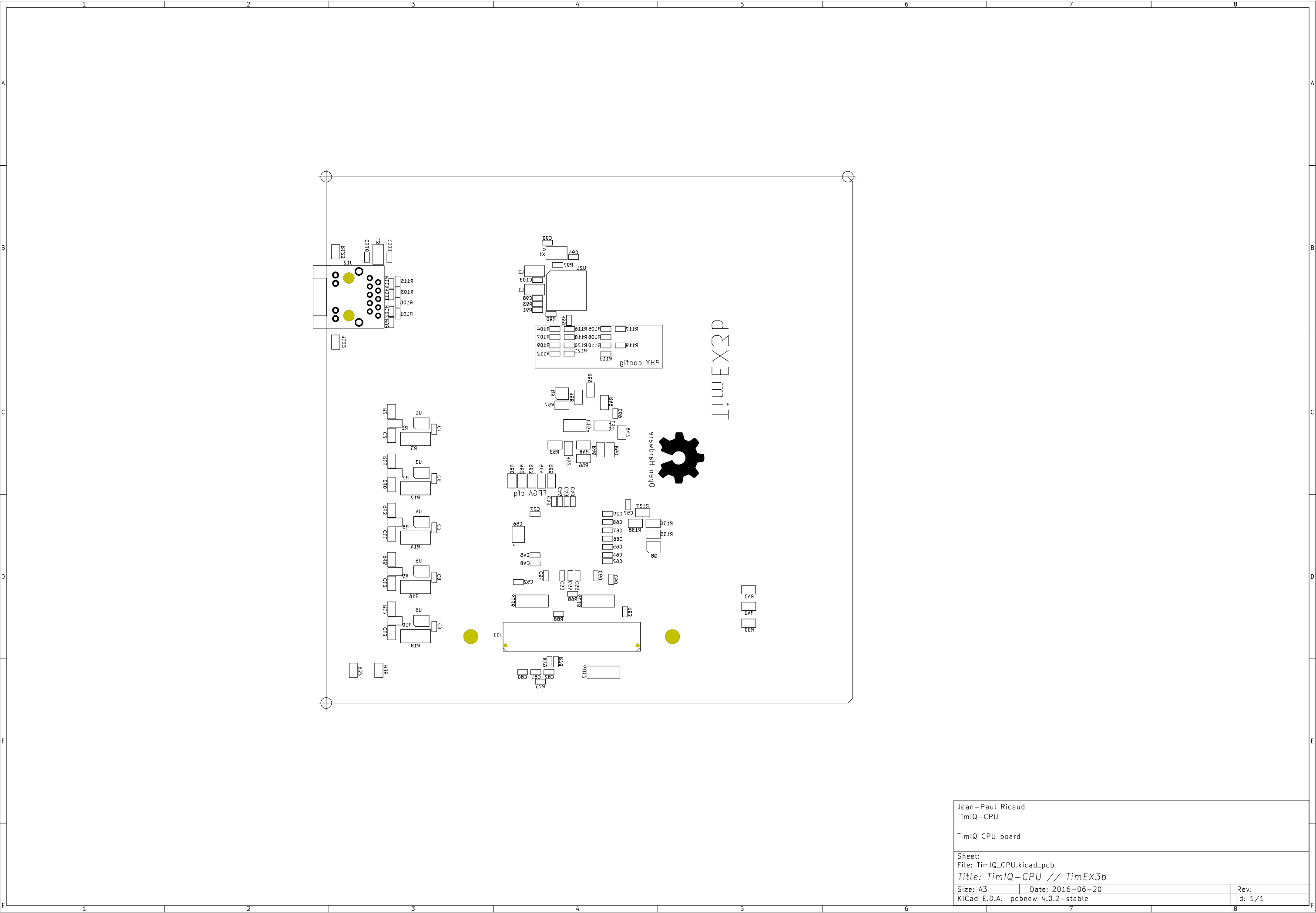




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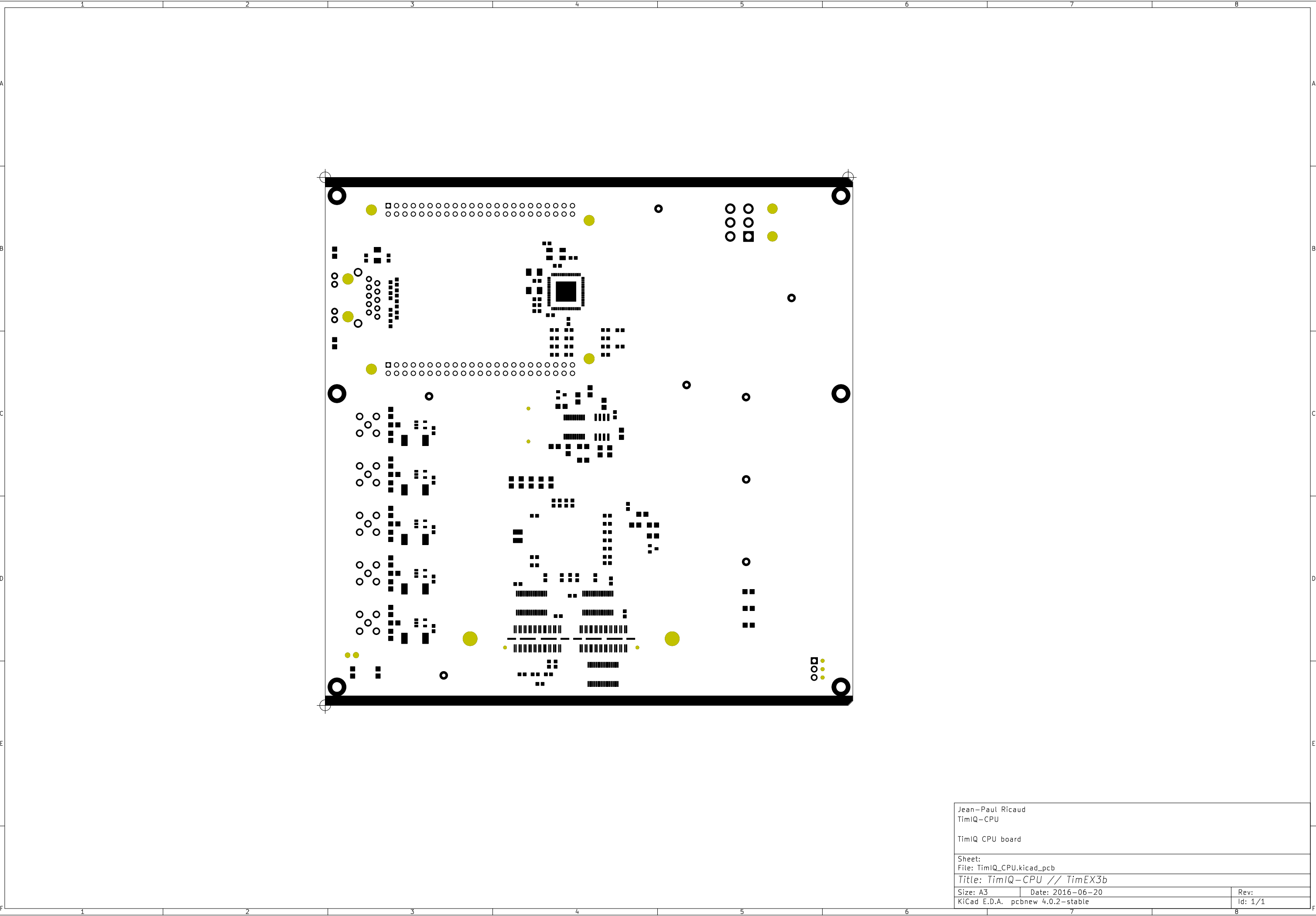
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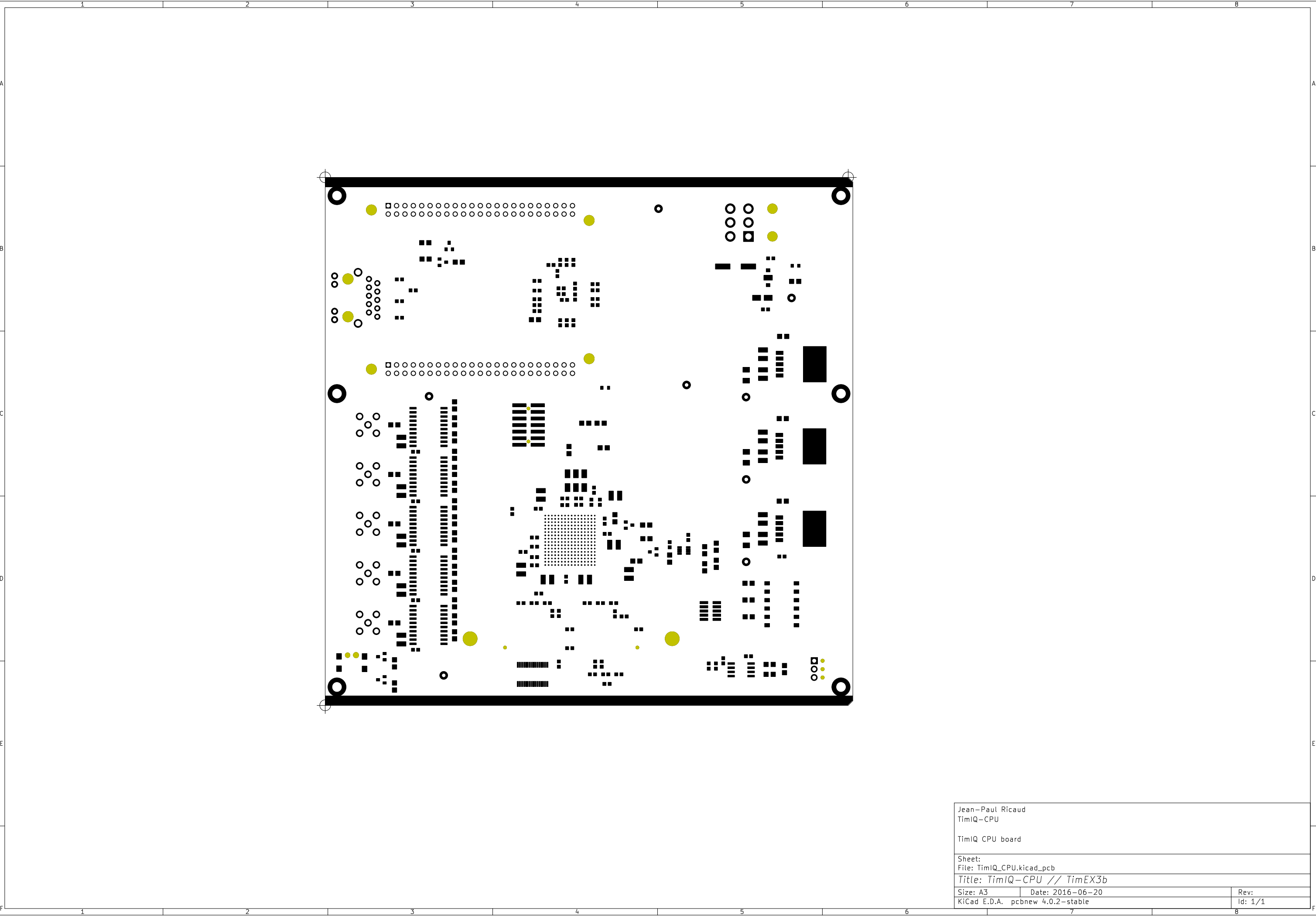
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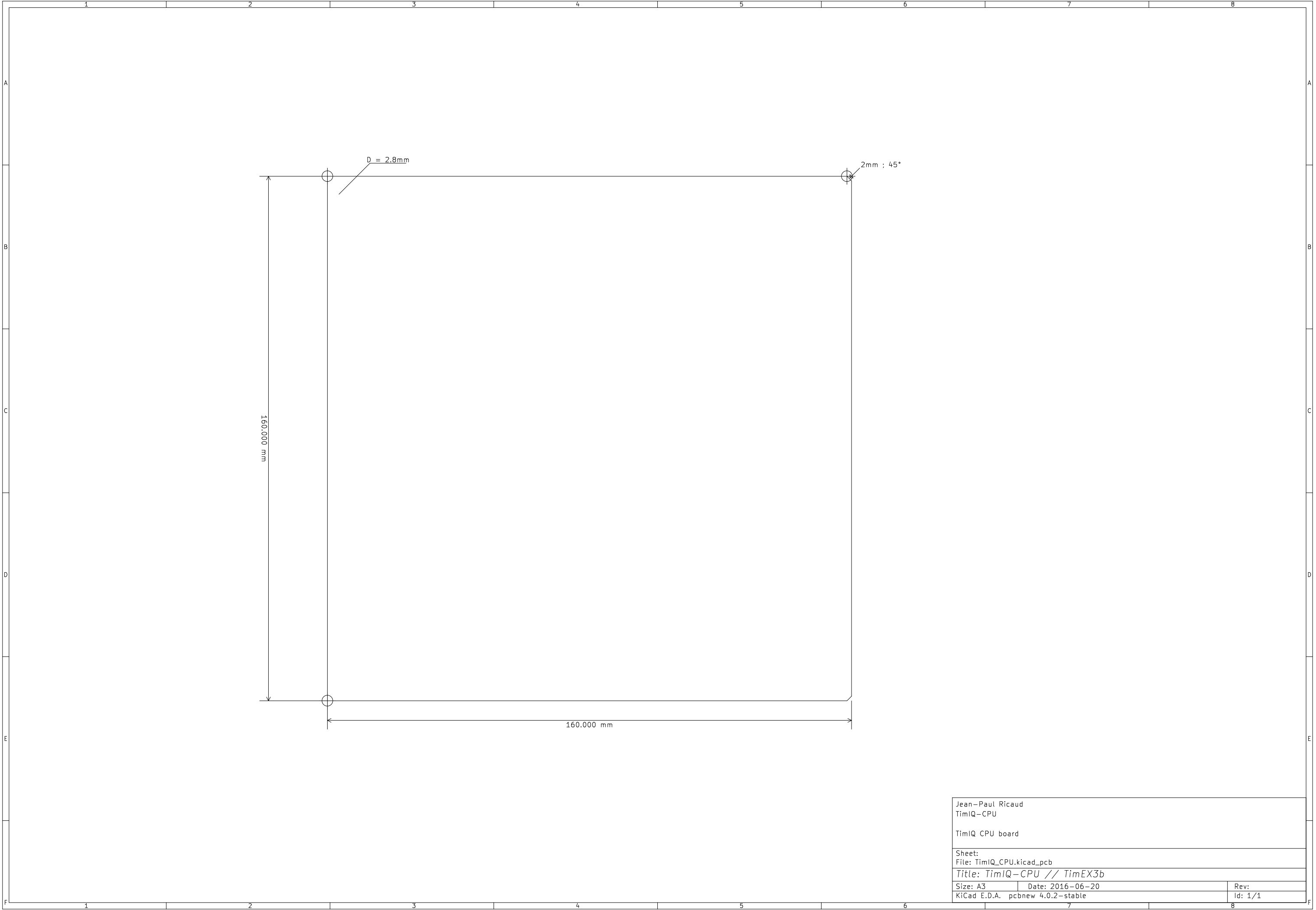
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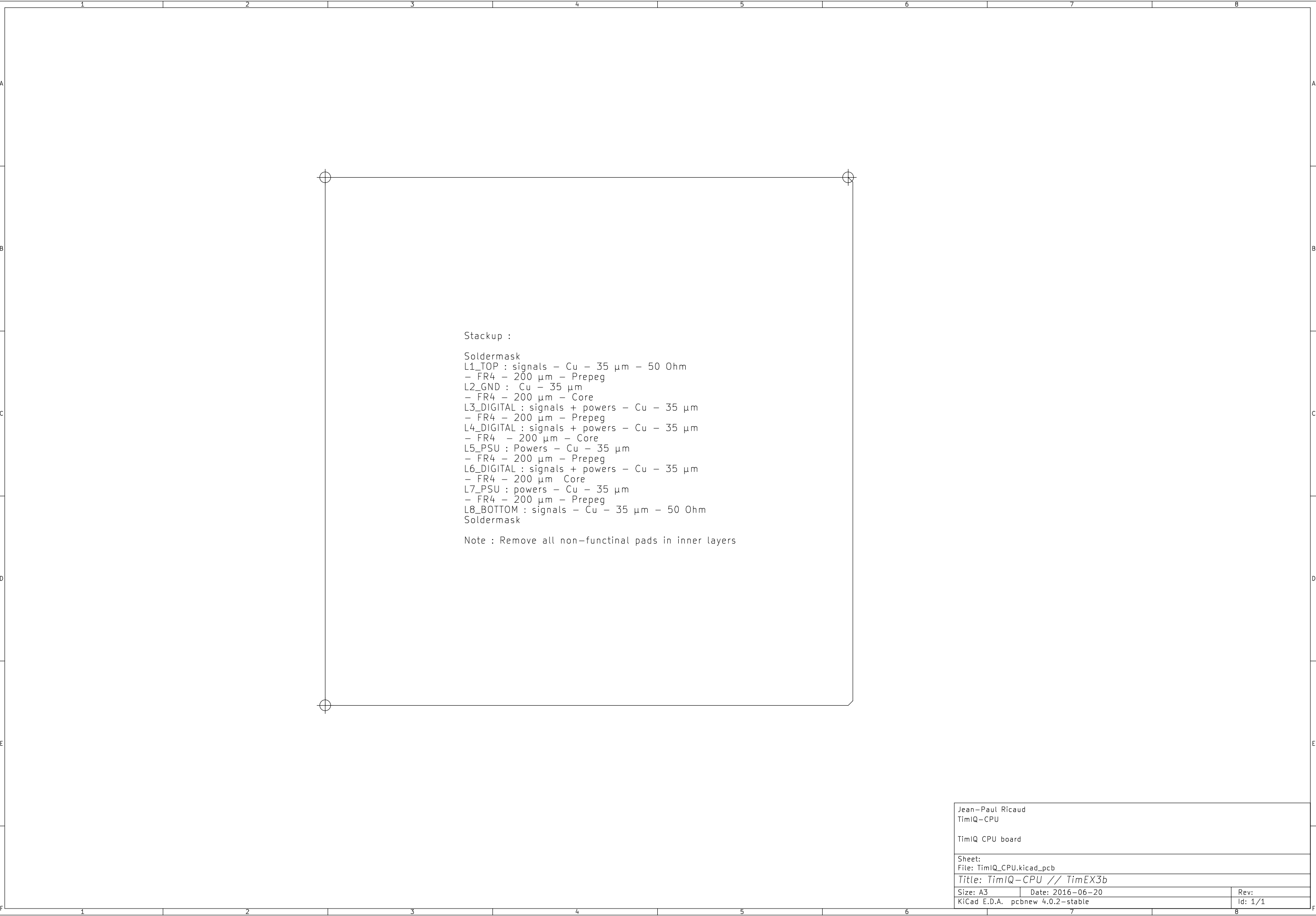
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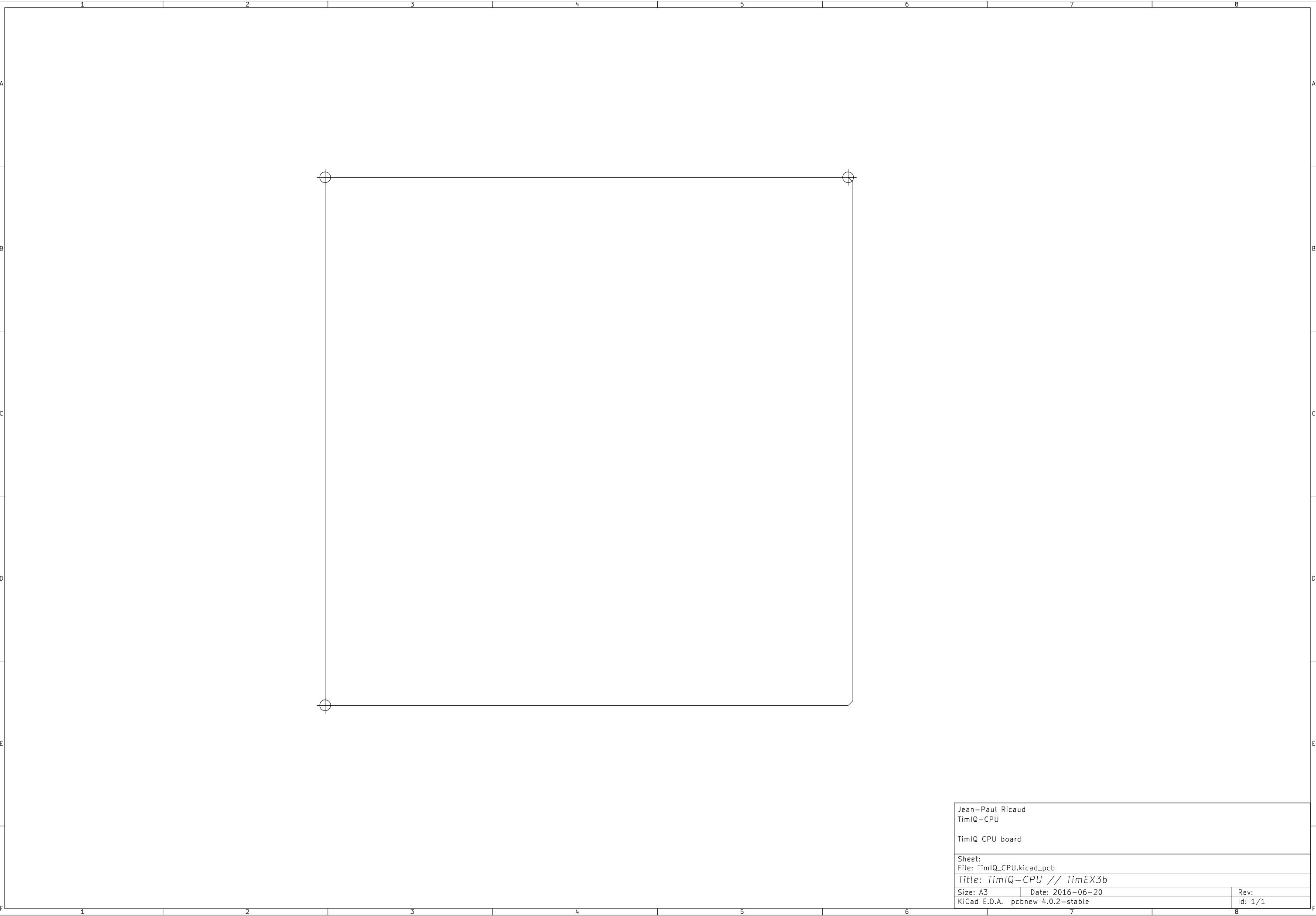
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Stackup :  
Soldermask  
L1\_TOP : signals - Cu - 35 µm - 50 Ohm  
- FR4 - 200 µm - Prepeg  
L2\_GND : Cu - 35 µm  
- FR4 - 200 µm - Core  
L3\_DIGITAL : signals + powers - Cu - 35 µm  
- FR4 - 200 µm - Prepeg  
L4\_DIGITAL : signals + powers - Cu - 35 µm  
- FR4 - 200 µm - Core  
L5\_PSU : Powers - Cu - 35 µm  
- FR4 - 200 µm - Prepeg  
L6\_DIGITAL : signals + powers - Cu - 35 µm  
- FR4 - 200 µm Core  
L7\_PSU : powers - Cu - 35 µm  
- FR4 - 200 µm - Prepeg  
L8\_BOTTOM : signals - Cu - 35 µm - 50 Ohm  
Soldermask  
  
Note : Remove all non-functinal pads in inner layers

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TimIQ CPU board

Title: TimIQ-CPU // TimEX3b

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