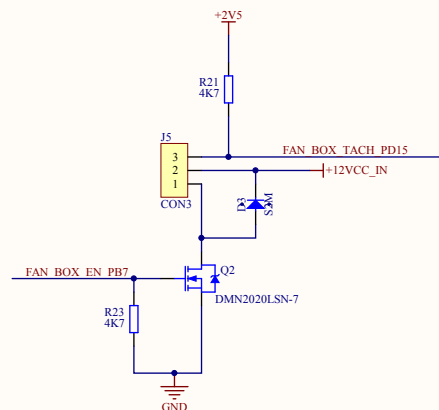
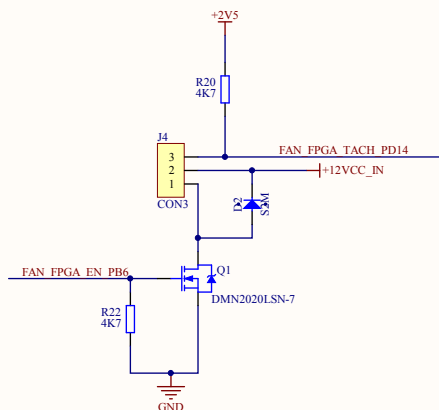
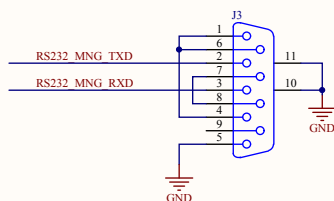


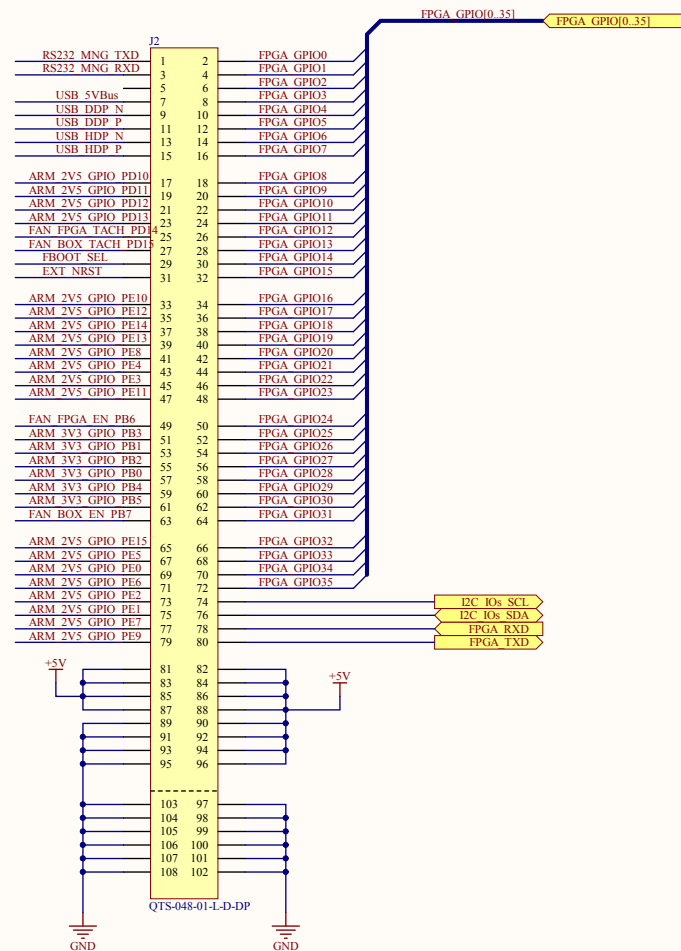
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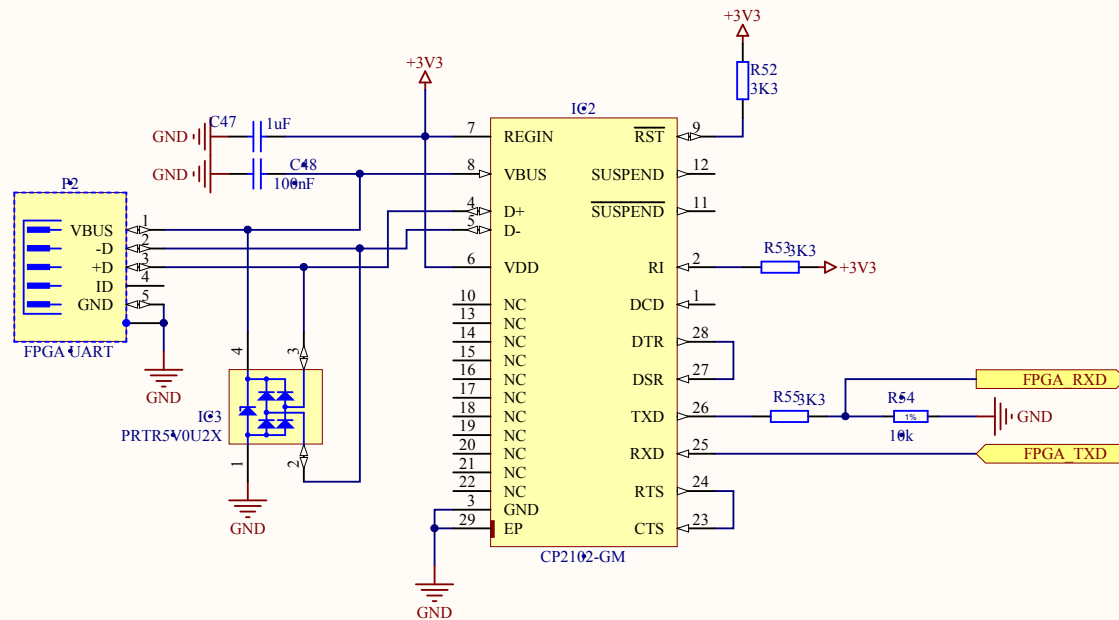
ARM 3V3 GPIO PB[0..5]

ARM 2V5 GPIO PD[10..13]

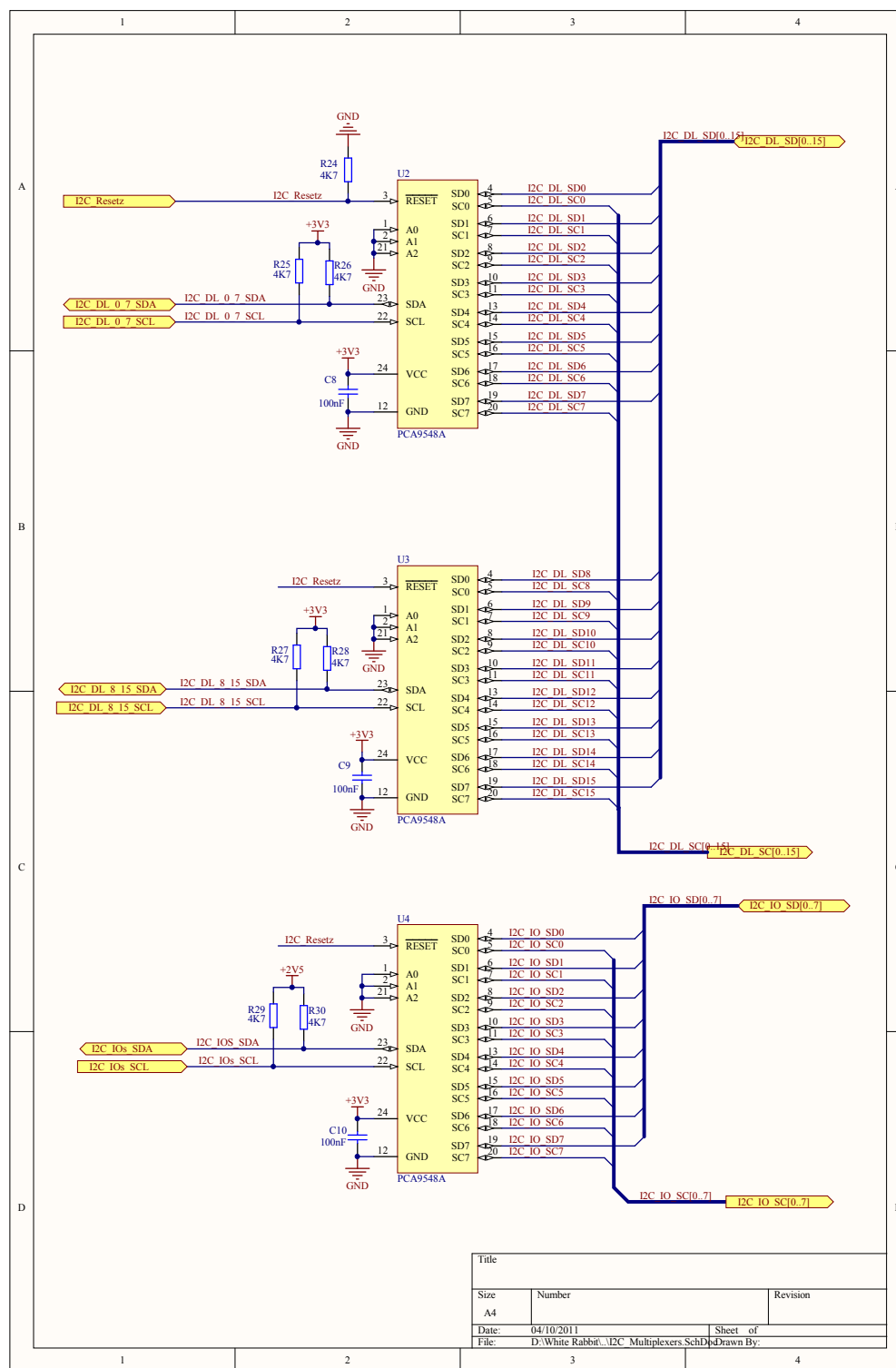
ARM 2V5 GPIO PE[0..15]



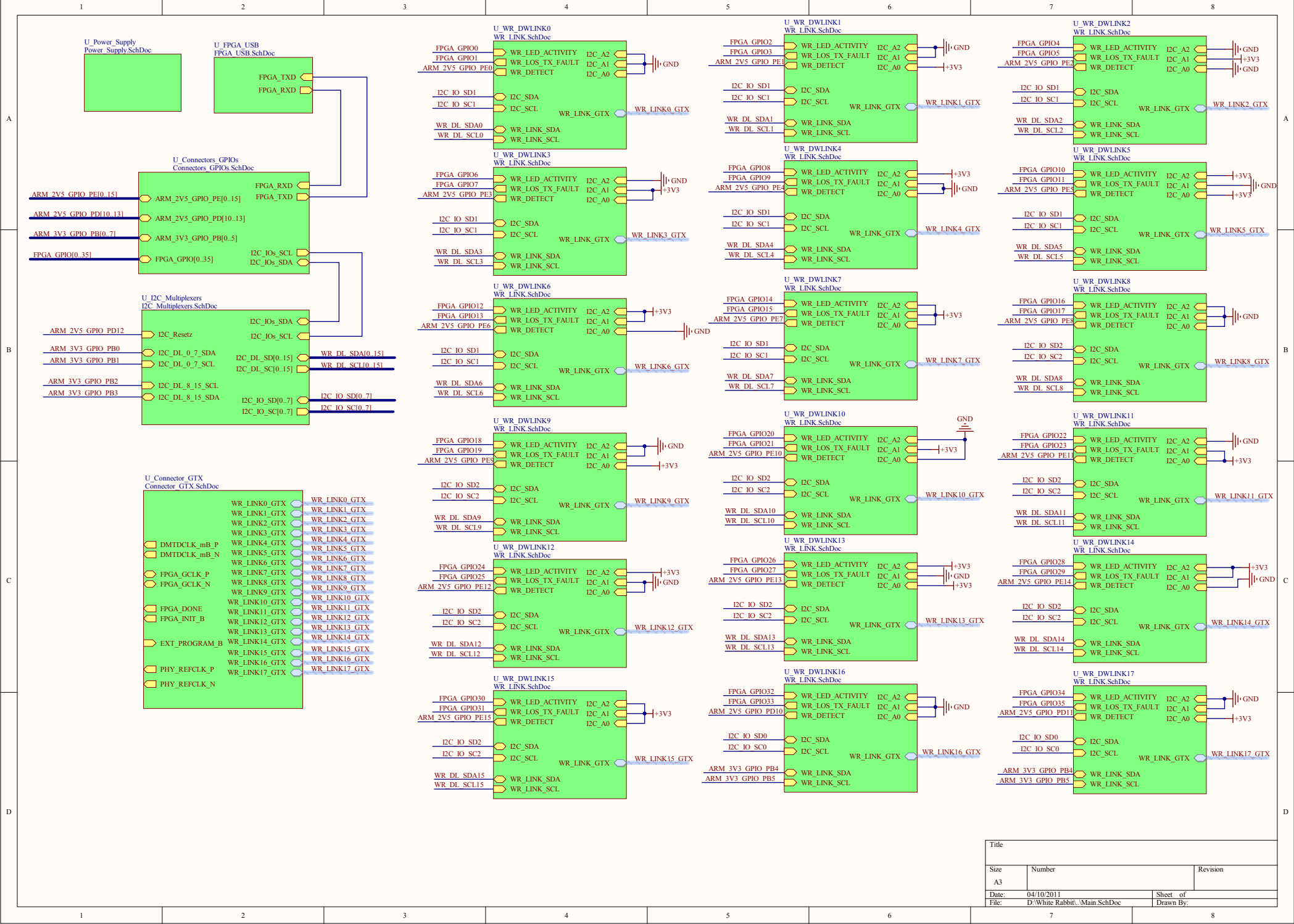
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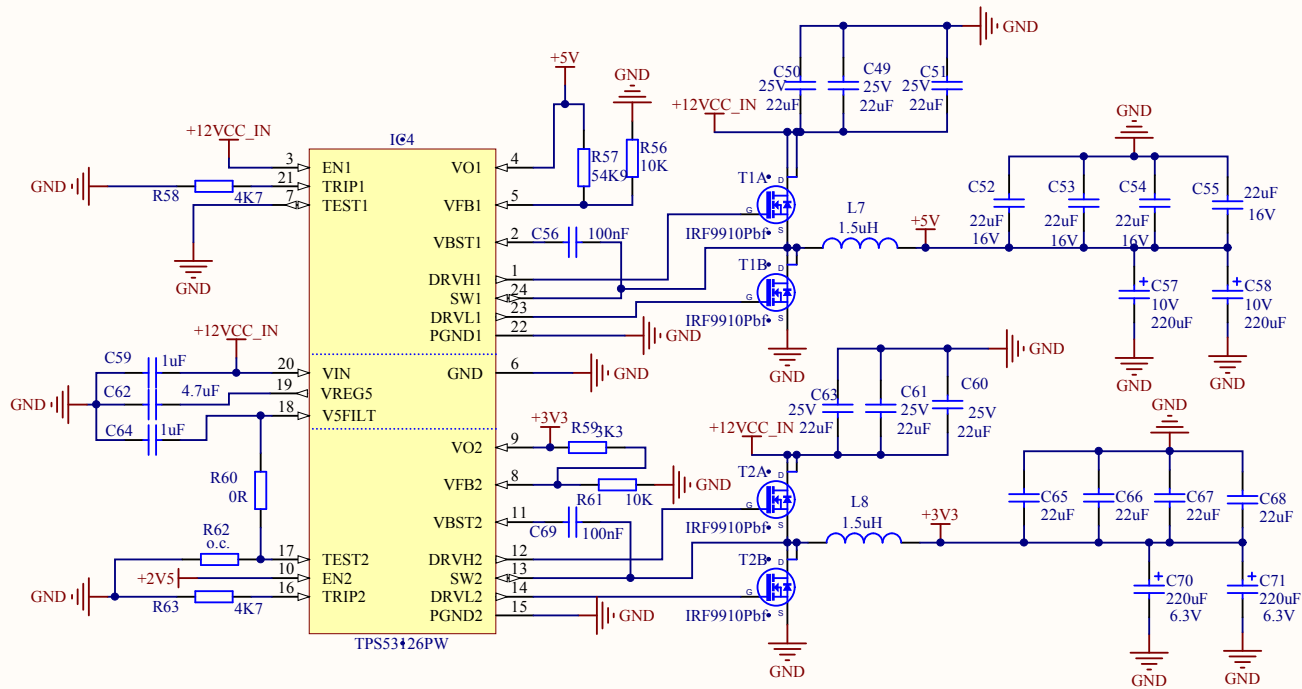
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Title		
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Title		
Size	Number	Revision
A4		
Date:	04/10/2011	Sheet of
File:	D:\White Rabbit\Power Supply.SchDoc	Drawn By:

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SFP_Control

SFP_Control

SFP_TX_DISABLE
SFP_LOS
SFP_DETECT
SFP_SCL
SFP_SDA
SFP_TX_FAULT

SFP_TX_DISABLE
SFP_LOS
SFP_DETECT
SFP_SCL
SFP_SDA
SFP_TX_FAULT

+2V5
R40
0R

SFP_LOS
SFP_DETECT
SFP_SCL
SFP_SDA
SFP_TX_DISABLE
SFP_TX_FAULT

LOS
RATE_SELECT
MOD_DEF0
MOD_DEF1
MOD_DEF2
TX_DISABLE
TX_FAULT

GND

SFP_TD_IN_N

19

TD_IN-

SFP_TD_IN_P

18

TD_IN+

SFP_RD_OUT_N

12

RD_OUT-

SFP_RD_OUT_P

13

RD_OUT+

SFP Module socket

GND

C40
10uF

C41
100nF

L5
220R@100MHz

+3V3

C44
10uF

C45
100nF

L6
220R@100MHz

C42
10uF

C43
100nF

SFP_GTX

SFP_GTX

SFP_TD_IN_N
SFP_TD_IN_P
SFP_RD_OUT_N
SFP_RD_OUT_P

SFP_TD_IN_N
SFP_TD_IN_P
SFP_RD_OUT_N
SFP_RD_OUT_P

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Date: 04/10/2011	Sheet of	
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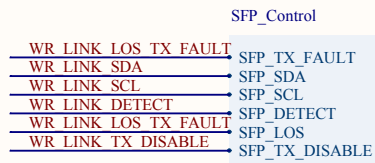
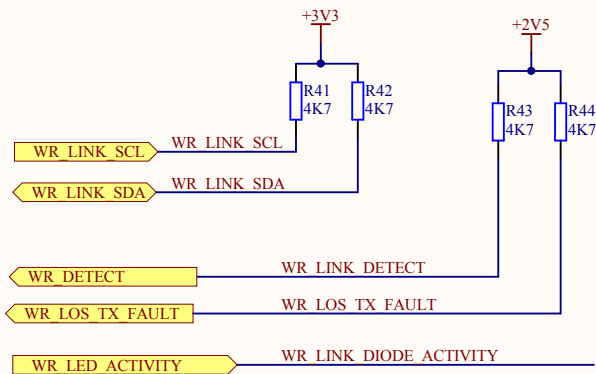
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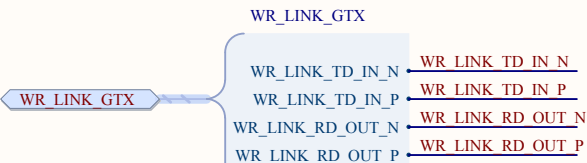
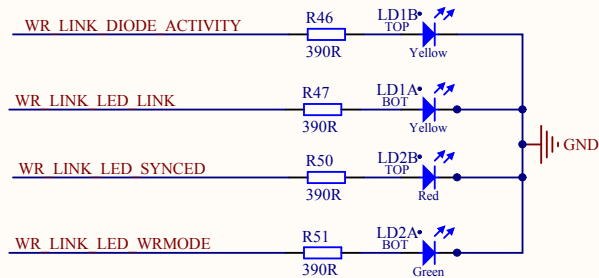
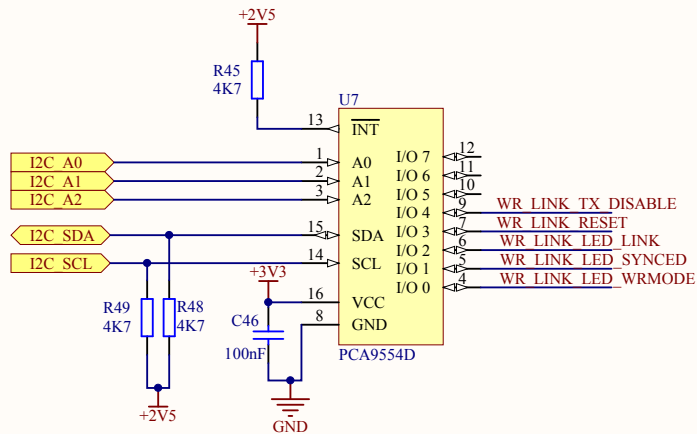
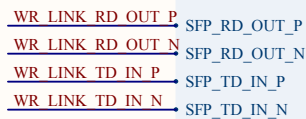
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U_SFP
SFP.SchDoc

SFP_Control

SFP_GTX

SFP_GTX



Title		
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